

CRC (Cyclic Redundancy Check) - Student Guide

What is This?

This demonstration shows how **CRC** (Cyclic Redundancy Check) works for error detection in data transmission. You'll see:

- How senders calculate CRC checksums
- · How receivers verify data integrity
- Why some errors go undetected (the surprising part!)

© Learning Objectives

After running this demonstration, you should understand:

- 1. The step-by-step CRC calculation process
- 2. How polynomial division using XOR works
- 3. Why CRC is effective but not perfect
- 4. The mathematical reason behind undetected errors



python crc_demonstration.py

No external libraries needed - just Python 3!

What the Code Does

Part 1: Sender Side

The sender wants to transmit: 1100101011101100

Steps:

- 1. **Pad with zeros**: Add 4 zeros (matching CRC degree) → 11001010111011000000
- 2. **Divide by generator**: Use XOR division with generator 11011
- 3. **Get remainder**: The remainder is the CRC checksum \rightarrow 1100
- 4. **Create codeword**: Append CRC to original message → 1100101111011001100

Key Insight: The resulting codeword is mathematically designed to be perfectly divisible by the generator polynomial!

Part 2: Receiver Side

The receiver gets: 11001010110110100100 (corrupted!)

Steps:

- 1. **Divide received data**: Divide entire received message by generator 11011
- 2. Check remainder:
 - If remainder = 0 → No error detected
 - If remainder ≠ 0 → Error detected X

The Surprise: In this example, the remainder is 0 even though errors occurred!

Part 3: The Mathematical Explanation

Why did CRC fail to detect the errors?

The Math:

- Transmitted: T (divisible by G)
- Received: T' (corrupted)
- Error pattern: E = T ⊕ T' (XOR of transmitted and received)

The receiver checks: $T' \div G = (T \oplus E) \div G$

If E is also divisible by G, then:

$$T' \div G = (T \div G) \oplus (E \div G) = 0 \oplus 0 = 0$$

Result: False negative - CRC says "no error" when errors actually occurred!

With Example 2 Understanding XOR Division

Traditional division uses subtraction, but binary polynomial division uses **XOR**:

Traditional: 5 - 3 = 2

XOR: 101 ⊕ 011 = 110

XOR Rules:

- $0 \oplus 0 = 0$
- 1 ⊕ 1 = 0
- 0 ⊕ 1 = 1
- 1 ⊕ 0 = 1

Why XOR? Because we're working with polynomials over GF(2) (Galois Field with 2 elements), where addition = subtraction = XOR!

Key Concepts Explained

Generator Polynomial

$$G(x) = x^4 + x^3 + x + 1$$

Translation to binary:

- Write coefficients: $1 \cdot x^4 + 1 \cdot x^3 + 0 \cdot x^2 + 1 \cdot x^1 + 1 \cdot x^0$
- Take coefficients: 11011

The degree (highest power) is 4, so CRC will be 4 bits.

Why Pad with Zeros?

We need room for the remainder!

Original message: 16 bits

· Add 4 zeros: 20 bits

· After division: last 4 bits become the CRC

Final codeword: 16 bits (message) + 4 bits (CRC) = 20 bits

Error Pattern

The error pattern shows which bits flipped:

Transmitted: 11001010111011001100

Received: 11001010110110100100

Error (XOR): 0000000001101101000

Bits that were flipped

III CRC Effectiveness

What CRC Detects Well:

- All single-bit errors (100% detection)
- · All double-bit errors (for most generators)
- All odd-number bit errors (if generator has factor x+1)
- Most burst errors (up to degree length)

X What CRC Can Miss:

- Error patterns that are multiples of G(x)
- Probability $\approx 1/2^n$ where n = CRC degree
- For 4-bit CRC: 1/16 = 6.25% of random error patterns

Real-World CRC Standards:

- CRC-8: 8-bit, used in 1-Wire protocol
- CRC-16: 16-bit, used in USB, Modbus
- CRC-32: 32-bit, used in Ethernet, ZIP files
- CRC-64: 64-bit, used in storage systems



Common Student Questions

Q: Why not just use parity bits?

A: Parity only detects odd numbers of errors. CRC detects much more, including all single and double-bit errors.

Q: Can CRC correct errors?

A: No! CRC only **detects** errors. To correct errors, you need codes like Hamming codes or Reed-Solomon.

Q: Why use polynomial division?

A: Polynomials have nice mathematical properties. Certain generators guarantee detection of specific error types (e.g., all burst errors up to length n).

Q: Is this the same CRC in ZIP files?

A: Same concept! ZIP uses CRC-32 (32-bit) with generator polynomial 0x04C11DB7. The principle is identical.

Q: How do I choose a good generator?

A: Use standardized generators! They're carefully chosen to maximize error detection. Common properties:

- Should have at least 2 terms (not just x⁴)
- Should have factor (x+1) for odd-error detection
- Should be irreducible (can't be factored)



Experiment Ideas

Try modifying the code to explore:

1. Different generators:

- 11001 $(X^4 + X^3 + 1)$
- 10011 $(x^4 + x + 1)$
- · Do they detect the same errors?

2. Different error patterns:

- · Single bit flip
- · Two adjacent bits
- · Bits exactly 4 apart
- 3. Longer messages: Does message length affect detection?
- 4. **Burst errors**: Try flipping consecutive bits. What's the longest burst that goes undetected?

Further Reading

- Theory: Look up "polynomial codes" and "cyclic codes"
- Standards: Search "CRC-32" or "IEEE 802.3 CRC"
- Advanced: "Galois Field arithmetic" explains the XOR operations
- Applications: Error detection in networks, storage, and communication protocols

Real-World Context

Where is CRC used?

- # Ethernet: Every network packet has a CRC-32
- Hard drives: Sector error detection
- **Bluetooth**: Link-layer error checking
- File compression: ZIP, PNG, GZIP all use CRC
- Space communication: Combined with error correction codes

Why not 100% perfect?

CRC balances:

- Fast computation (XOR is cheap!)
- Simple hardware implementation
- Very good error detection

X Small chance of undetected errors (acceptable trade-off!)

For critical applications (space, medical), CRC is combined with error-correcting codes for redundancy.

© Check Your Understanding

After running the demonstration, try answering:

- 1. What is the relationship between CRC degree and checksum length?
- 2. Why must the transmitted codeword be divisible by G?
- 3. What makes an error pattern "undetectable"?
- 4. If we used a 32-bit CRC instead of 4-bit, how would the error detection probability change?
- 5. Can you think of a scenario where an 8-bit error would go undetected?

Summary

CRC is a brilliant error detection technique that:

- · Uses polynomial mathematics to create checksums
- Detects the vast majority of transmission errors
- Has a small, predictable probability of missing errors
- · Is fast enough for real-time applications
- Is used everywhere in modern computing!

The key insight: **CRC transforms error detection into a polynomial division problem**, which computers can solve very efficiently using simple XOR operations.

Questions? Review the code output carefully - every step shows you exactly what's happening mathematically!