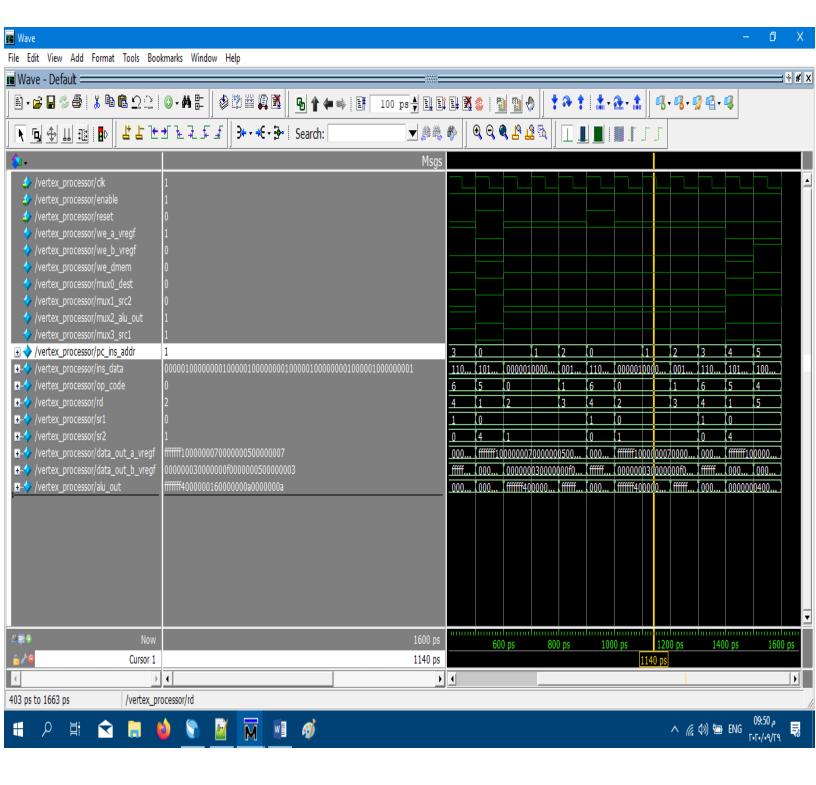
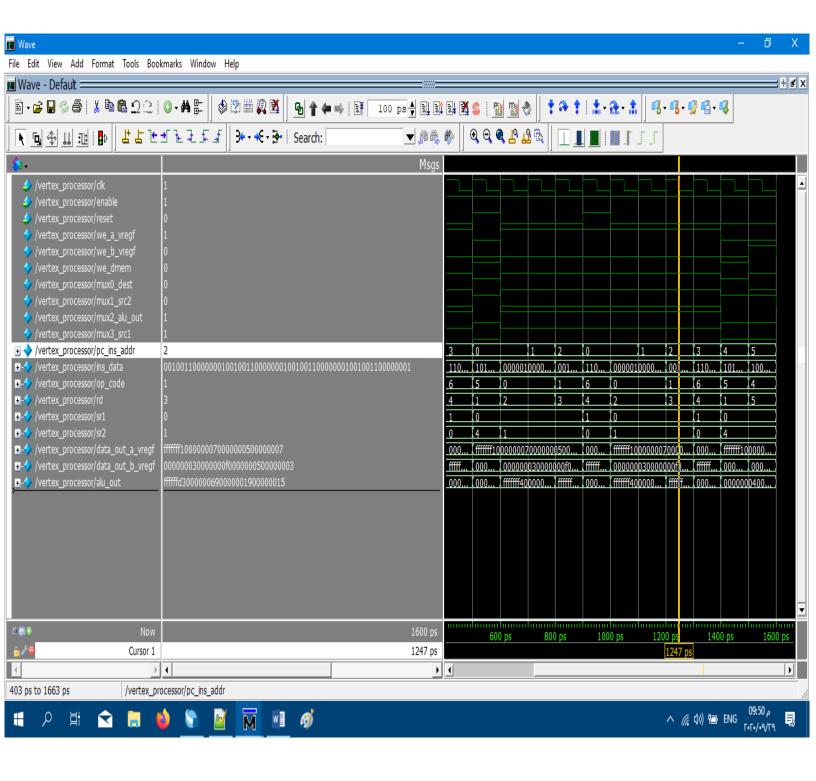
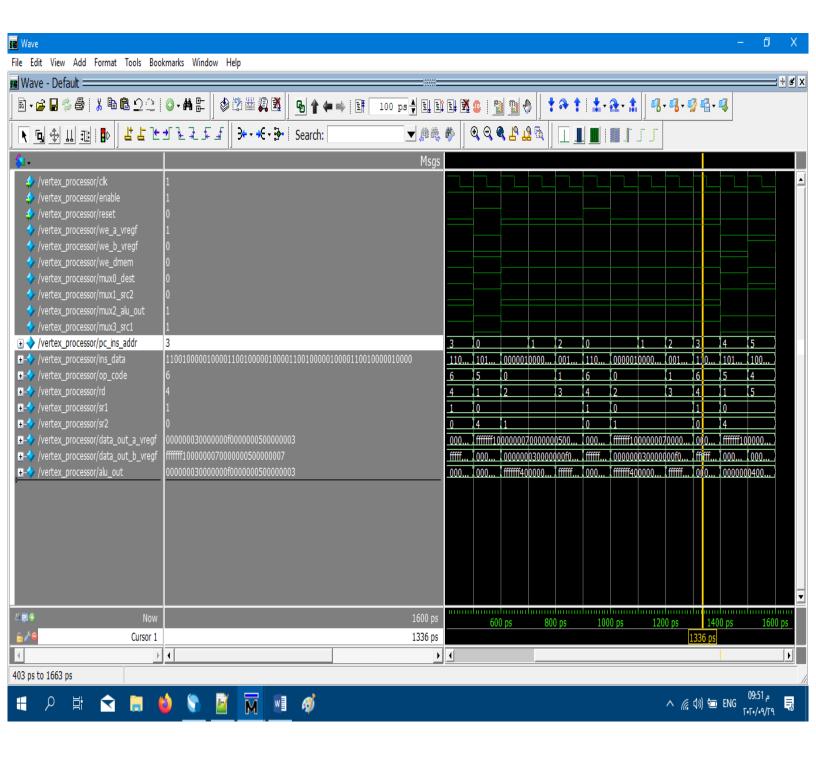
ADD R2,R0,R1



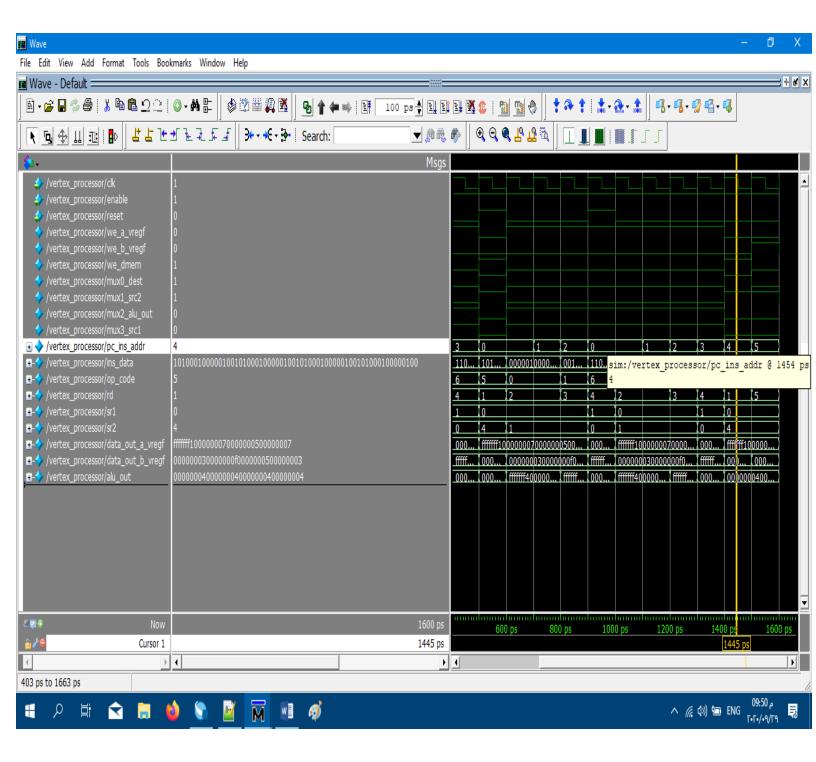
MUL R3,R0,R1



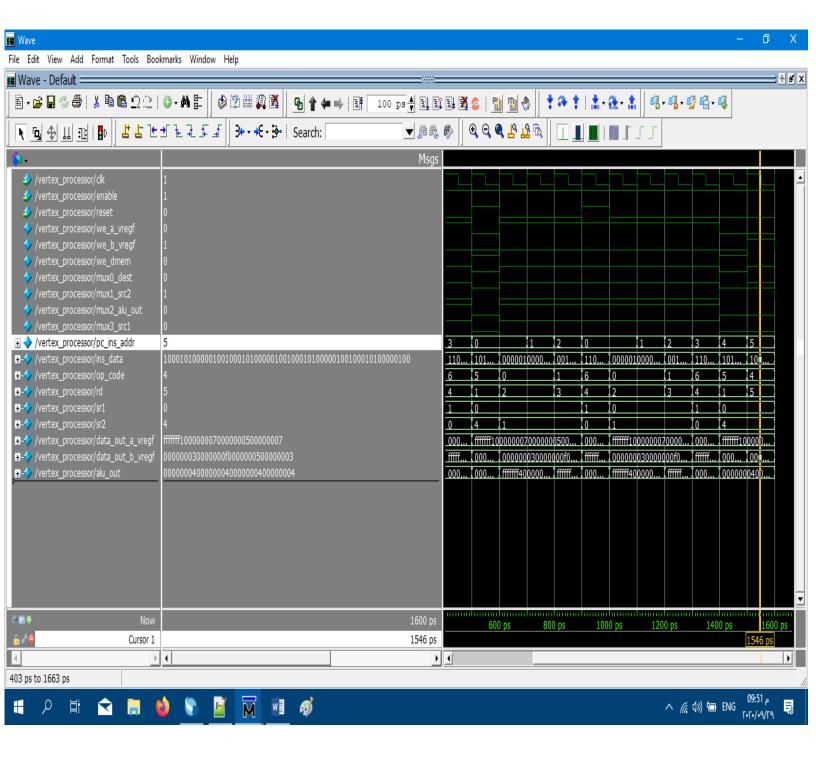
MOV R4,R1



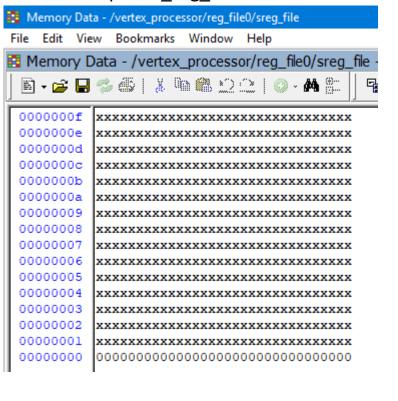
STR R1=[R0,#4]



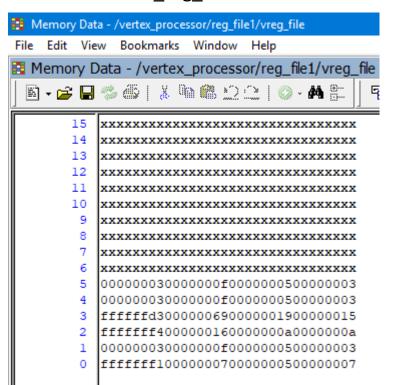
LDR R5=[R0,#4]



Special reg file



Vector reg file



data_memory

Memory Data - /vertex_processor/d_mem0/d_mem

File Edit View Bookmarks Window Help

