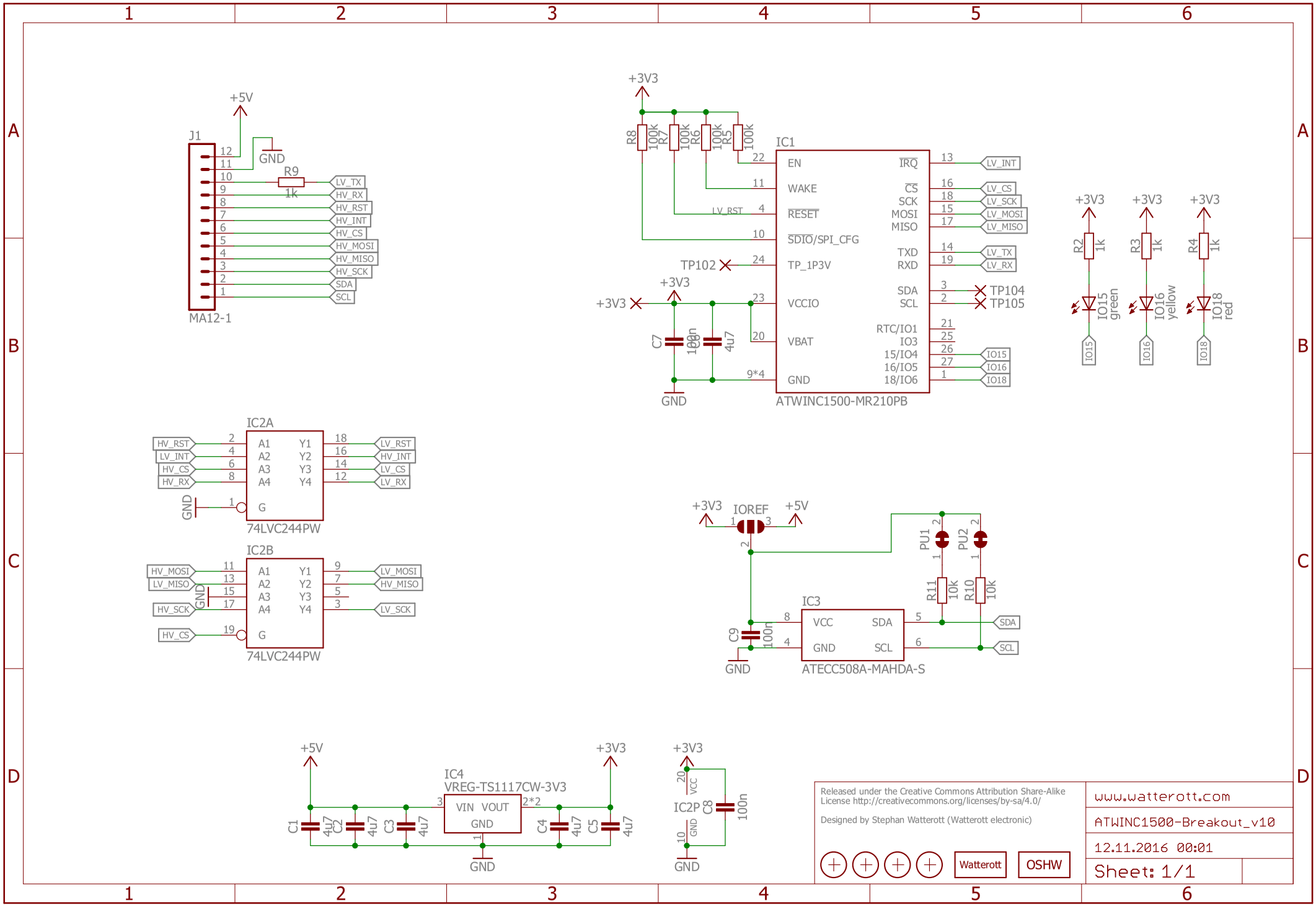
J1
MA12-1
+5V
GND
R9
1k
LV_TX
HV_RX
HV_RST
HV_INT
HV_CS
HV_MOSI
HV_MISO
HV_SCK
SDA
SCL
+3V3
R8 100k
R7 100k
R6 100k
R5 100k
IC1
EN
WAKE
RESET
SDIO/SPI_CFG
TP_1P3V
VCCIO
VBAT
GND
IRQ
CS
SCK
MOSI
MISO
TXD
RXD
SDA
SCL
RTC/IO1
IO3
15/IO4
16/IO5
18/IO6
LV_RST
TP102
TP104
TP105
C7 100n
4u7
ATWINC1500-MR210PB
R2 1k
R3 1k
R4 1k
IO15 green
IO16 yellow
IO18 red
IO15
IO16
IO18
IC2A
HV_RST
LV_INT
HV_CS
HV_RX
A1
A2
A3
A4
G
Y1
Y2
Y3
Y4
LV_RST
HV_INT
LV_CS
LV_RX
74LVC244PW
IC2B
HV_MOSI
LV_MISO
HV_SCK
HV_CS
LV_MOSI
HV_MISO
LV_SCK
74LVC244PW
IOREF
PU1
PU2
R11 10k
R10 10k
IC3
VCC
GND
SDA
SCL
C9 100n
ATECC508A-MAHDA-S
IC4
VREG-TS1117CW-3V3
VIN
VOUT
C1 4u7
C2 4u7
C3 4u7
C4 4u7
C5 4u7
IC2P
C8 100n
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Designed by Stephan Watterott (Watterott electronic)
Watterott
OSHW
www.watterott.com
ATWINC1500-Breakout_v10
12.11.2016 00:01
Sheet: 1/1

ATWINC1500B
Watterott
electronic
Open
Hardware
TP104 TP105
3V3
5V
IOREF
I2C
Pullups
ATECC508A
TP102
WiFi
Netw.
Error
SCL
SDA
SCK
MISO
MOSI
CS
Int
RST
RX
TX
GND
+5V