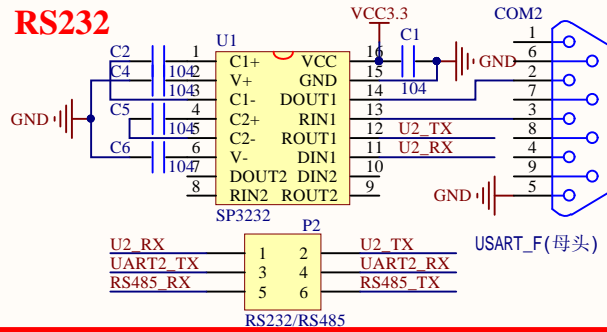
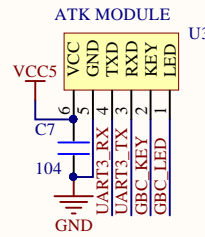


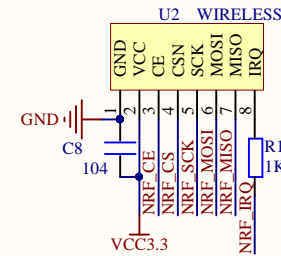
## RS232



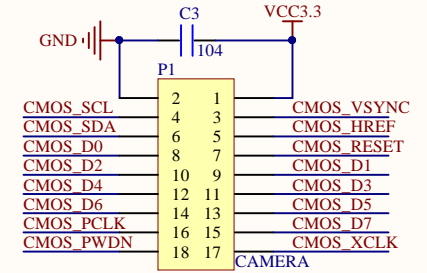
## ATK MODULE



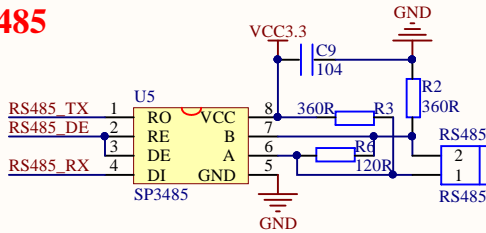
## WIRELESS



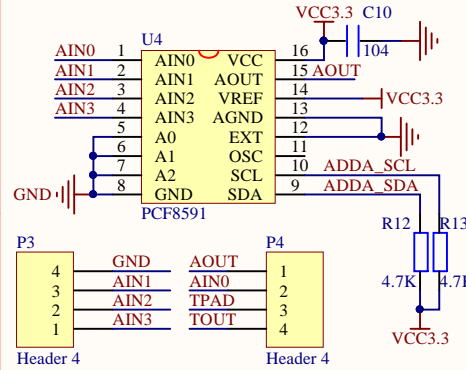
## OLED&CAMERA



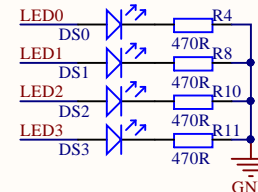
## RS485



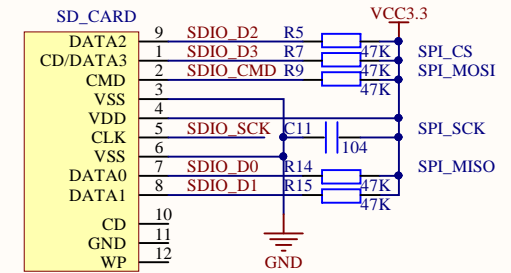
## AD/DA



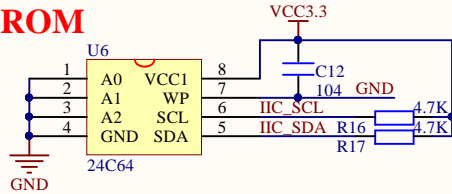
## LED



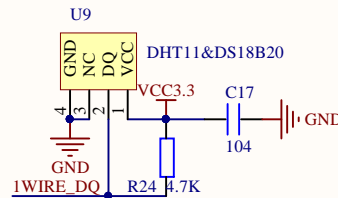
## SD CARD



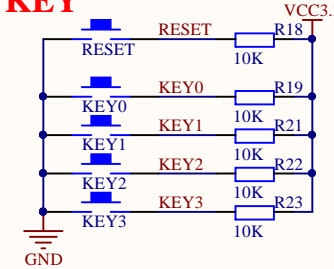
## EEPROM



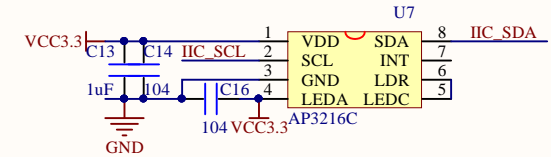
## TEMP&HUMI SENSOR



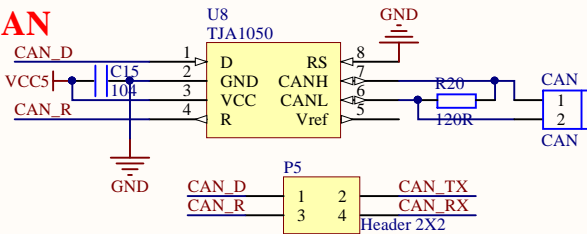
## KEY



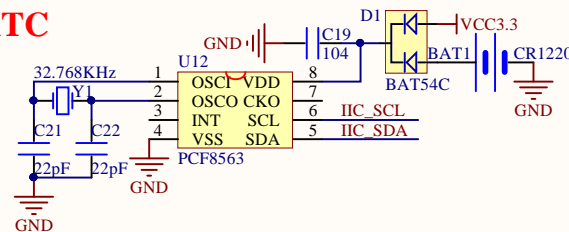
## ALS&PS SENSOR



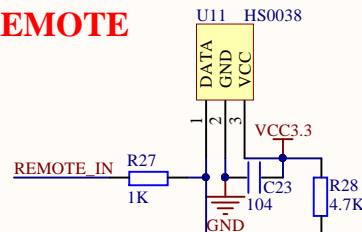
## CAN



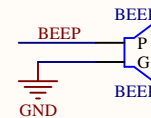
## RTC



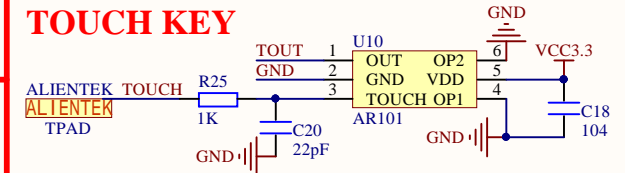
## REMOTE



## BEEP

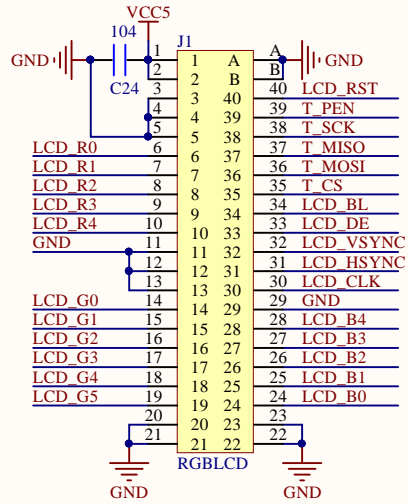


## TOUCH KEY

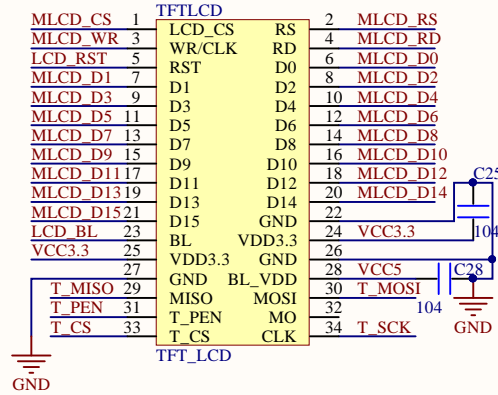


Title:		
Author:	Size:	
Date:	File:	Cannot open file
Revision:	Version:	E:\protel...

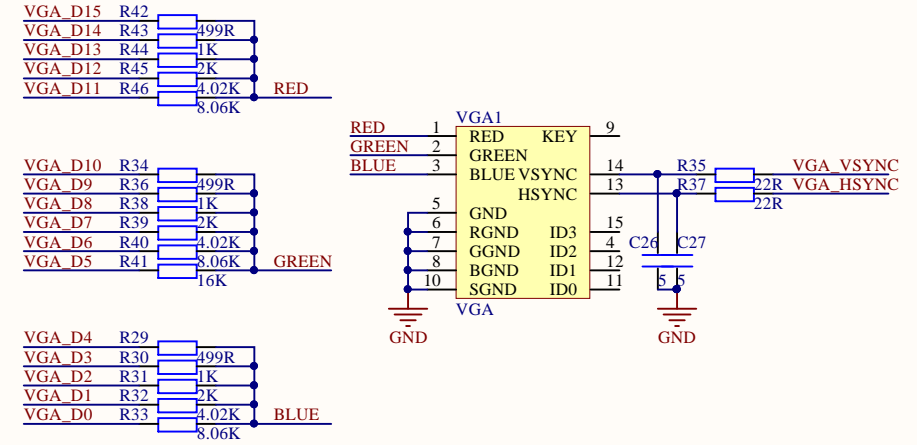
## RGB LCD



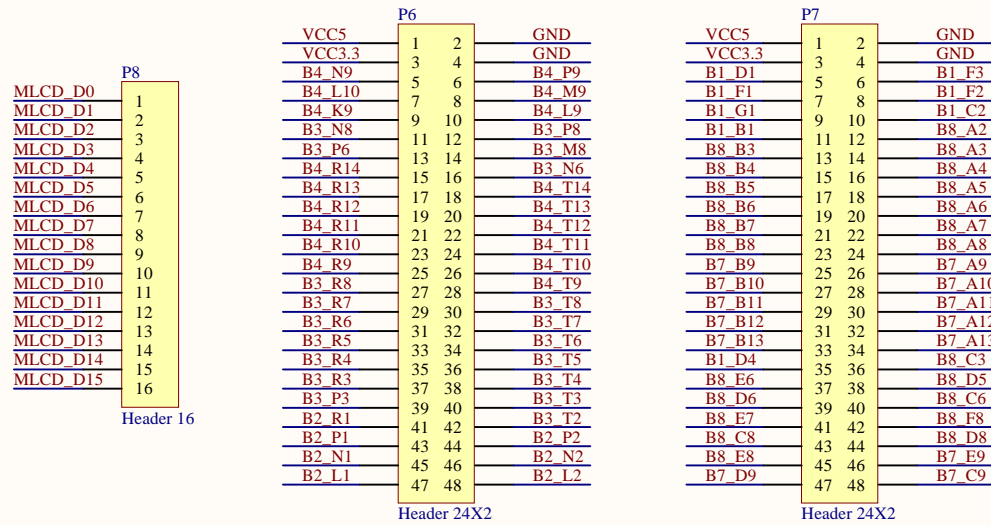
## MCU LCD



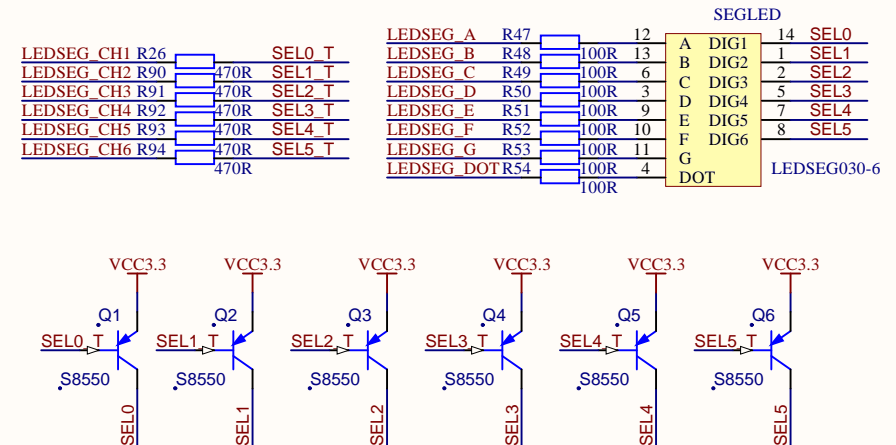
## VGA



## IO

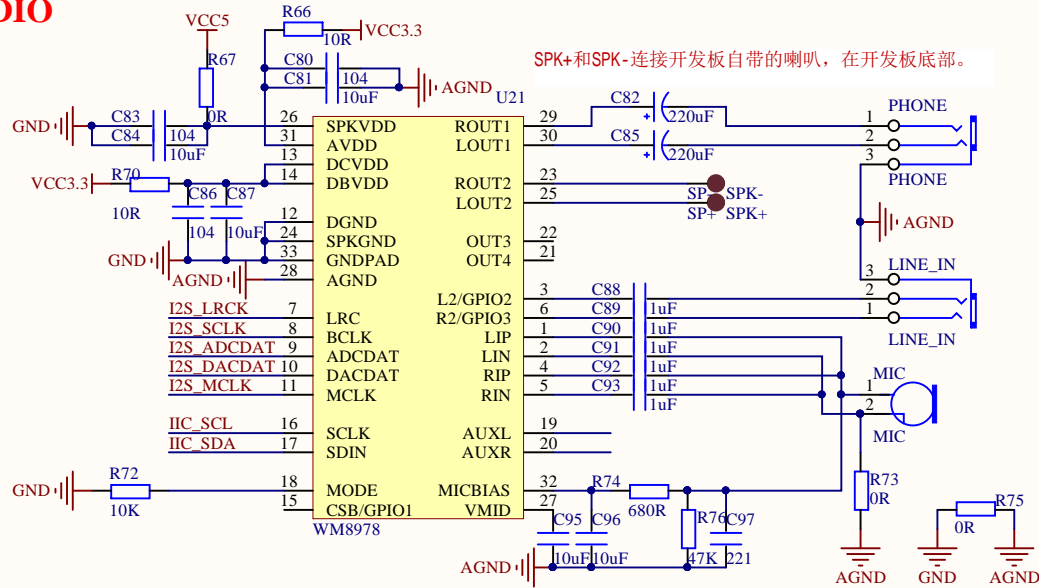


## LED DIGITAL TUBE

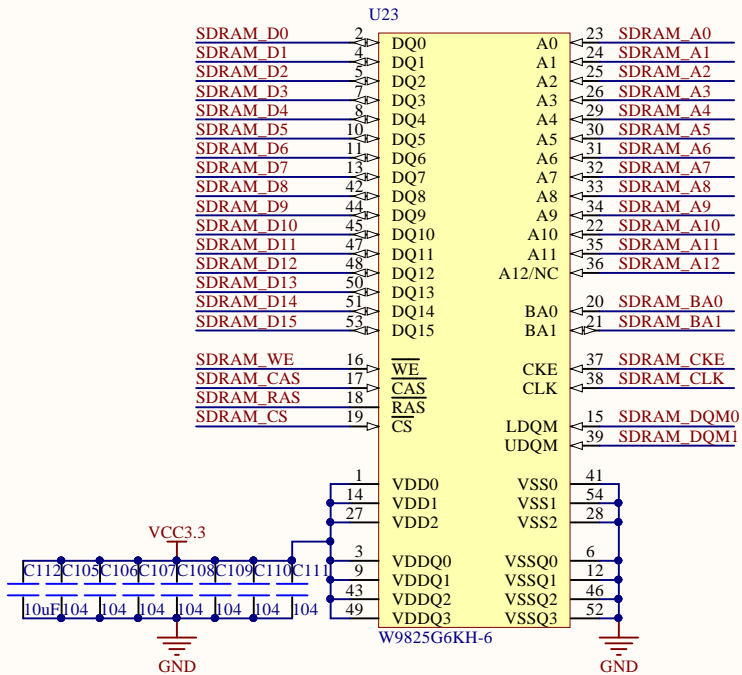


Title:		Cannot open file E:\protel...
Author:	Size:	
Date: 2018/7/4	File: DISPLAY&PINOUT.SchDoc	
Revision:	Version:	

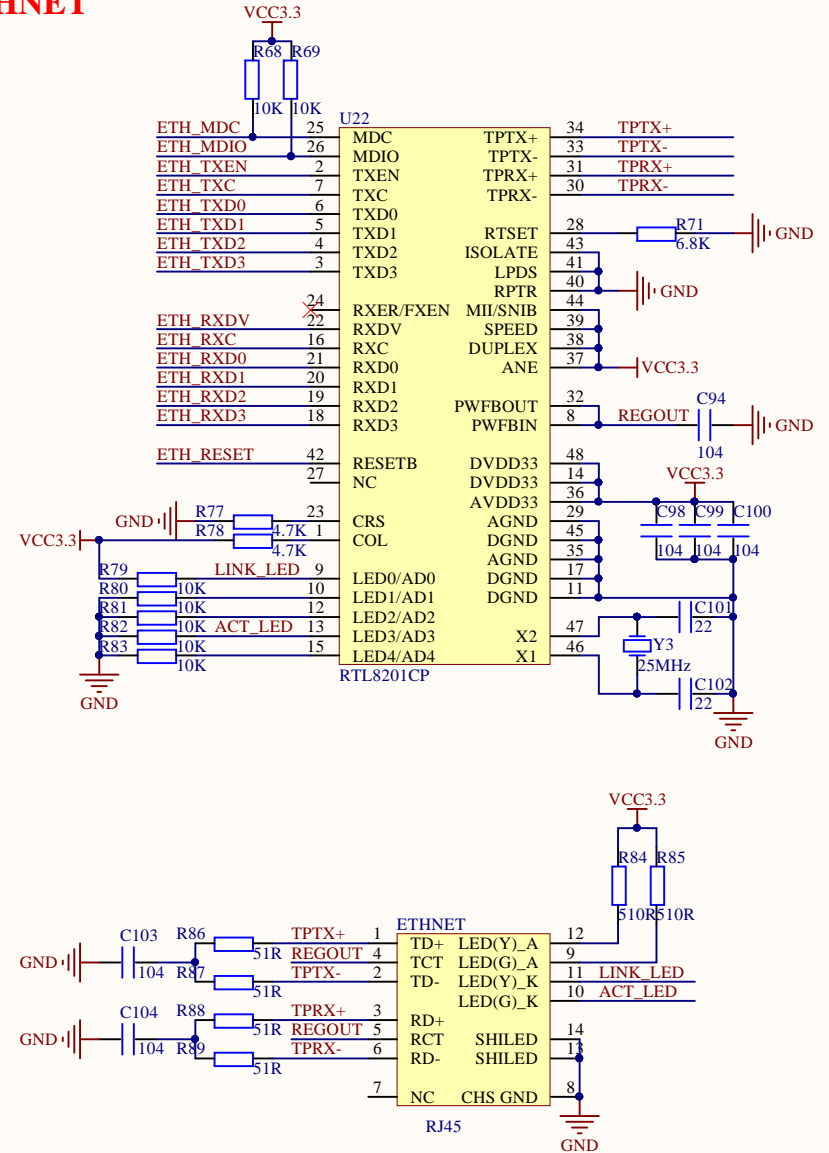
## AUDIO



## SDRAM



## ETHNET



Title:		
Author:	Size:	
Date:	File:	Cannot open file
2018/7/4	ETH&AUDIO&SDRAM.SchDoc	E:\protel...
Revision:	Version:	

## FPGA

U14A

BANK 1

IO	D4	B1	D4	UART3_TX
IO	D5	B1	E5	ETH_MDC
IO	D6	B1	F5	ETH_RXD0
IO	D1	B1	B1	SDIO D2
IO	D2	B1	C2	SDIO D3
IO	D1	B1	C1	EPSC ASDO
IO	D3	B1	F3	NRF_MISO
IO	D2	B1	D2	EPSC CS
IO	D1	B1	D1	SDIO CMD
IO	D5	B1	G5	ETH_RXD1
IO	D1	B1	E2	NRF_SCK
IO	D2	B1	F1	NRF_MOSI
IO	D2	B1	G2	NRF_CS
IO	D1	B1	G1	NRF_IRQ
IO	D2	B1	H2	EPSC_DATA0
IO				IO, (DATA0)

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U14B

BANK 2

IO	D5	B2	J2	SDIO SCK
IO	D1	B2	J1	NRF_CE
IO	D6	B2	J6	ETH_TXC
IO	D6	B2	K6	ETH_TXD0
IO	D6	B2	L6	ETH_RESET
IO	D2	B2	K2	SDIO D1
IO	D1	B2	K1	SDIO D0
IO	D2	L2		T_PEN
IO	D1	B2	L1	LCD_RST
IO	D3	B2	L3	ETH_TXD3
IO	D2	B2	N2	T_MISO
IO	D1	B2	N1	T_SCK
IO	D5	B2	K5	ETH_TXD1
IO	D4	B2	L4	ETH_TXEN
IO	D1	B2	R1	LCD_BL
IO	D2	B2	P2	T_CS
IO	D1	B2	P1	T_MOSI
IO				IO, (DM1L/BWS#IL)/

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U14C

BANK 3

IO	D5	B3	N3	CAN_TX
IO	D3	B3	P3	LCD_VSYN
IO	D3	B3	R3	LCD_CLK
IO	D3	B3	T3	LCD_HSYN
IO	D2	B3	T2	LCD_DE
IO	D4	B3	B4	LCD_B3
IO	D4	B3	T4	LCD_B4
IO	D5	B3	N5	UART1_RX
IO	D6	B3	N6	CMOS_D5
IO	D6	B3	M6	CAN_RX
IO	D6	B3	P6	CMOS_D4
IO	D7	B3	M7	UART1_TX
IO	D8	B3	K8	ETH_RXD3
IO	D5	B3	R5	LCD_B1
IO	D5	B3	T5	LCD_B2
IO	D6	B3	R6	LCD_G5
IO	D6	B3	T6	LCD_B0
IO	D7	B3	L7	ETH_TXD2
IO	D7	B3	R7	LCD_G3
IO	D7	B3	T7	LCD_G4
IO	D8	B3	L8	ETH_RXC
IO	D8	B3	M8	CMOS_D3
IO	D8	B3	N8	CMOS_D2
IO	D8	B3	P8	CMOS_D1
IO	D8	B3	R8	LCD_G1
IO	D8	B3	T8	LCD_G2
IO				MLCD_D6
IO				MLCD_D7

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U14D

BANK 4

IO	D9	B4	R9	LCD_R4
IO	D9	B4	T9	LCD_G0
IO	D9	B4	K9	CMOS_D0
IO	D9	B4	L9	CMOS_RESET
IO	D9	B4	M9	CMOS_HREF
IO	D9	B4	N9	CMOS_SCL
IO	D10	B4	R10	LCD_R2
IO	D10	B4	T10	LCD_R3
IO	D11	B4	R11	LCD_R0
IO	D11	B4	T11	LCD_R1
IO	D12	B4	T12	CMOS_PWDN
IO	D10	B4	K10	SDRAM_CS
IO	D10	B4	L10	CMOS_SDA
IO	D9	B4	P10	CMOS_VSYN
IO	D11	B4	P11	LEDSEG_G
IO	D13	B4	R13	CMOS_PCLK
IO	D13	B4	T13	CMOS_XCLK
IO	D10	B4	M10	LEDSEG_E
IO	D14	B4	N11	LEDSEG_F
IO	D14	B4	T14	CMOS_D7
IO	D15	B4	T15	LEDSEG_CH1
IO	D14	B4	R14	CMOS_D6
IO	D14	B4	P14	SDRAM_D0
IO	D11	B4	L11	SDRAM_D6
IO	D11	B4	M11	LEDSEG_A
IO	D10	B4	B20p	IO, (DQ50B/CQ1B,DPCLK3)/(DQ50B/CQ1B,DPCLK3)
IO				IO
IO				IO, DIFFIO_B21p
IO				IO, DIFFIO_B21n
IO				IO, DIFFIO_B22p
IO				IO, DIFFIO_B22n

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U14E

BANK 5

IO	N13	B5	N13	LEDSEG_D
IO	M12	B5	M12	SDRAM_D1
IO	I12	B5	L12	SDRAM_D3
IO	I12	B5	K12	SDRAM_D7
IO	I14	B5	N14	SDRAM_D2
IO	I15	B5	P15	LEDSEG_CH3
IO	I16	B5	P16	LEDSEG_CH4
IO	I16	B5	R16	LEDSEG_CH2
IO	I11	B5	K11	SDRAM_RAS
IO	I10	B5	N16	LEDSEG_CH6
IO	I15	B5	N15	LEDSEG_CH5
IO	I14	B5	L14	SDRAM_D5
IO	I13	B5	L13	SDRAM_D4
IO	I16	B5	L16	SDRAM_D14
IO	I15	B5	L15	SDRAM_D15
IO	I11	B5	J11	SDRAM_D9
IO	I16	B5	K16	SDRAM_D12
IO	I15	B5	K15	SDRAM_D13
IO	I16	B5	J16	SDRAM_D10
IO	I15	B5	J15	SDRAM_D11
IO	I14	B5	J14	SDRAM_DQ0M0
IO	I12	B5	J12	SDRAM_CAS
IO	I13	B5	J13	SDRAM_WE
IO				IO, RUP3, (DM1R/BWS#1R)/
IO				IO, RDN3, (DQ1R)/
IO				IO, DIFFIO_R11n, (DQ53R/CQ3R#)/(DQ53R/CQ3R#)
IO				IO, DIFFIO_R11p, (DQ1R)/
IO				IO, DIFFIO_R10n, (DQ1R)/
IO				IO, DIFFIO_R10p, (DQ1R)/
IO				IO, VREFB5N0
IO				IO, (DQ1R)/
IO				IO, DIFFIO_R9n, (DQ1R)/
IO				IO, DIFFIO_R9p, (DQ1R)/
IO				IO, DIFFIO_R8n, (DQ1R)/
IO				IO, (DQ51R/CQ1R#DPCLK4)/(DQ51R/CQ1R#DPCLK4)
IO				IO, DIFFIO_R7n, (DEV_OE)
IO				IO, DIFFIO_R7p, (DEV_CLRN)
IO				IO, DIFFIO_R6n, (DQ1R)/
IO				IO, DIFFIO_R6p, (DQ1R)/
IO				IO, (DQ1R)/

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U14F

BANK 6

IO	D5	B6	G16	SDRAM_D8
IO	D5	B6	G15	SDRAM_DQ0M1
IO	D13	B6	F13	SDRAM_BA1
IO	D16	B6	F16	SDRAM_CKE
IO	D15	B6	F15	SDRAM_A12
IO	D16	B6	F16	SDRAM_A6
IO	D14	B6	F14	SDRAM_A10
IO	D16	B6	D16	SDRAM_A11
IO	D15	B6	D15	SDRAM_A9
IO	D11	B6	G11	SDRAM_BA0
IO	D16	B6	C16	SDRAM_A8
IO	D15	B6	C15	SDRAM_A7
IO				IO, DIFFIO_R4n, (INIT_DONE)
IO				IO, DIFFIO_R4p, (CRC_ERROR)
IO				IO, DIFFIO_R3n, (nCEO)
IO				IO, DIFFIO_R3p, (CLKUSR)
IO				IO, (DQ50R/CQ1R,DPCLK5)/(DQ50R/CQ1R,DPCLK5)
IO				IO, VREFB6N0
IO				IO, DIFFIO_R2n, (DQ1R)/
IO				IO, DIFFIO_R2p, (DQ1R)/
IO				IO, DIFFIO_R1n, (DQ52R/CQ3R)/(DQ52R/CQ3R)
IO				IO, DIFFIO_R1p, (DQ1R)/

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U14G

BANK 7

IO	D5	B7	C14	SDRAM_A3
IO	D14	B7	D14	SDRAM_A2
IO	D11	B7	D11	LED0
IO	D12	B7	D12	BEEP
IO	D13	B7	A13	GBC_KEY
IO	D13	B7	B13	GBC_LED
IO	D14	B7	A14	SDRAM_A4
IO	D14	B7	B14	SDRAM_CLK
IO	D11	B7	E11	SDRAM_A1
IO	D10	B7	E10	LED2
IO	D12	B7	B12	UART2_RX
IO	D12	B7	T12	UART2_TX
IO	D11	B7	A11	RS485_DE
IO	D11	B7	B11	VGA_HSYN
IO	D11	B7	C11	LED1
IO	D10	B7	F10	I2WIRES_DQ
IO	D10	B7	F9	LED3
IO	D11	B7	F11	SDRAM_A0
IO	D15	B7	A15	SDRAM_A5
IO	D10	B7	A10	VGA_D10
IO	D10	B7	B10	VGA_VSYN
IO	D9	B7	C9	LEDSEG_C
IO	D9	B7	D9	LEDSEG_DOT
IO	D9	B7	E9	ADDA_SCL
IO	D9	B7	A9	VGA_D13
IO	D9	B7	B9	VGA_D12
IO				IO, DIFFIO_T21n, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T21p, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T20n, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T20p, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T19n, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T19p, (DQ5T)/(DQ5T)
IO				IO, PLL2_CLKOUTn
IO				IO, PLL2_CLKOUTp
IO				IO, RUP4
IO				IO, RDNA
IO				IO, DIFFIO_T18n, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T18p, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T17n, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T17p, (DQ5T)/(DQ5T)
IO				IO, VREFB7N0
IO				IO, DIFFIO_T16n, (DQ52T/CQ3T)/(DQ52T/CQ3T)
IO				IO, DIFFIO_T16p, (DQ52T/CQ3T)/(DQ52T/CQ3T)
IO				IO, DIFFIO_T15n, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T15p, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T14n, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T14p, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T13n, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T13p, (DQ5T)/(DQ5T)
IO				IO, (DM5T/BWS#5T)/(DM5T/BWS#5T)
IO				IO, (DQ54T/CQ5T)/(DQ54T/CQ5T)
IO				IO, DIFFIO_T12n, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T12p, (DQ5T)/(DQ5T)

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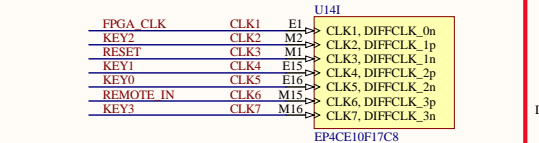
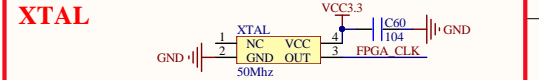
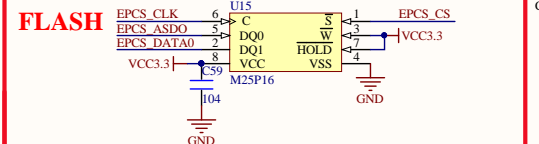
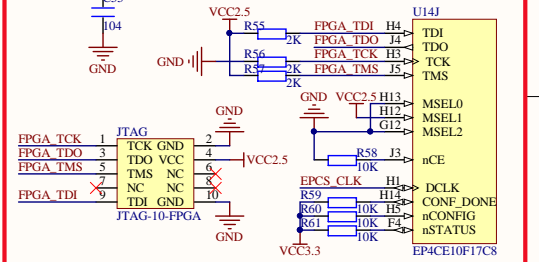
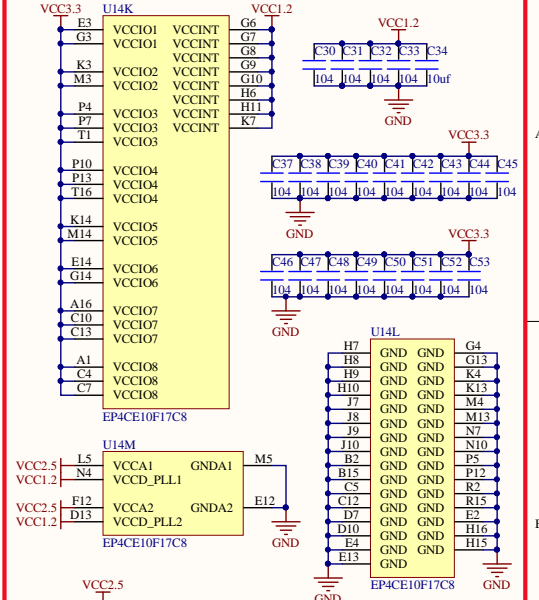
U14H

BANK 8

IO	D8	B8	A8	VGA_D15
IO	D8	B8	B8	VGA_D14
IO	D8	B8	C8	IIC_SDA
IO	D8	B8	D8	IIC_SCL
IO	D8	B8	E8	ADDA_SDA
IO	D8	B8	F8	TPAD
IO	D7	B8	A7	VGA_D6
IO	D7	B8	B7	VGA_D5
IO	D6	B8	F6	ETH_RXDV
IO	D6	B8	E7	ETH_RXD2
IO	D6	B8	C6	I2S_DACDAT
IO	D6	B8	A6	VGA_D8
IO	D6	B8	B6	VGA_D7
IO	D7	B8	E7	I2S_MCLK
IO	D6	B8	E6	I2S_LRCK
IO	D5	B8	A5	VGA_D0
IO	D2	B8	A2	VGA_D4
IO	D5	B8	B5	VGA_D9
IO	D4	B8	A4	VGA_D1
IO	D5	B8	D5	VGA_D11
IO	D6	B8	D6	I2S_ADCDAT
IO	D3	B8	A3	VGA_D3
IO	D3	B8	B3	VGA_D2
IO	D3	B8	C3	UART3_RX
IO	D3	B8	D3	ETH_MDIO
IO				IO, DIFFIO_T2p, (DQ5T1/CQ1T#,DPCLK7)/(DQ5T1/CQ1T#,DPCLK7)
IO				IO, DIFFIO_T1n, (DQ5T)/(DQ5T)
IO				IO, DIFFIO_T1p, (DQ5T)/(DQ5T)

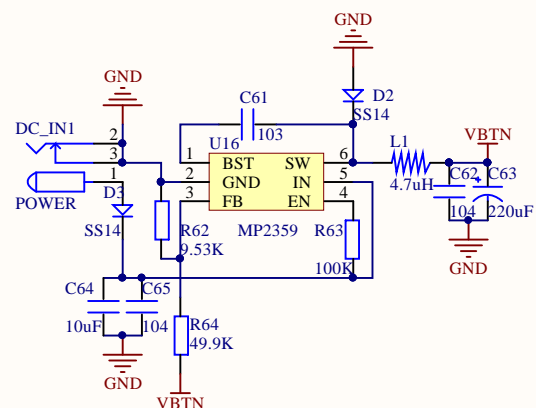
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## POWER&amp;JTAG

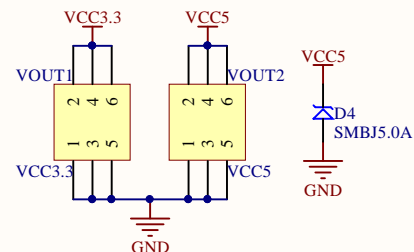


Title:		
Author:	Size:	Cannot open file
Date:	File:	E:/protel
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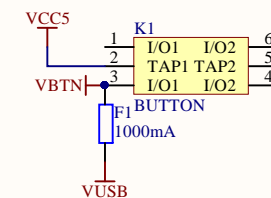
## DC POWER IN



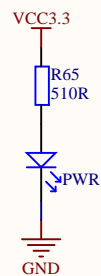
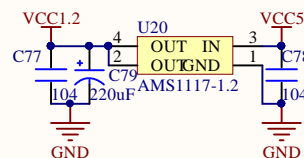
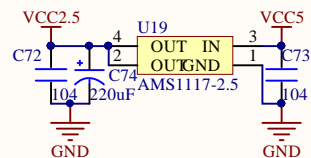
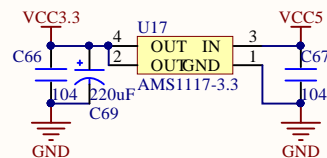
## ON BOARD POWER SOURCE



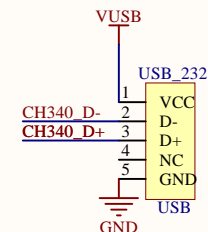
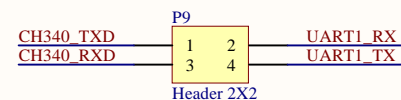
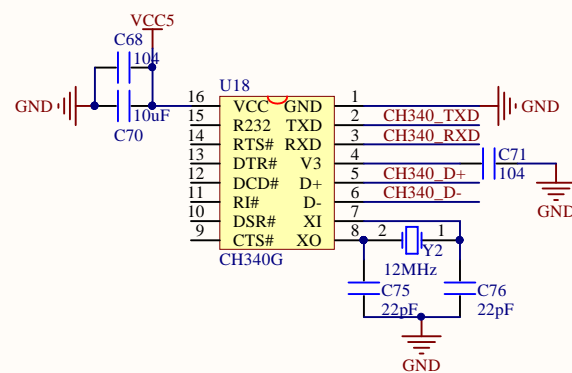
## POWER SWITCH



### 3.3V/2.5V/1.2V/ POWER



## USB USART&USB POWER



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