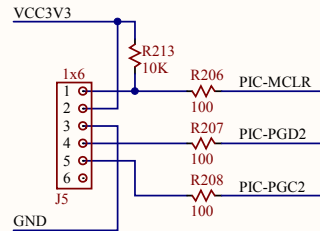
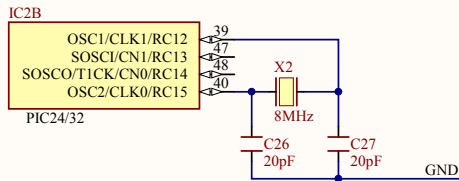
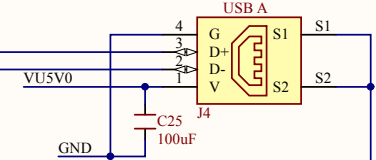
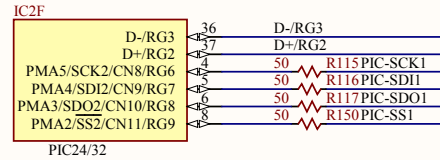
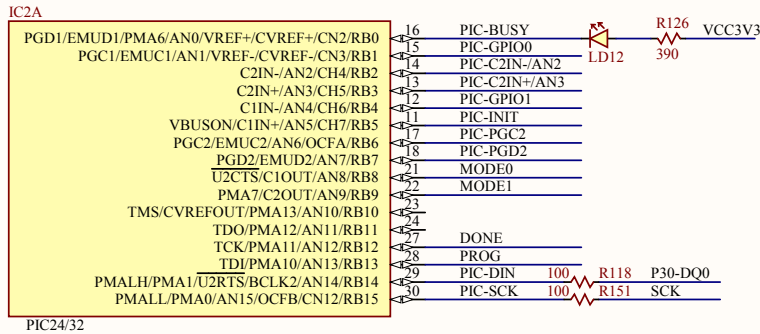
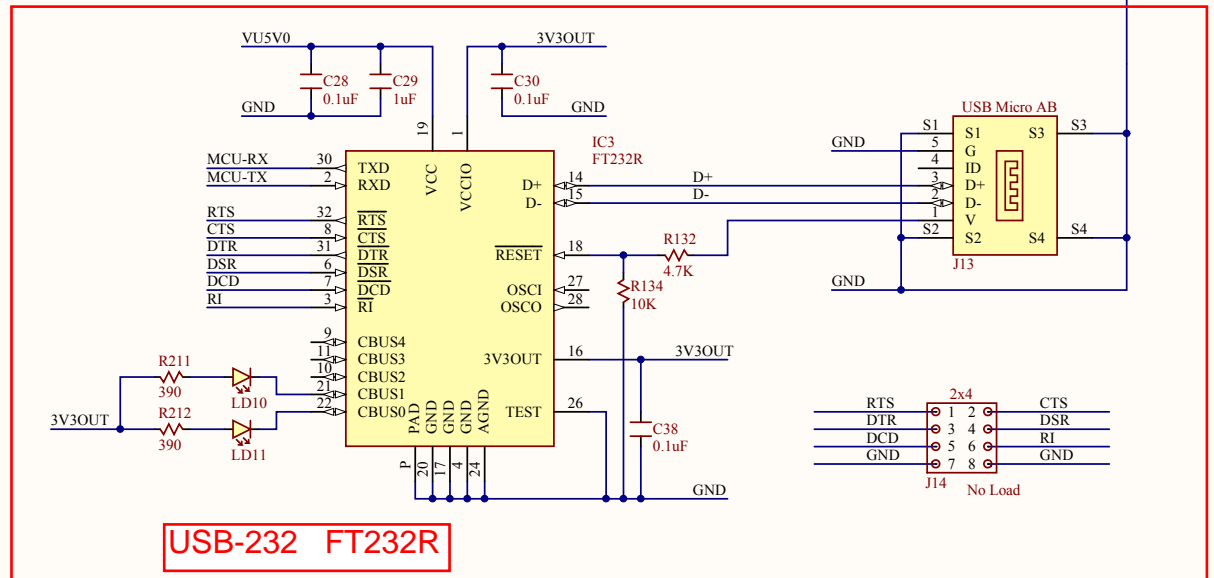
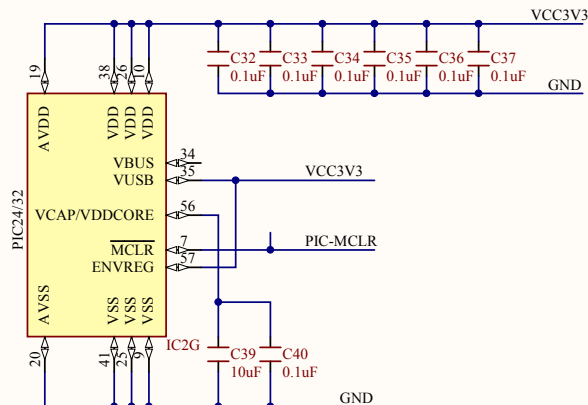
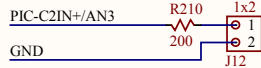
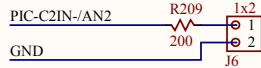


Title Nexys III		Rev B.0 Copyright 2011
Circuit General IO		
Doc# 500-182		
Engineer CC		
Author GMA		
Date 2/17/2011		
Sheet# 2 out of 10		


DIGILENT
 BEYOND THEORY



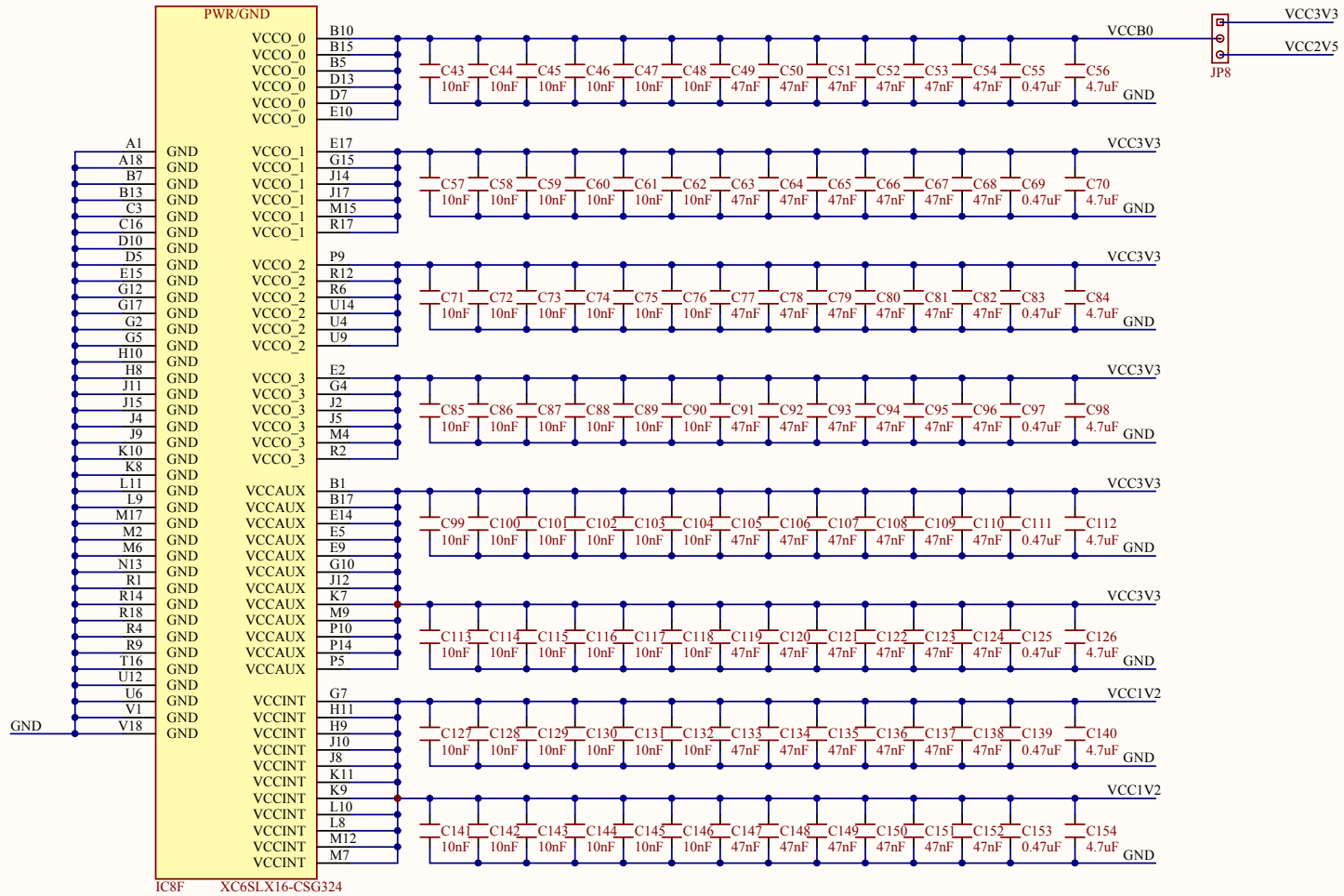
ANALOG HEADERS

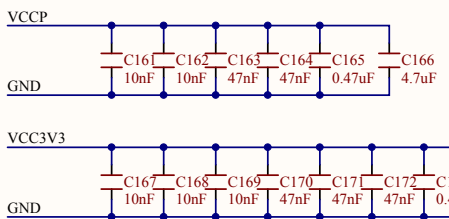
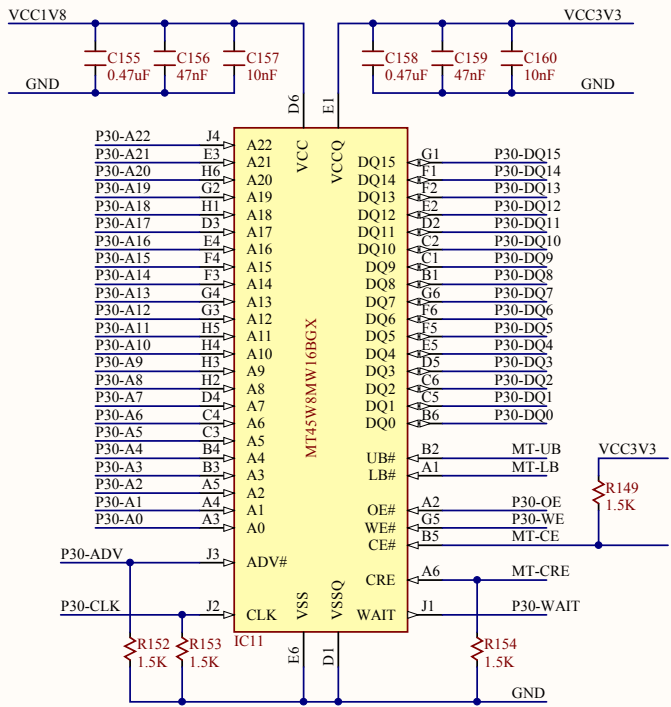


Title		Nexys III		Rev	B.0
Circuit		USB Host		Copyright 2011	
Doc#		500-182			
Engineer		CC			
Author		GMA			
Date		2/17/2011			
Sheet#		4 out of 10			

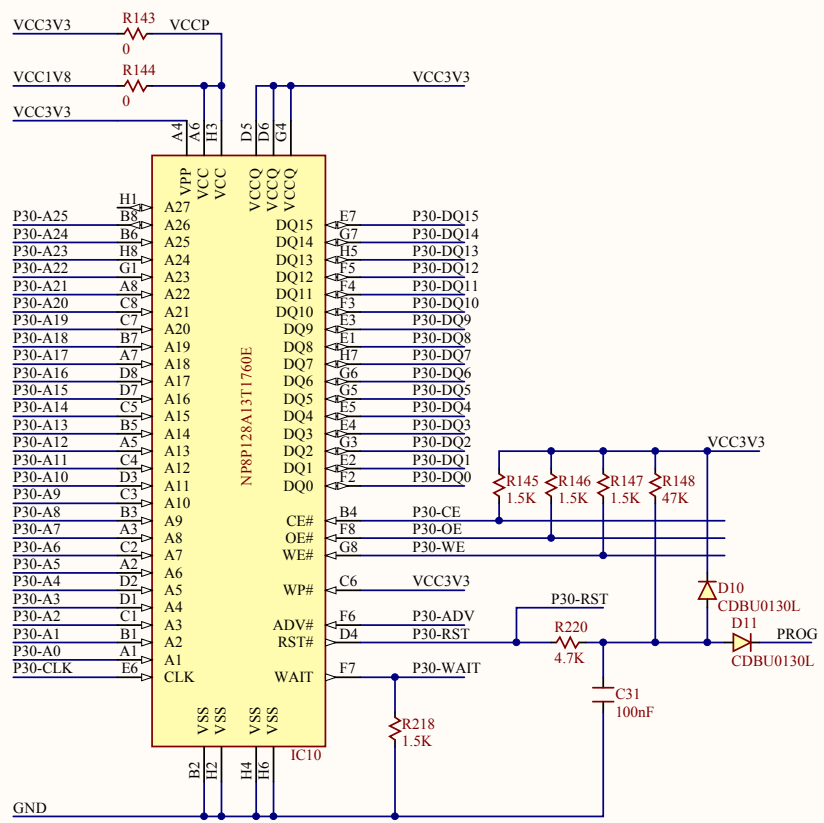


供电






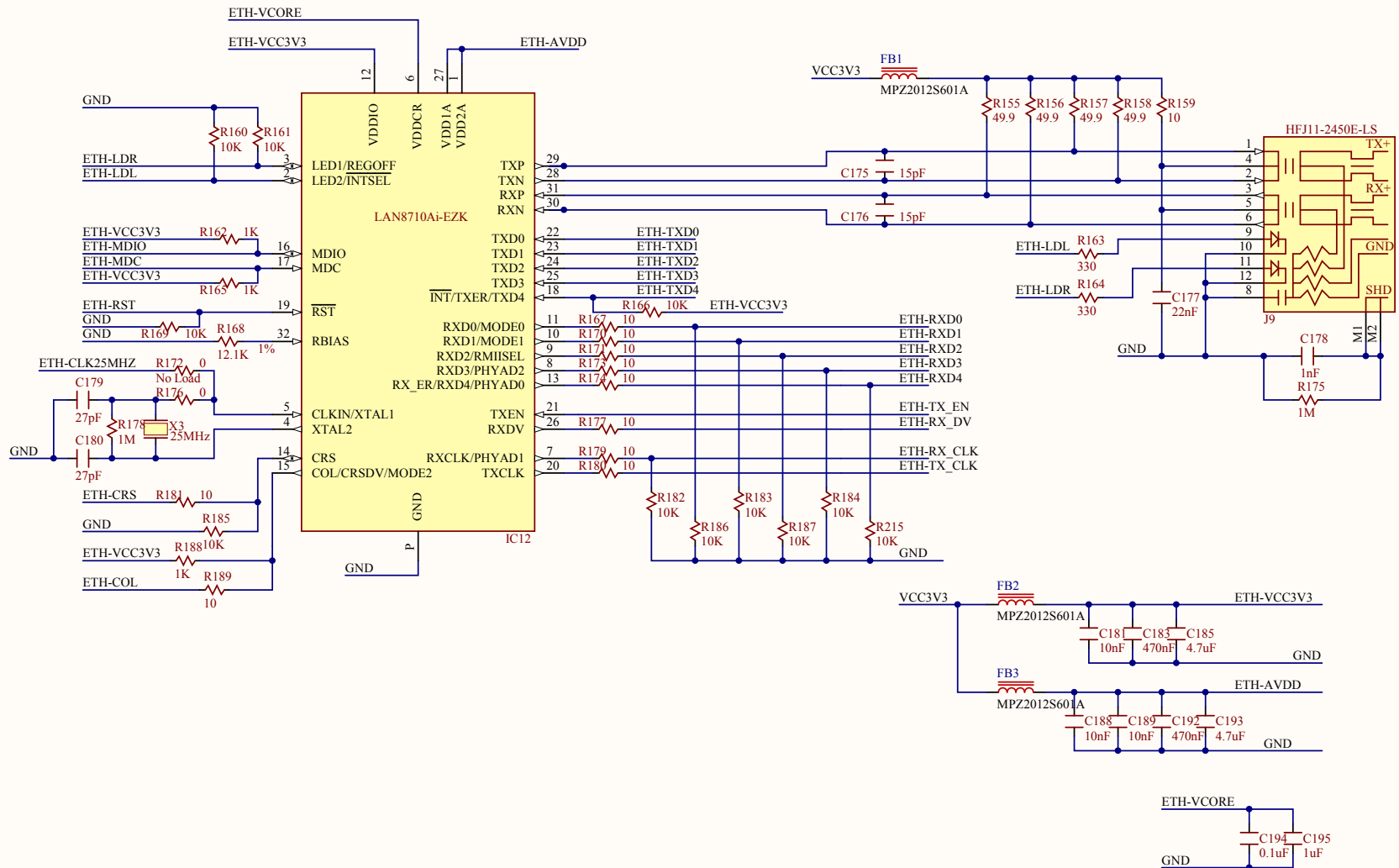
Note: Load resistor to 3.3V and remove resistor to 1.8V for P33




Title Nexys III		Rev B.0 Copyright 2011
Circuit	Memory	
Doc#	500-182	
Engineer	CC	
Author	GMA	
Date	2/17/2011	
Sheet#	8 out of 10	

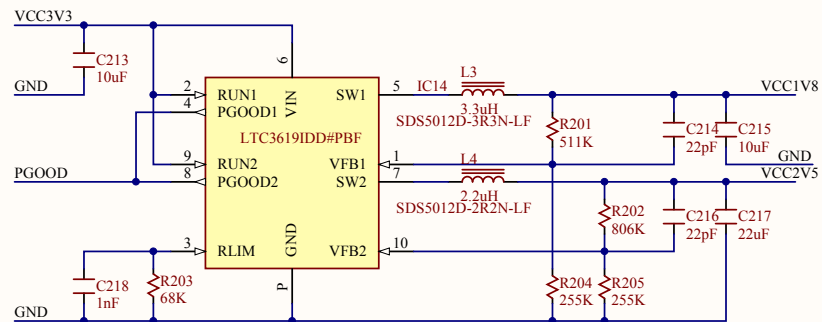
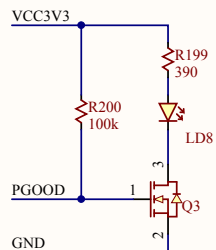
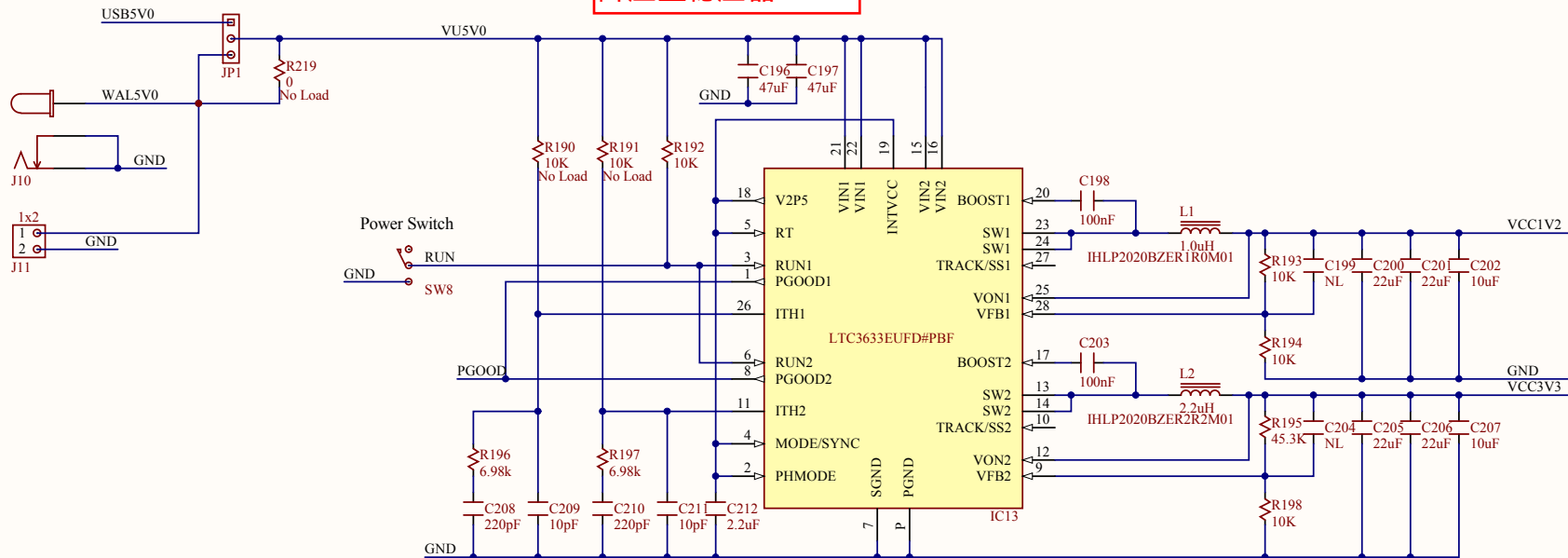
**DIGILENT**
BEYOND THEORY

ETHERNET



Title Nexys III		Rev B.0 Copyright 2011
Circuit Ethernet		
Doc# 500-182		
Engineer CC		
Author GMA		
Date 2/17/2011		
Sheet# 9 out of 10		

降压型稳压器



Title		Nexys III		Rev		B.0	
Circuit		Power		Doc#		500-182	
Author		GMA		Date		2/17/2011	
Sheet#		10 out of 10		Engineer		CC	
				Date		2/17/2011	
				Sheet#		10 out of 10	



DIGILENT
BEYOND THEORY