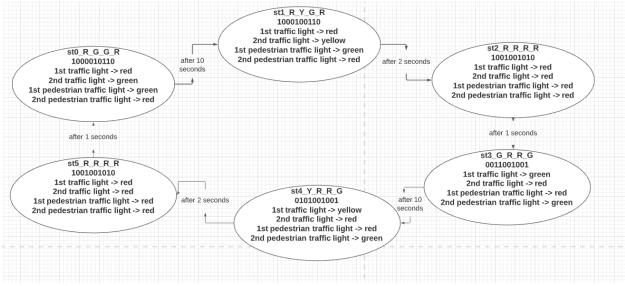
EDA Project Documentation (Traffic light controller)

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o Introduction:

We have two traffic lights for cars. Each one of them has traffic light for pedestrian. When 1st traffic light of car is green / yellow, the 2nd traffic light will be red, 1st pedestrian traffic light will be red, 2nd pedestrian traffic light will be green and same for 2nd traffic light for cars. In some second all traffic lights will be red. Two pedestrian traffic lights have buttons for pedestrian to press them to be able to cross the street. When pedestrian press button two traffic lights of car will be red and two traffic lights for pedestrian will be green for two seconds. After two seconds state of traffic lights will continue from last state.

State Diagram:



o Code of Traffic light controller:

```
LIBRARY IEEE;
2
      USE IEEE.STD_LOGIC_1164.ALL;
3
      USE IEEE.STD LOGIC UNSIGNED.ALL;
 4
      ENTITY TrafficLightController IS
5
       PORT (
 6
          trafficLights :OUT STD LOGIC VECTOR(9 DOWNTO 0);
                                                                 -- we need here 10 bits (3 for 1st traffic light, 3 for 2nd
          clk : IN STD LOGIC;
8
          press : IN STD LOGIC
9
       );
      END TrafficLightController;
10
11
12
      ARCHITECTURE Behavioral of TrafficLightController IS
13
        TYPE state type IS(st0 R G G R, st1 R Y G R, st2 R R R R, st3 G R R G, st4 Y R R G, st5 R R R R, st6 R R G G);
        SIGNAL state:state type;
14
        SIGNAL state1:state_type;
15
                                        -- to save state of two traffic lights so if pedestrain press button we have last st
16
        SIGNAL count:STD_LOGIC_VECTOR(4 DOWNTO 0); -- to count seconds for traffic lights for cars
                                                        -- to count seconds for traffic lights of pedestrain if pedestrain p
17
        SIGNAL countp:STD LOGIC VECTOR (4 DOWNTO 0);
        CONSTANT sec10:STD_LOGIC_VECTOR(4 DOWNTO 0) := "01010";
CONSTANT sec2:STD_LOGIC_VECTOR(4 DOWNTO 0) := "00010";
18
19
        CONSTANT sec1:STD LOGIC VECTOR(4 DOWNTO 0) := "000001";
21
        BEGIN
22
          PROCESS(clk, press)
23
            BEGIN
24
              IF press = '1' THEN --pedestrain pressed button
                state <= st6_R_R_G_G;
25
26
                if countp < sec2 then
27
                  state <= st6 R R G G; --reset to red for two traffic lights and green for two pedestrain traffic lights
                  countp <= countp + 1;
28
29
                else
30
                  countp <= "00001";
31
                end if:
              ELSIF Clk' event and Clk = '1' then -- check for raising edge
32
33
                case (statel) is
                  when st0_R_G_G_R =>
34
                    if count < sec10 then
35
36
                      statel <= st0 R G G R;
37
                    state <= statel;
```

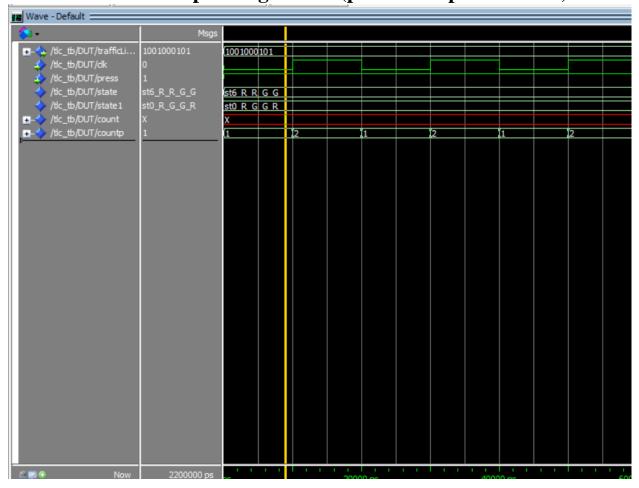
```
38
                      count <= count + 1;
39
                    else
40
                      statel <= stl_R_Y_G_R;
41
                      state <= state1;
                      count <= "000000";
42
43
                    end if;
                  when stl_R_Y_G_R =>
44
45
                    if count < sec2 then
46
                      statel <= stl R Y G R;
47
                      state <= statel;
48
                      count <= count + 1;
49
                      state1 <= st2 R R R R;
50
                      state <= statel;
                      count <= "000000";
53
                     end if;
54
                  when st2_R_R_R_R =>
                    if count < sec1 then
                      state1 <= st2_R_R_R_R;
                      state <= statel;
                      count <= count + 1;
59
60
                      statel <= st3 G R R G;
61
                      state <= statel;
                      count <= "000000";
63
                    end if;
64
                  when st3 G R R G =>
65
                    if count < sec10 then
                      statel <= st3 G R R G;
66
67
                      state <= statel;
                      count <= count + 1;
68
69
                    else
                      statel <= st4 Y R R G;
70
                      state <= state1:
                      count <= "00000";
```

```
74
                  when st4 Y R R G =>
75
                   if count < sec2 then
                     statel <= st4_Y_R_R_G;
76
77
                     state <= statel;
78
                      count <= count + 1;
79
                   else
80
                     statel <= st5_R_R_R_R;
                     state <= statel;
81
                     count <= "000000";
82
83
                    end if;
84
                  when st5_R_R_R_R =>
85
                   if count < sec1 then
                     statel <= st5_R_R_R_R;
86
87
                     state <= statel:
88
                     count <= count + 1;
89
                   else
90
                     statel <= st0 R G G R;
                     state <= statel;
91
                     count <= "000000";
92
93
                   end if:
                 when st6_R_R_G_G =>
94
95
                   countp <= "000000";
                 END CASE;
97
               END IF;
              END PROCESS;
98
99
       OUTPUT_DECODE: process (state)
100
101
         BEGIN
102
           case state is
             when st0_R_G_G_R => Trafficlights <= "1000010110"; -- 1st traffic -> Red, 2nd traffic -> green, 1st peds traffic ->
103
              when stl R Y G R => Trafficlights <= "1000100110"; -- 1st traffic -> Red, 2nd traffic -> yellow, 1st peds traffic
104
              when st2 R R R => Trafficlights <= "1001001010"; -- 1st traffic -> Red, 2nd traffic -> red, 1st peds traffic ->
105
              when st3 G_R_R_G => Trafficlights <= "0011001001"; -- 1st traffic -> green, 2nd traffic -> red, 1st peds traffic ->
106
              when st4 Y R R G => Trafficlights <= "0101001001"; -- 1st traffic -> yellow, 2nd traffic -> red, 1st peds traffic
107
              when st5 R R R R => Trafficlights <= "1001001010"; -- 1st traffic -> Red, 2nd traffic -> red, 1st peds traffic ->
108
              when st6 R R G G => Trafficlights <= "1001000101"; -- 1st traffic -> red, 2nd traffic -> red, 1st peds traffic ->
           END CASE;
110
       END process;
111
112
     END Behavioral;
```

• Code of test bench:

```
LIBRARY IEEE;
 1
      USE IEEE.STD_LOGIC_1164.ALL;
 2
 3
     ENTITY TLC_tb is
 4
     END ENTITY;
     ARCHITECTURE tb of TLC_tb IS
 5
 6
     signal Trafficlights : std logic vector (9 downto 0);
     signal clk, press: std logic;
 7
    begin
 8
9
10
    DUT : ENTITY work.TrafficLightController
11
    PORT MAP(Trafficlights=>Trafficlights,clk=>clk,press=>press);
12
    Clock : process
13
    begin
    Clk <= '0';
14
15
     wait for 10 ns;
16
     Clk <= '1';
17
     wait for 10 ns;
18
     end process;
19
     stimulis : process
     begin
20
21
22
      report ("Starting simulation");
      press <= 'l'; wait for 60 ns;
23
     press <= '0'; wait for 200000000 ns;
24
      report ("End simulation");
25
    end process;
26
    end architecture;
```

• Wave Form of pressing button (pedestrian press button):



• Wave Form of traffic lights

