

Exercise 2: Register File

Student's Name: Mohammed Fareed Section: 4

PreLab		Point Value	Points Earned	Comments
PreLab	MIPs Questions	10	10	agf 2-8
	Testbench Cases	5	5	

Demo		Point Value	Points Earned	Date
Demo	Behavioral Simulation	10	10	agf 2-22-24
	Post-Synthesis Timing Simulation	5	5 5	
	Synthesis Schematic	5	5	
	Post-Implementation Timing Simulation	5	5	
	Implementation Utilization Report	5	5	
	EDAPlayground Behavioral Waveform	5	5	
	Hardware Demonstration	10	10	

To receive any grading credit students must earn points for both the demonstration and the report. Students must also submit all VHDL code as .TXT files. Failing to do so may result in no credit for the assignment.