## CMPE 260 Laboratory Exercise 3 Instruction Fetch and Decode Stages

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Performed: March 7, 2024 Submitted: March 10, 2024

Lab Section: 4

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TA: Aubrey Tarmu Henry Bang William Tom

Lecture Section: 2

Professor: Prof. Marcin Lukowiak

By submitting this report, you attest that you neither have given nor have received any assistance (including writing, collecting data, plotting figures, tables or graphs, or using previous student reports as a reference), and you further acknowledge that giving or receiving such assistance will result in a failing grade for this course.

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Abstract

Design Methodology

Results and Analysis

Conclusion

Exercise 1: Introduction to Vivado & Simple ALU

Student's Name: Mohammed Farced Section: 4

Demo		Point Value	Points Earned	Date
Part 1:	Behavioral Simulation	4	4	J
	Post-Synthesis Timing Simulation	4	LI	oft 2.
-bit	Synthesis Schematic	4	Ч	
ALU	Synthesis Utilization Report	4	Ц	
	Post- Implementation Timing Simulation	4	4	
	RTL Schematic for srlN shifter	4	U	
	Hardware Demonstration	4	4	
Part 2:	Behavioral Simulation	16	16	N
32-bit ALU	Post- Implementation Timing Simulation	16	6	

To receive any grading credit students must earn points for both the demonstration and the report.