Exercise 1: Introduction to Vivado & Simple ALU

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	Demo	Point Value	Points Earned	Date
Part 1: 4-bit ALU	Behavioral Simulation	4	4	
	Post-Synthesis Timing Simulation	4	4	oct 2-8
	Synthesis Schematic	4	4	
	Synthesis Utilization Report	4	4	
	Post-Implementation Timing Simulation	4	4	
	RTL Schematic for srlN shifter	4	4	
	Hardware Demonstration	4	4	
Part 2: 32-bit ALU	Behavioral Simulation	16	16	
	Post-Implementation Timing Simulation	16	16	

To receive any grading credit students must earn points for both the demonstration and the report.