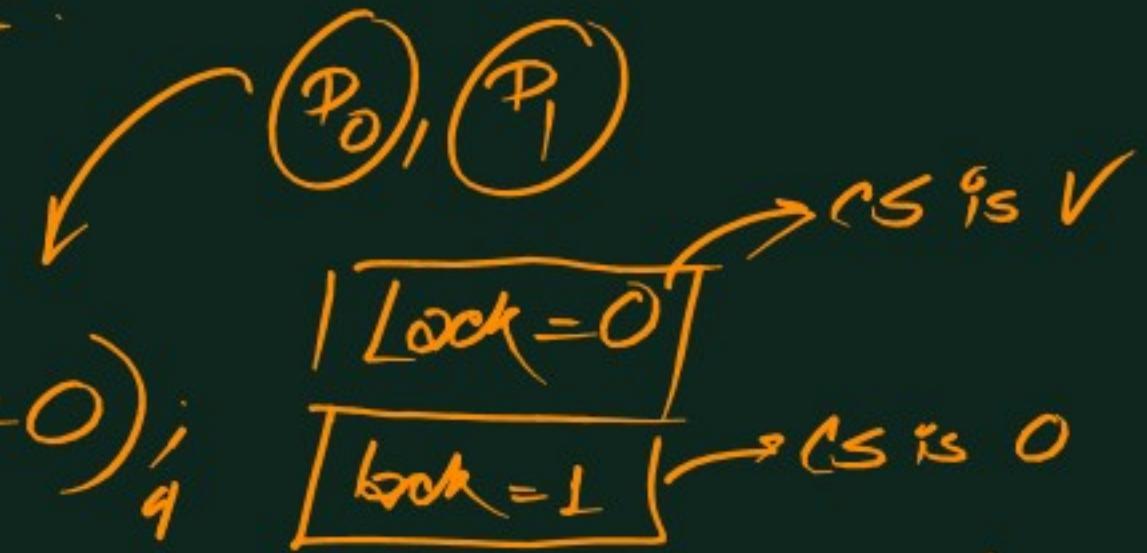
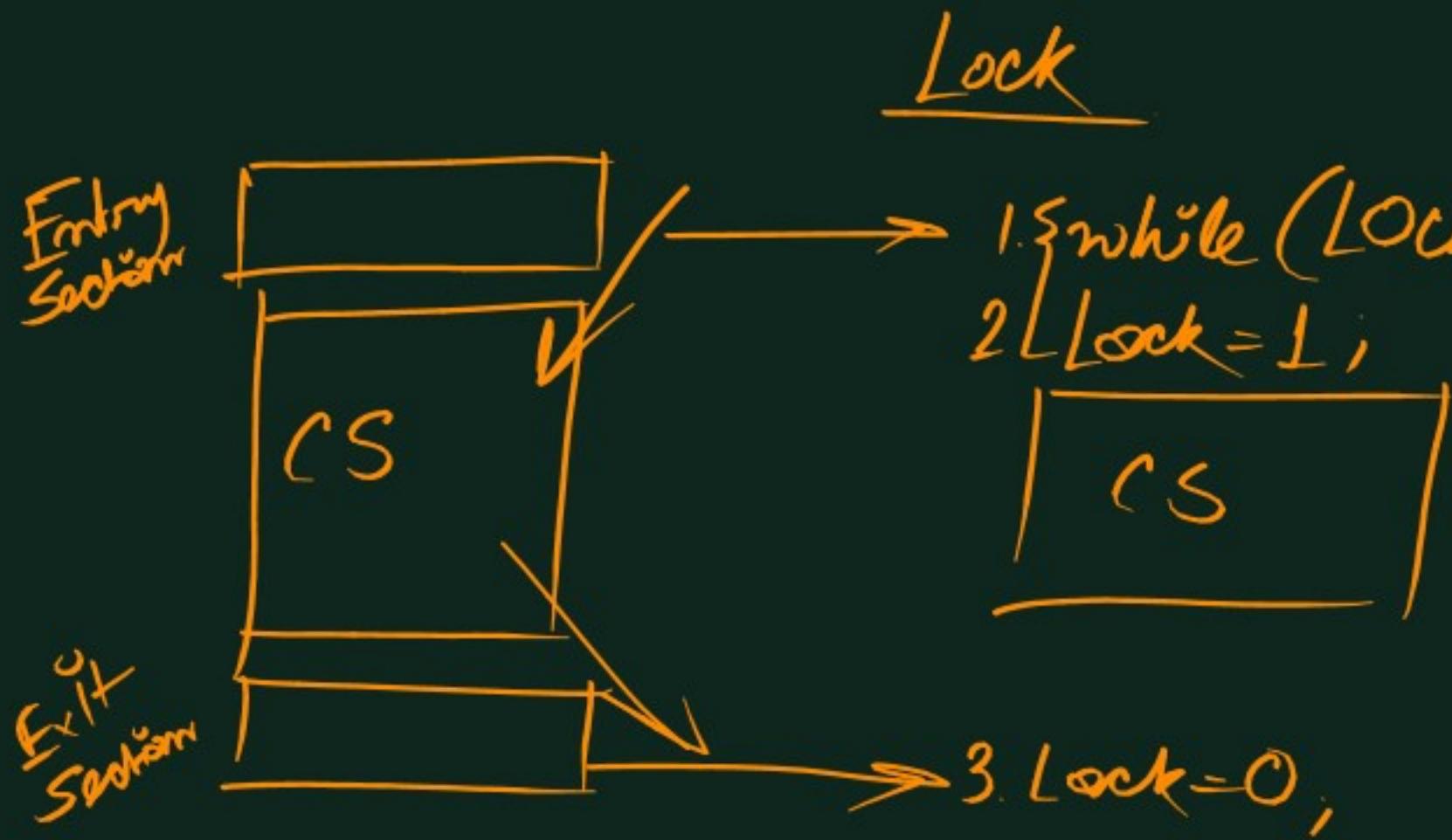


Process Synchronization



Assembly code

```

1. MOV R0, LOCK
2. CMP R0, #0
3. JNE Step1
4. STORE LOCK, #1
  
```

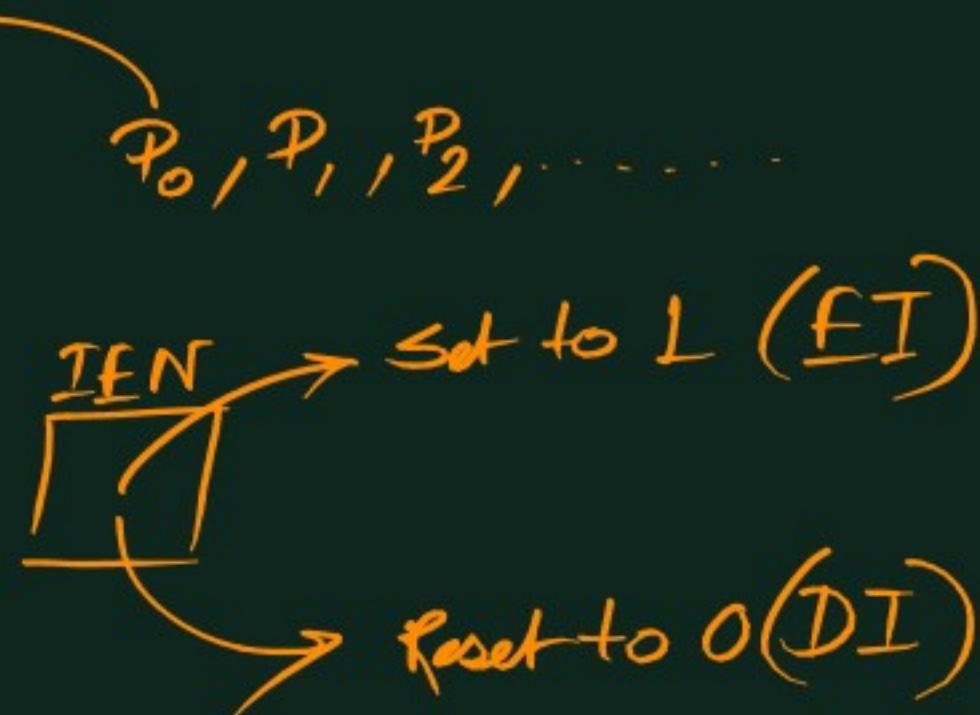
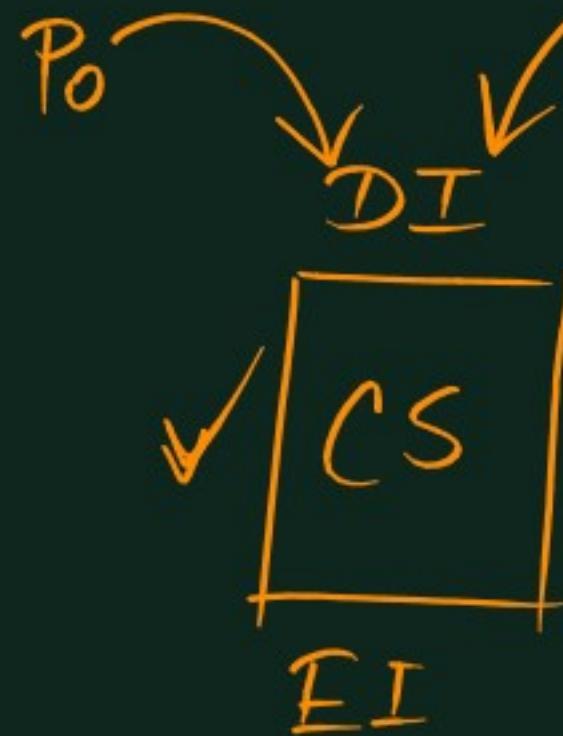
1 Mutual Exclusion }
2 Progress

3 Bounded Waiting }
4 AN/PI }

X TSL Lock \Rightarrow TSL
CMP R₀, #0
JNZ Step L

MOV R₀, Lock
STORE Lock, #1
CMP R₀, #0
JNZ Step L

Interrupts

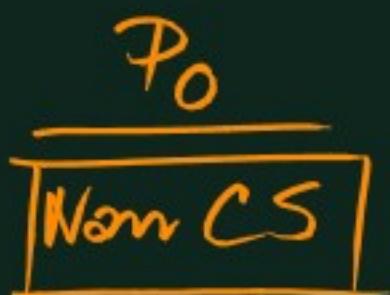


Turn Variable (with strict alternation)

- Binary waiting solution

- 2-process solution

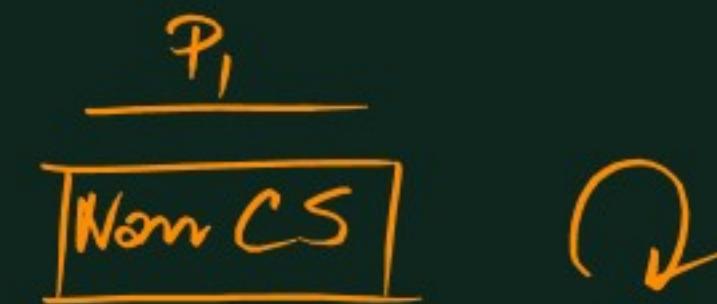
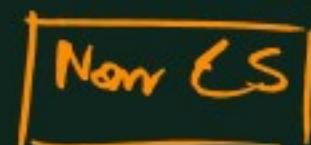
- S(n) mechanism implemented at "user mode."



Ens: `while(Turn!=0);`



ExS: `Turn=1;`



Ens: `while(Turn!=1);`



ExS: `Turn=0;`

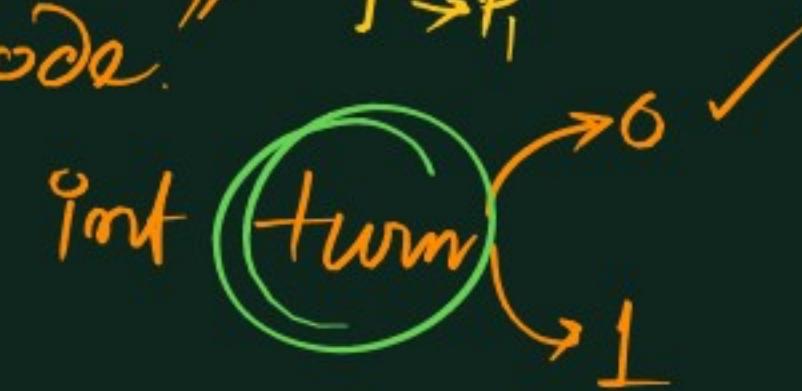


$\frac{L}{0 \rightarrow P_0, P_1, P_2}$

$\frac{1 \rightarrow X}{1}$

$\frac{1}{0 \rightarrow P_0}$

$1 \rightarrow P_1$



ME ✓
 $(P \times)$
 BW ✓
 AN ✓

$P_1: \text{Ens} \quad | \quad P_0: \text{Ens, CS, ExS} \quad | \quad P_1: \text{ExS}$

$\frac{\text{CS}}{\text{ExS}}$

Interested Variable

Interested[0] = T $\rightarrow P_0$ is interested in executing CS

Interested[1] = T $\rightarrow P_1$ " " " "

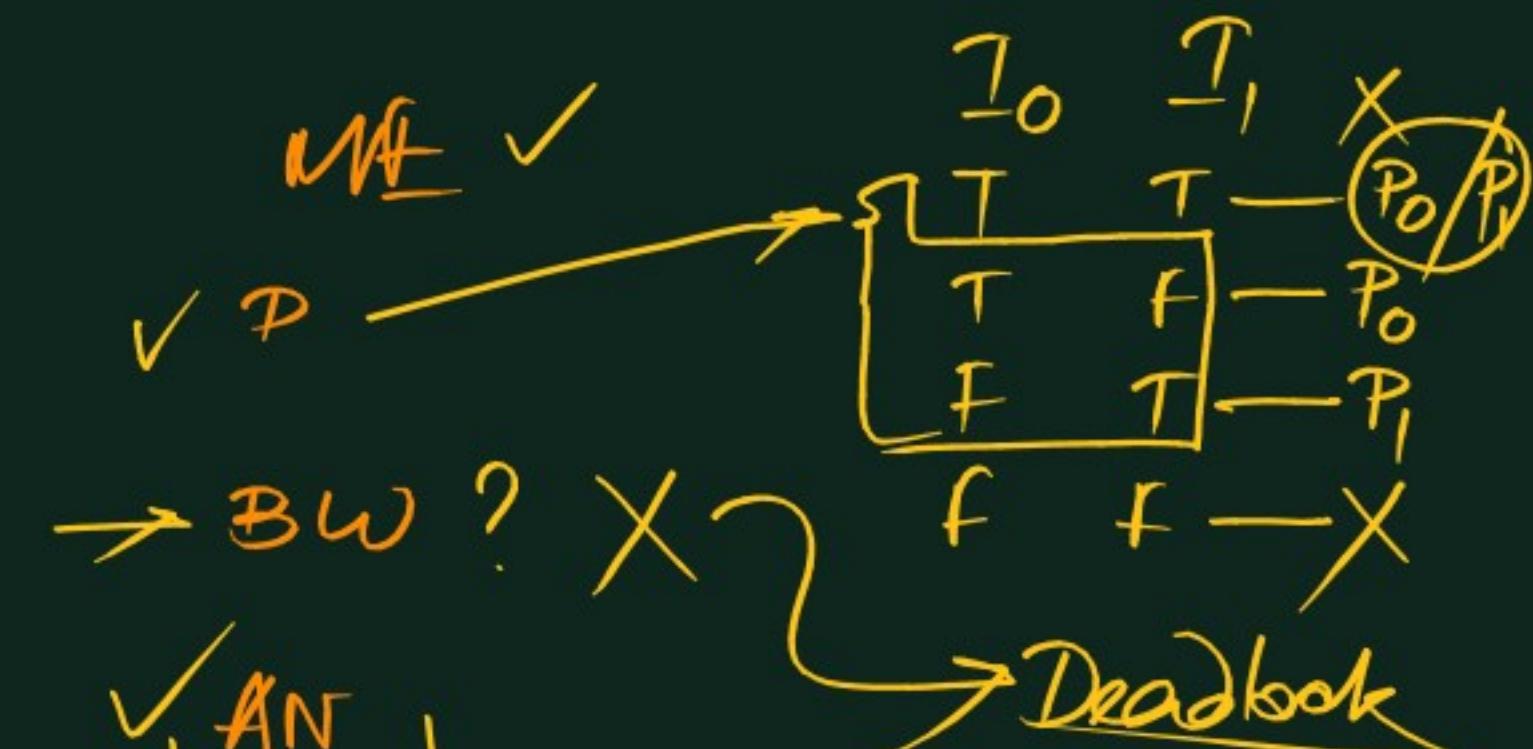
while(1) $\frac{P_0}{\text{Non-CS}}$
 EnS: Interested[0] = T;
 while(Interested[1] == T);
 CS
 ExS: Interested[0] = F;

while(1) $\frac{P_1}{\text{Non-CS}}$
 EnS: Interested[1] = T;
 while(Interested[0] == T);
 CS
 ExS: Interested[1] = F;

Interested[0] = ~~F~~ / F
 Interested[1] = ~~F~~ / T

$P_0 : \text{EnS, CS} \mid P_1 : \text{EnS}$ | $P_0 : \text{CS, ExS} \mid P_1 : \text{EnS}$
 CS

$\frac{P_0}{T_0=T}$	$\frac{P_1}{T_1=T}$	$\frac{P_0}{T_0=F}$	$\frac{P_1}{W^F}$
CS	WR	<u>AN</u> ... $T_0=T$ WR	-



Lock < TSL

Interrupt

{ Turn Variable
Interested Variable

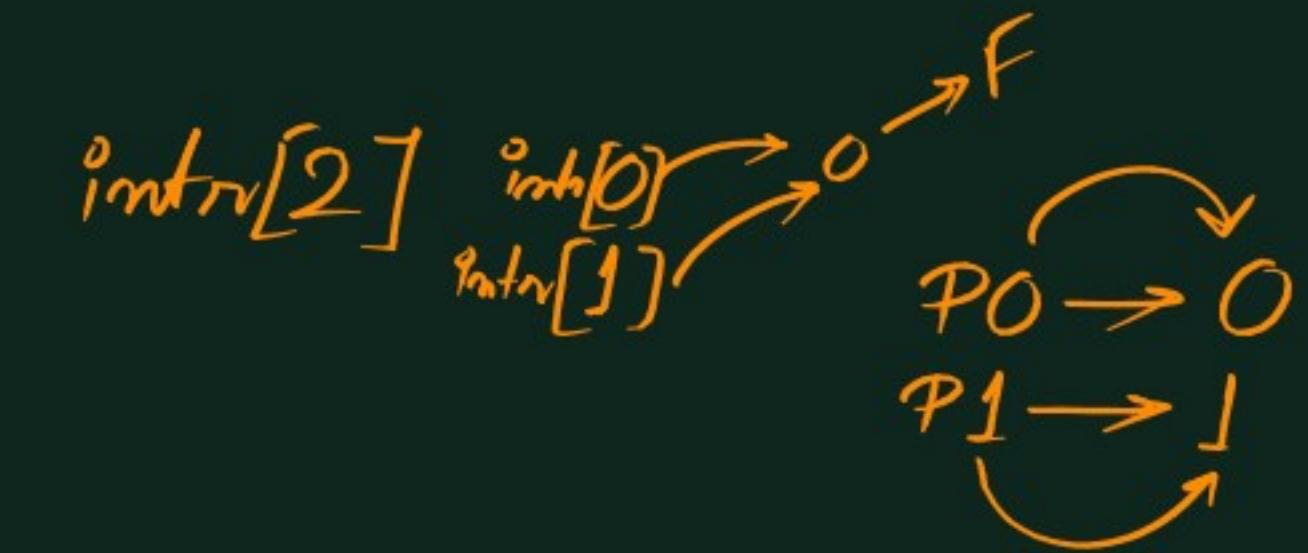
✓ Peterson

→ 2-process solution

Merge { Turn → Global
 Interested → local

Peterson's Algorithm

```
#define N 2
#define TRUE 1
#define FALSE 0
int interested[N] = FALSE;
int turn;
void Entry_section(int process)
{
    int other;
    other = 1 - process;
    interested[process] = TRUE;
    turn = process;
    while (interested[other] == TRUE && turn == process);
}
```



```
void Exit_Section(int process)
{
    interested[process] = FALSE;
}
```

```

#define N 2
#define TRUE 1
#define FALSE 0
int interested[N] = FALSE;
int turn;
void Entry-section (Port broken)
{
    1. int other;
    2. other = 1 - process;
    3. interested[process] = TRUE;
    4. turn = process; } which ever process executes this line first, will get the chance to get into the CS, first
5. while (interested[other] == TRUE && (turn == process),
}
void Exit-section (int broken)
{
    6. interested[process] = FALSE;
}

```

