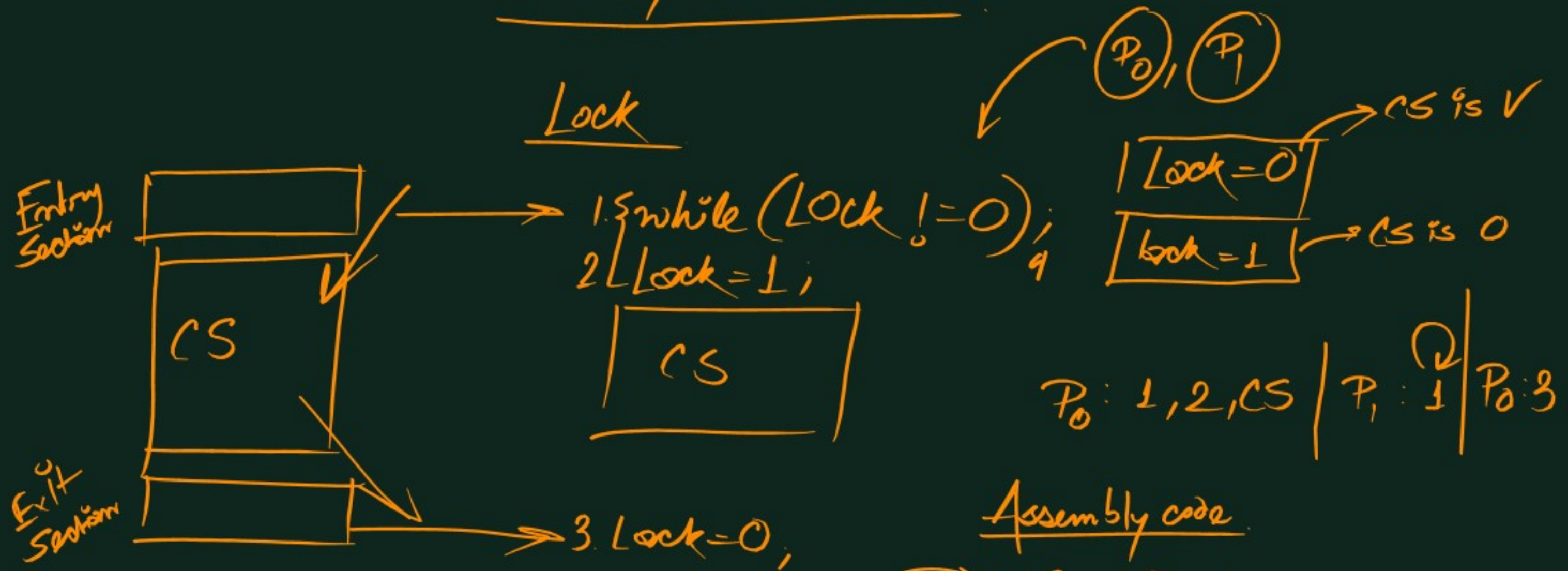


Process Synchronization



Assembly code

```
→ 1. MOV R0, LOCK
→ 2. CMP R0, #0
→ 3. JNZ Step 1
→ 4. STORE LOCK, #1
```

1. Mutual Exclusion } Primary
2. Progress

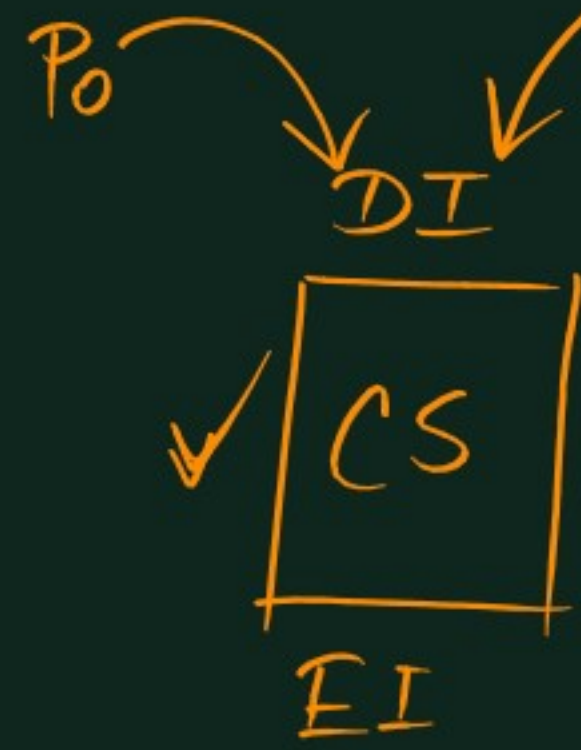
3. Bounded Waiting } optional
4. AN/PI

X TSL Lock \Rightarrow TSL
CMP R0, #0
JNZ Step 1

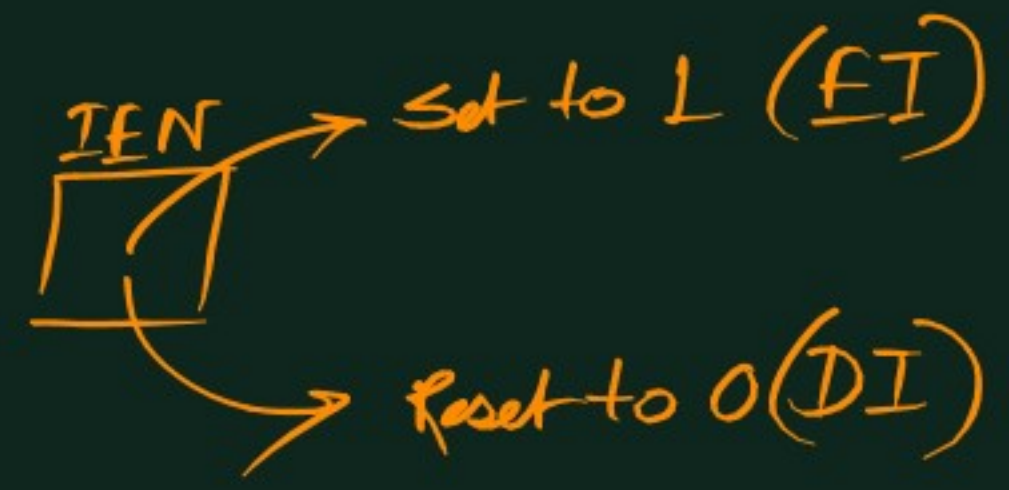
MOV R0, Lock
STORE Lock, #1

CMP R0, #0
JNZ Step 1

Interrupts



P_0, P_1, P_2, \dots



Turn Variable (with strict alternation)

- Busy waiting solution
- 2-process solution (P_0, P_1) (P_i, P_j)
- S/W mechanism implemented at "user mode"

$$\frac{L}{0} \rightarrow P_0, P_1, P_2$$

$$\frac{1}{1} \rightarrow X$$

$$\frac{1}{0} \rightarrow P_0$$

$$1 \rightarrow P_1$$



ME ✓
 PX
 BW ✓
 AN ✓

P₀

Non CS

Ens: while (turn != 0);

CS

ExS: turn = 1;

Non CS

P₁

Non CS

Ens: while (turn != 1);

CS

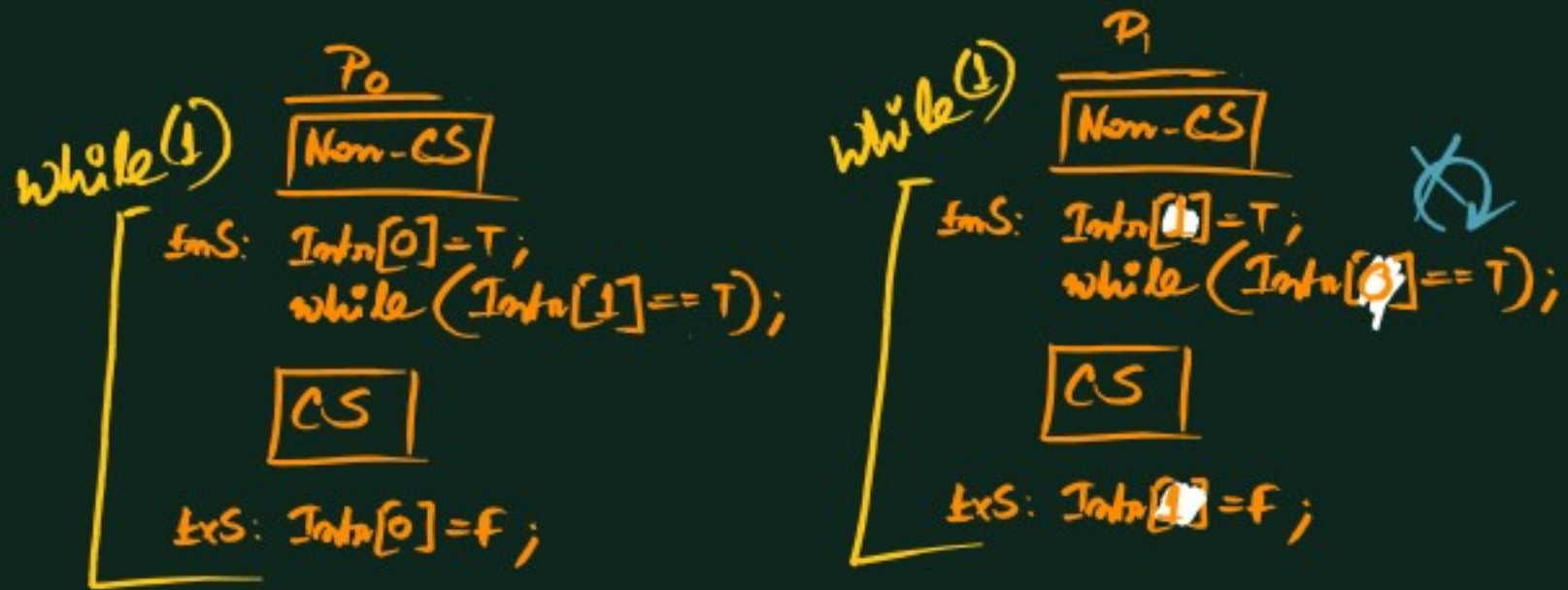
ExS: turn = 0;

Non CS

P₁: Ens | P₀: Ens, CS, ExS | P₁: Ens
 CS
 ExS

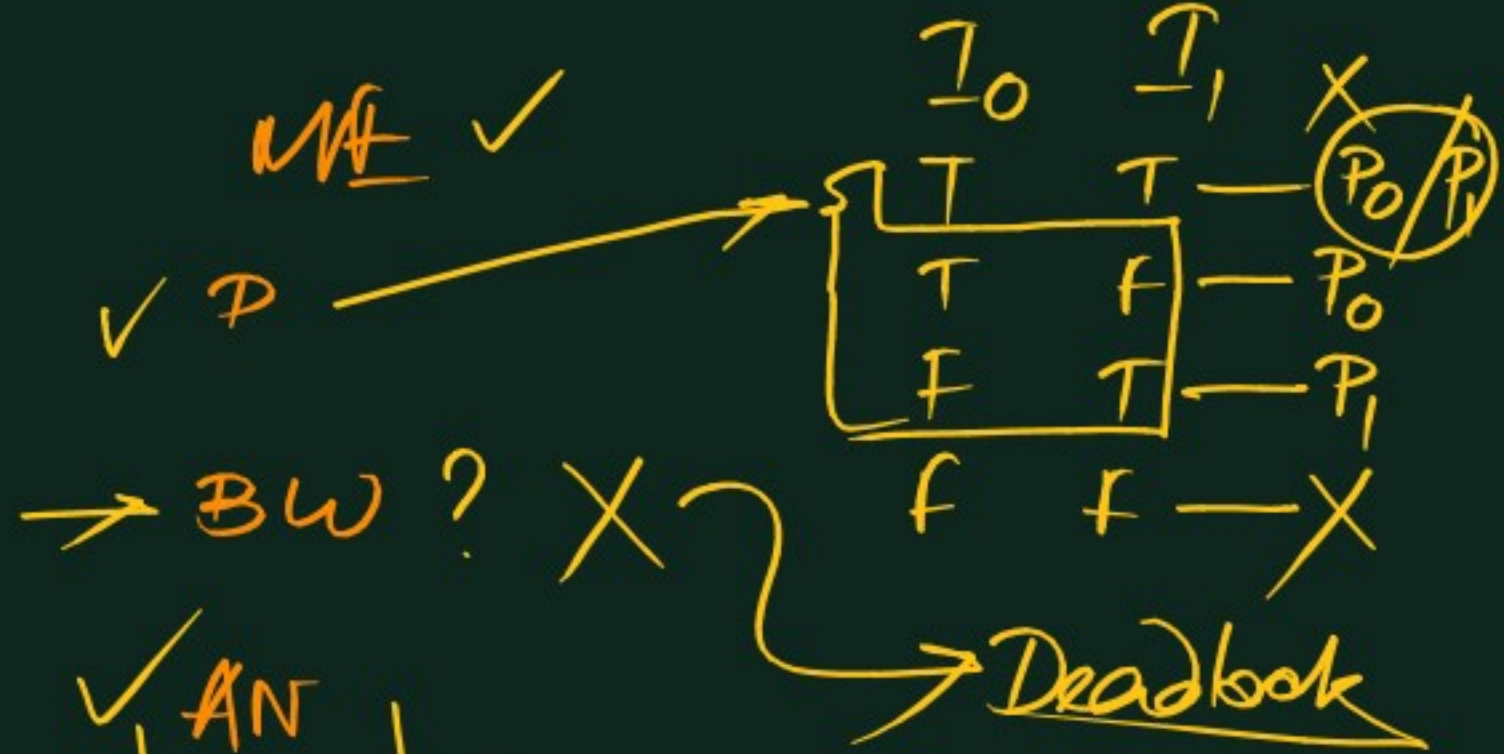
Interested Variable

Interested[0] = T \rightarrow P₀ is interested in executing CS
 Interested[1] = T \rightarrow P₁ " " " " " "



Interested[0] = ~~f~~ ~~T~~ f
 Interested[1] = ~~f~~ T

P₀: EnS, CS | P₁: EnS | P₀: CS, ExS | P₁: EnS, CS



✓ AN

P ₀	P ₁	P ₀	P ₁
I ₀ = T	I ₁ = T	I ₀ = f	Wf
CS	Wf	I ₀ = T	
		Wf	

Lock \leftarrow TSL

Interrupt

{ Turn Variable
Interested Variable

✓ Peterson \longrightarrow 2-process solution

~~Merged~~ { Turn \rightarrow Global
Interested \rightarrow local

Peterson's Algorithm

```
#define N 2
```

```
#define TRUE 1
```

```
#define FALSE 0
```

```
int interested[N] = FALSE;
```

```
int turn;
```

```
void Entry_section(int process)
```

```
{
```

```
    int other;
```

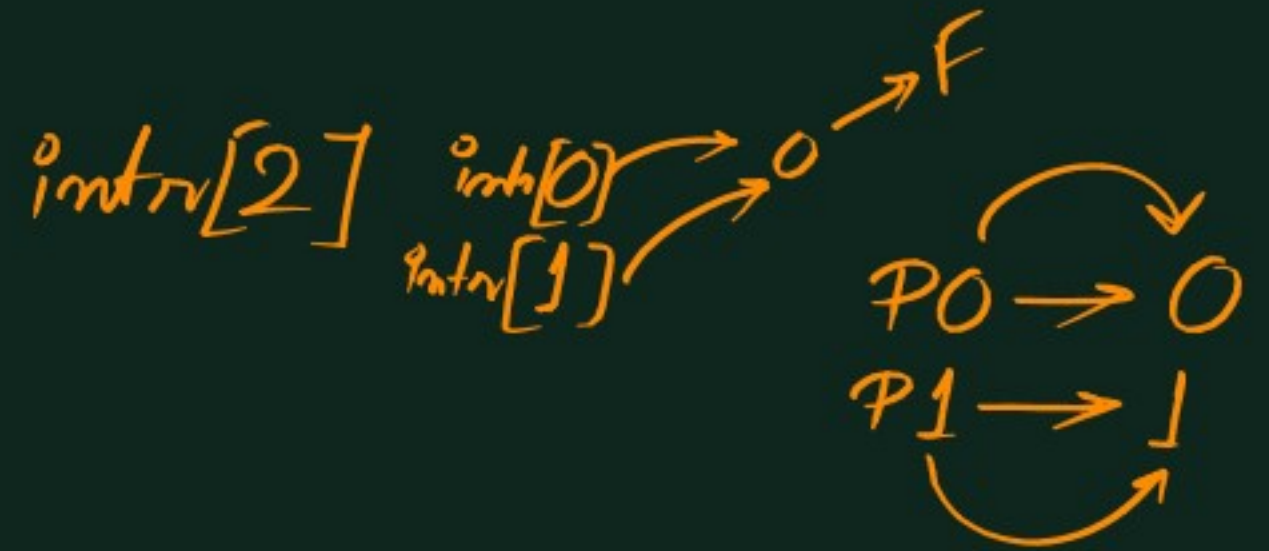
```
    other = 1 - process;
```

```
    interested[process] = TRUE;
```

```
    turn = process;
```

```
    while (interested[other] == TRUE && turn == process);
```

```
}
```



```
void Exit_section(int process)
```

```
{
```

```
    interested[process] = FALSE;
```

```
}
```


#define N 2

#define TRUE 1

#define FALSE 0

int interested[N] = FALSE;

int turn;

void Entry-section (int process)

{
1. int other;

2. other = 1 - process;

3. interested[process] = TRUE;

4. turn = process; }

5. while (interested[other] == TRUE && turn == process);
}

void Exit-section (int process)

{

6. interested[process] = FALSE;

}

whichever process
executes this line
first, will get
the chance to get
into the CS, first

Situation 1:

P₀: 1 2 3 4 5 CS | P₁: 1 2 3 4 5 | P₀: CS

P₁: 5 CS

indx

1	2
---	---

turn

0	1
---	---

Situation 2:

P₀: 1 2 3 | P₁: 1 2 3 4 5 | P₀: 4 5 | P₁: 5 CS

indx

1	2
---	---

turn

0	1
---	---



P₀: 1 2 3 | P₁: 1 2 3 | P₀: 4 5 | P₁: 4 5 | P₀: 5 CS

indx

1	2
---	---

turn

0	1
---	---

Case 1: | P₁: 5 CS

Case 2: | P₀: 1 2 3 4 5