

Introduction to Concurrency

1. Concurrency

Concurrency refers to the execution of **multiple instruction sequences in an overlapping time period**.

In an operating system, concurrency occurs when **multiple processes or threads are in progress at the same time**, allowing better utilization of system resources.

Note: Concurrency does not necessarily mean simultaneous execution. Simultaneous execution is called **parallelism**.

2. Thread

A **thread** is:

- A **single sequence stream of execution** within a process
- An **independent path of execution** inside a process
- A **light-weight process**
- A mechanism to achieve concurrency by dividing a process into **independent execution paths**

Examples:

- Multiple tabs running in a web browser
 - In a text editor, typing, spell checking, formatting, and auto-saving occur concurrently using multiple threads
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3. Thread Scheduling

- Threads are scheduled for execution based on **priority and scheduling policy**.
 - The **operating system scheduler** assigns CPU time slices to threads.
 - Even though threads belong to a process, **CPU allocation is controlled by the OS**, not the runtime environment.
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4. Thread Context Switching

Thread context switching occurs when the OS switches the CPU from one thread to another **within the same process**.

During thread context switching:

- The **memory address space is not switched** (shared among threads)
- The following are switched:
 - Program Counter (PC)

- CPU registers
 - Stack
 - Thread switching is **faster than process context switching**
 - Cache performance may improve due to locality, but **cache contents are not guaranteed to be preserved**
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5. How Does Each Thread Get Access to the CPU?

- Each thread has its **own Program Counter**
 - Based on the **thread scheduling algorithm**, the OS selects a thread for execution
 - The OS fetches instructions using the **PC of the selected thread** and executes them on the CPU
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6. I/O or Time-Quantum Based Context Switching

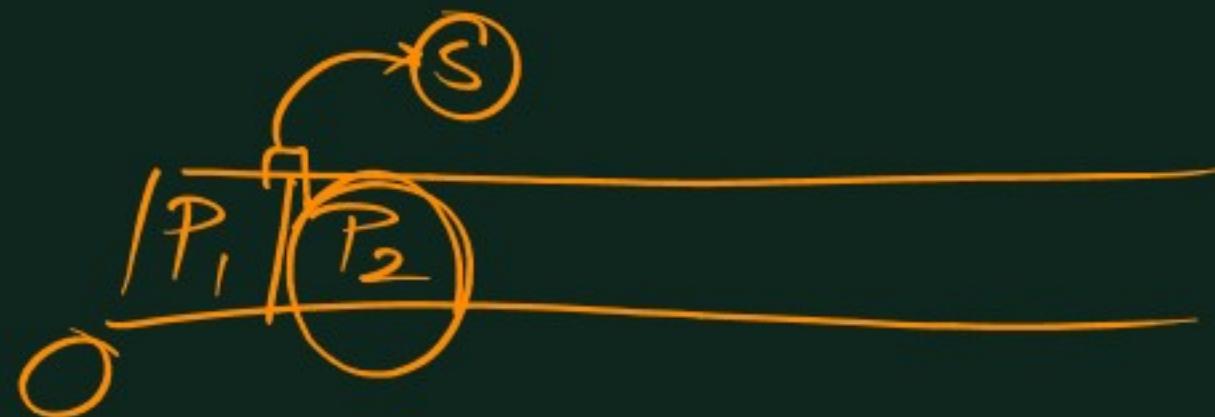
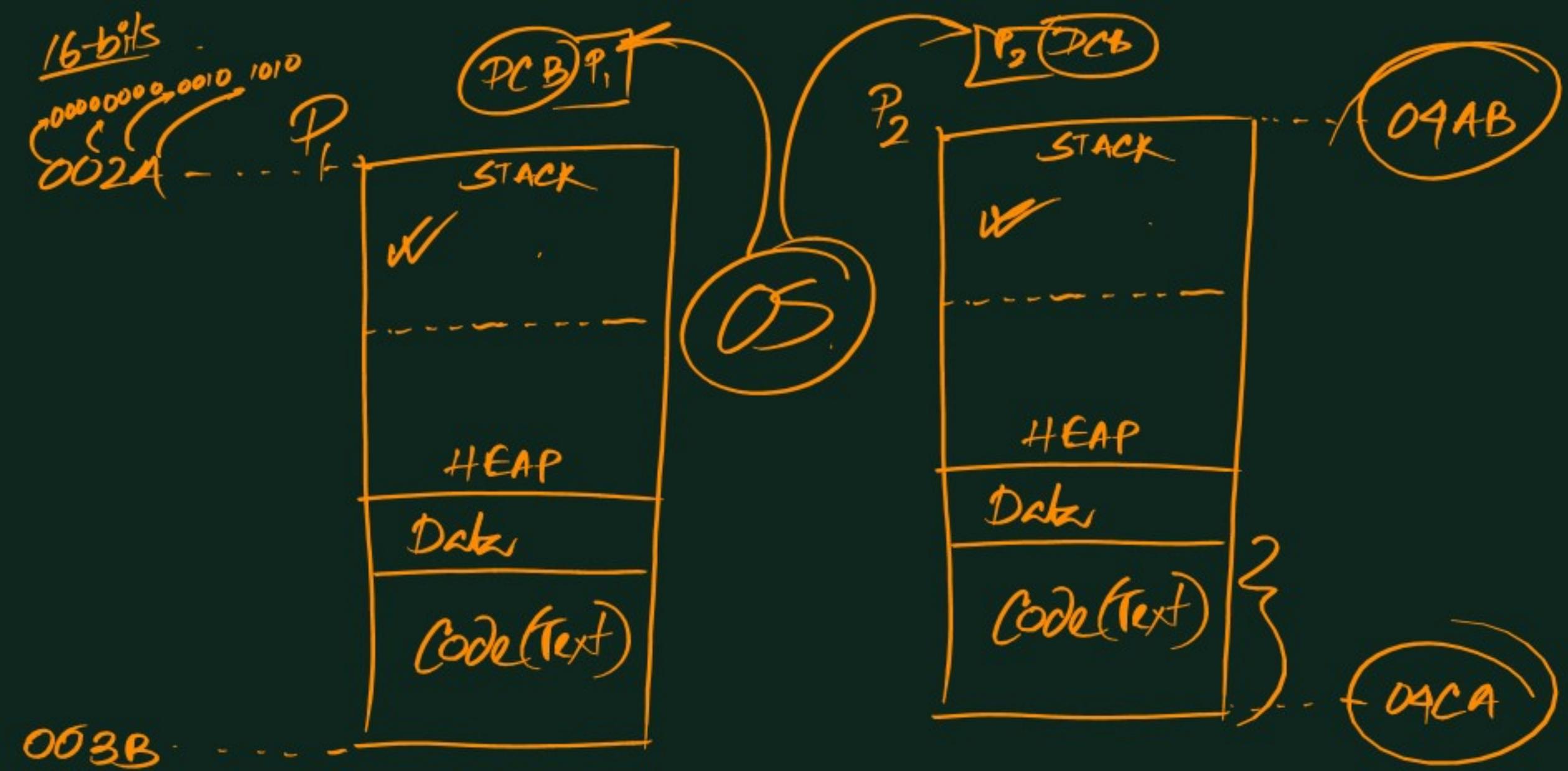
- Context switching can occur due to:
 - I/O operations
 - Expiry of time quantum
 - Each thread has a **Thread Control Block (TCB)**:
 - Similar to a Process Control Block (PCB)
 - Stores thread state information during context switching
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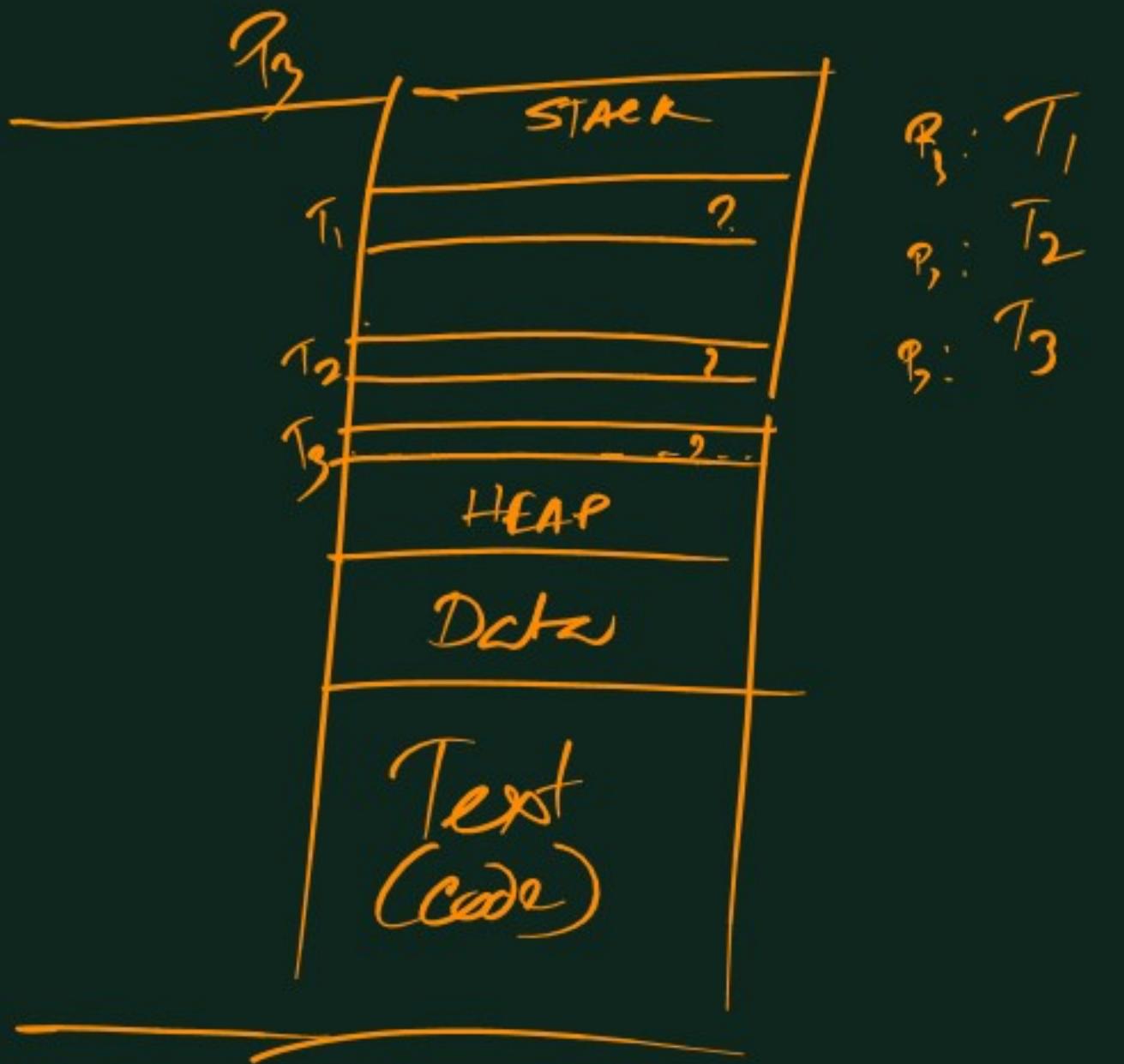
7. Multithreading on a Single-CPU System

- A single-CPU system **does not gain execution speed or throughput** from multithreading
 - Only **one thread executes at a time**
 - However, multithreading **improves responsiveness**, especially for I/O-bound applications
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8. Benefits of Multithreading

- **Responsiveness:** Applications remain active even if one thread is blocked
- **Resource Sharing:** Threads efficiently share process resources
- **Economy:**
 - Thread creation and context switching are cheaper than process creation
 - Process creation requires significant memory and resource allocation
- **Scalability:** Efficient utilization of **multicore and multiprocessor architectures**

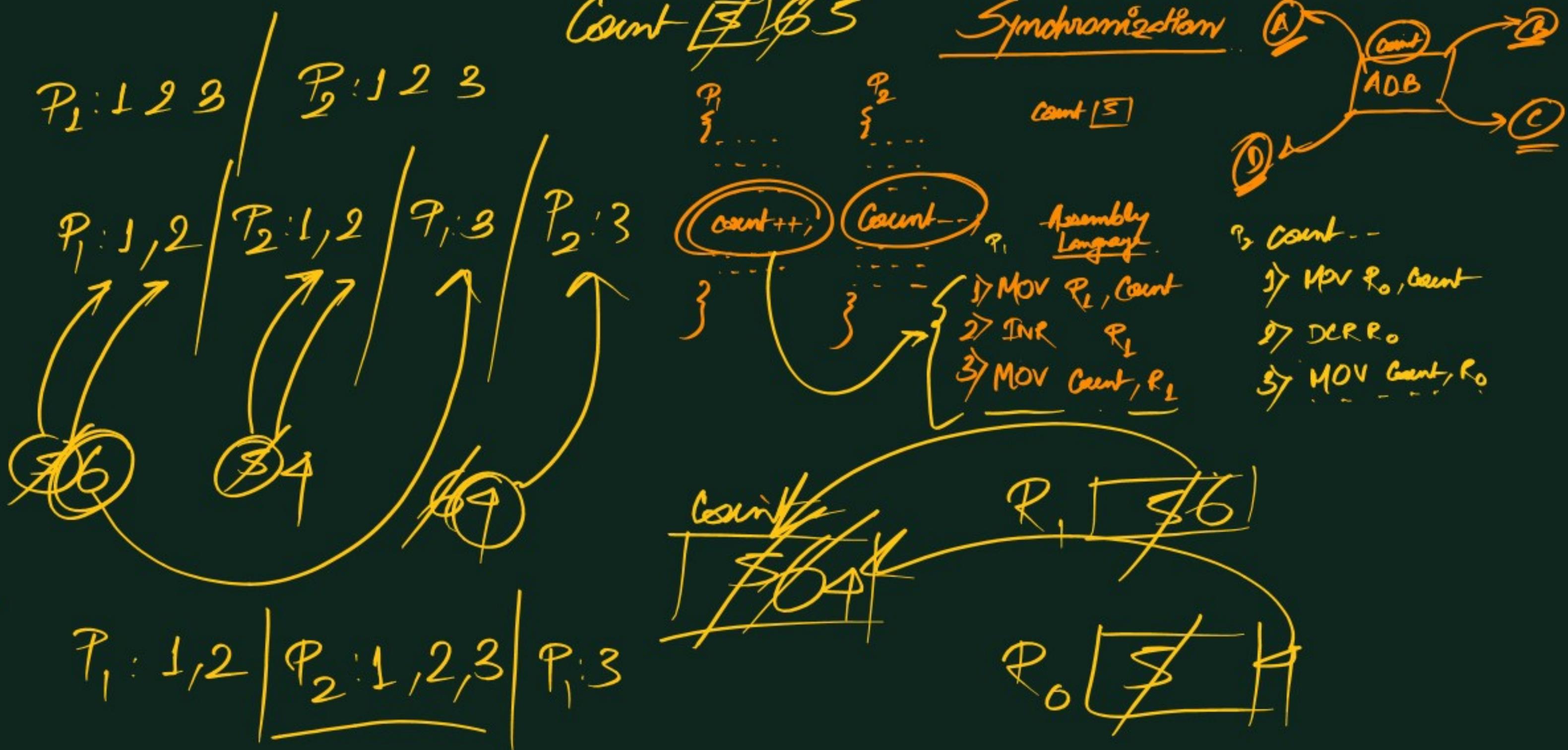


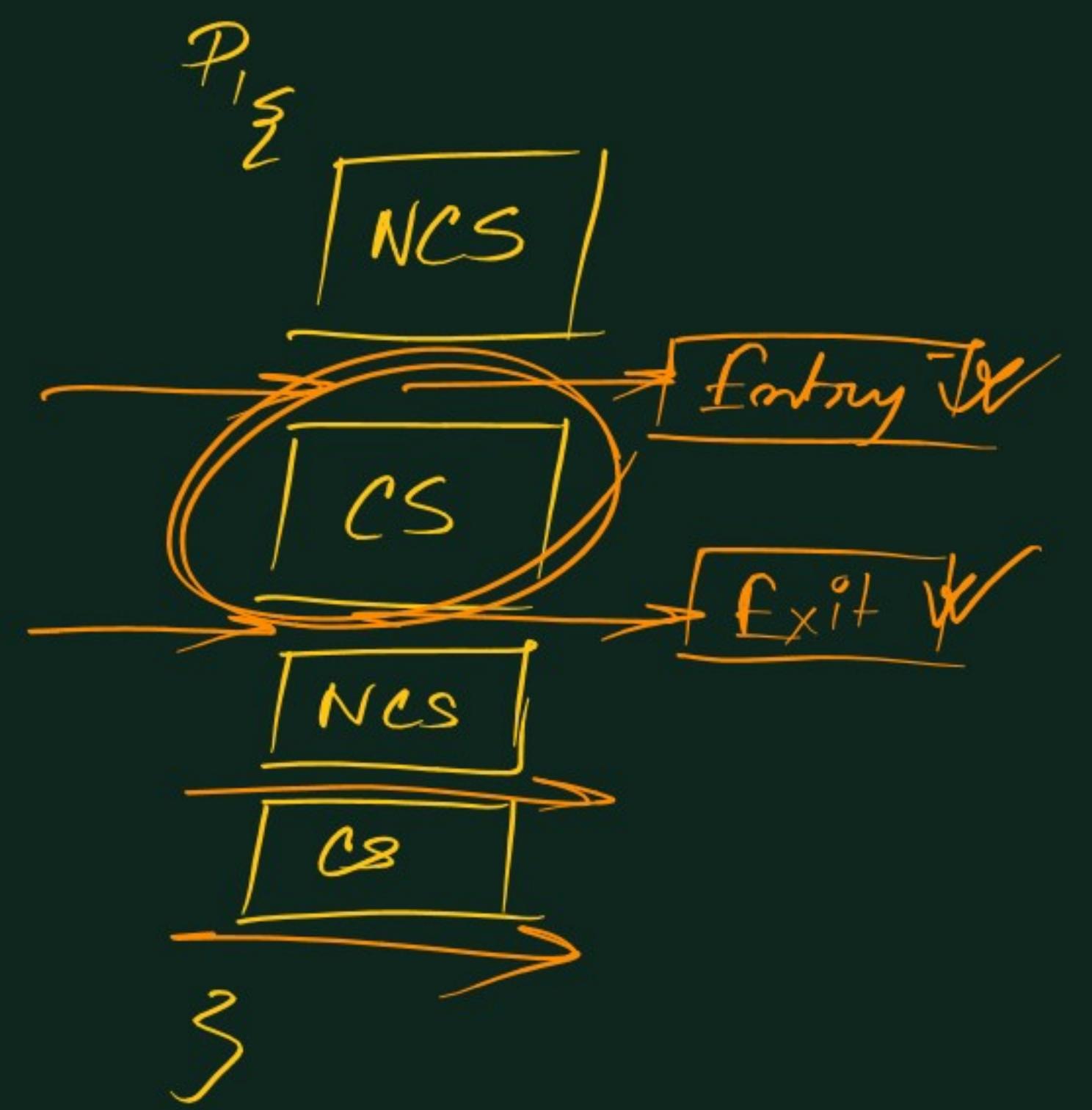


$P_3 : T_3$

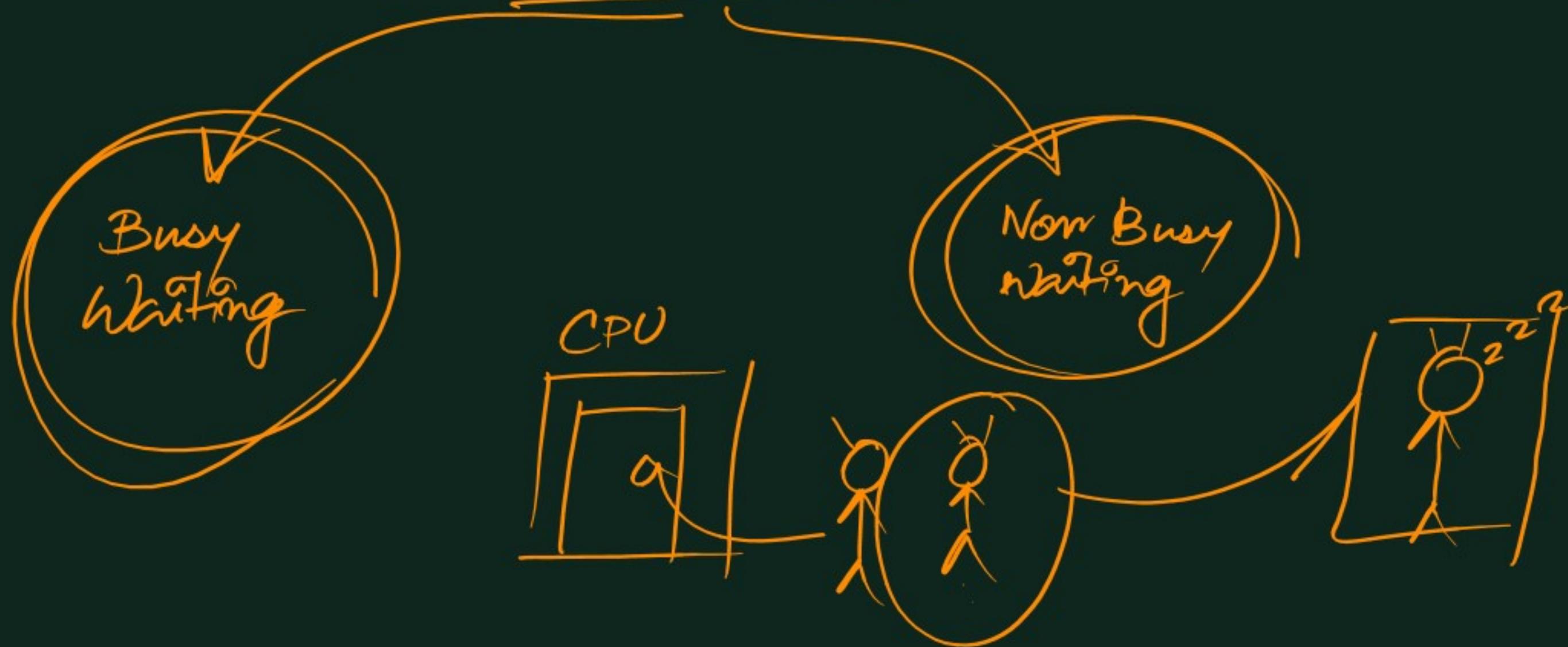
$P_2 : T_2$

$P_1 : T_1$





Synchronization Mechanism



- ↗
- ① Mutual exclusion
 - ② Progress
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- ③ Bounded waiting
 - (N) Non-Preemption / Architectural
Normality
- Optional*

Göteria for
Process synchronization
Mechanism



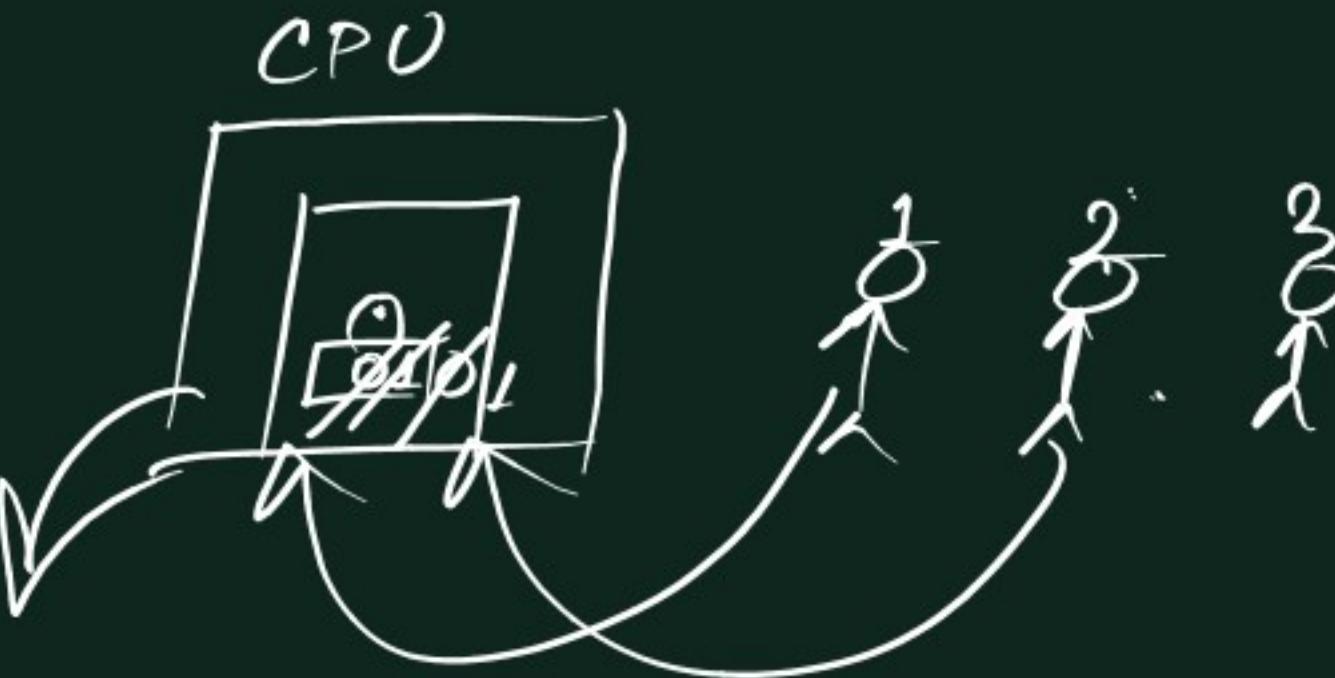
Entry [

while (Lock != 0);

Lock = 1;

Count++; | CS

Exit [Lock = 0;



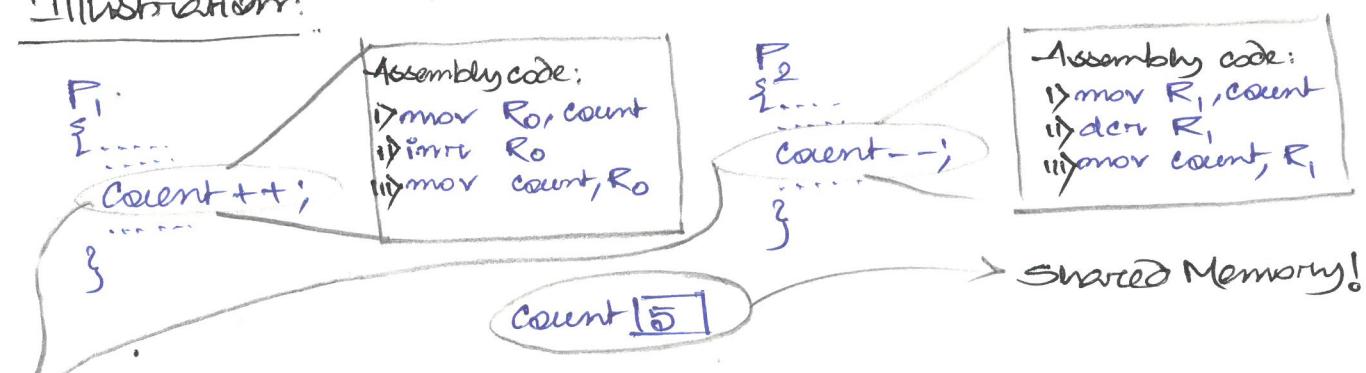
Process Synchronization.

FBR
OS-9✓

In CN, interprocess communication occurs for between 2 processes residing in 2 different computers. Here the communication is handled by protocol stacks (i.e. ISO-OSI, TCP/IP etc.)

In OS, interprocess communication occurs for between 2 processes residing for the same computer. It happens when 2 or more processes try to compete for some resource or they co-operate each other to complete some particular task.

Illustration:



3 situations of execution:

$P_1: 1, 11, 111$	$ $	$P_2: 1, 11, 111$
$P_1: 1, 11$	$ $	$P_2: 1, 11, 111$ $P_1: 111$
$P_1: 1, 11$	$ $	$P_2: 1, 11$ $P_1: 111 P_2: 111$

$Coherent = 5$
$Coherent = 6$
$Coherent = 4$

Race condition
The order of execution of instructions defines the results produced...

① → Preemption

Critical Section:

It is the part of the program where shared resources are accessed by processes.

As a solution to race condition, we modify our code segment by introducing an entry section (before the Critical Section) & an exit section (after the critical section) for every critical section in our code. This is known as Synchronization Mechanism.

Requirements of Synchronization Mechanisms:
(A.K.A. Criteria of solving critical section problem)

Primary: ① Mutual exclusion: Treat the Critical section as dressing room, i.e. only one at a time.

② Progress: Dog (which is not interested in eating grass) should not block the horse to the room full of grass (critical section).

Secondary: ① Bounded waiting: The waiting time for a process before it goes into Critical section should be limited.

② Portability / Architectural neutrality.

Synchronization Mechanisms

with Busy waiting

- A process, seeing another process accessing the critical section will stay busy waiting for the process to come out of the critical section so that it can enter.

without

Busy waiting

Better

- A process, seeing that the critical section is preoccupied will not waste CPU time by waiting, rather it'll go and sleep. Then again it will resume and enter the critical section once the previous process gets out of the critical section.

Lock variable:

- Software mechanism implemented for user mode.
- Busy waiting solution.
- can be used even for more than two processes.

Synchronization
mechanism

entry section

```
1. while (LOCK != 0);  
2. LOCK = 1
```

Critical Section

```
LOCK = 0
```

exit section

Lock = 0 - CS vacant
= 1 - CS occupied

- Kernel mode:
Support needed from OS will have to be taken from OS code, a.k.a. Kernel mode.
- User mode:
Independent user code which doesn't need any support from OS.