






University Of Khartoum

Microcontroller and embedded systems coursework project

Mosque Watch

Prepared by:

-  **Mohammed Salah Ibrahim Abdallah – 144074**
-  **Maeen Abdelbadea NasrAllah – 144096**
-  **Husam Mohammed Abduljabbar – 144024**
-  **Amro kamal Mohammed Abbas - 144048**
-  **Mohammed Ali Abdallah Ahmed - 144080**

27/9/2017

Supervised by Ustaza

Sara Omer Mahjoob

Functional

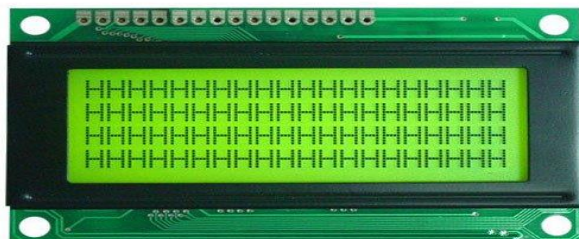
Our project displays the current, Current Date Next prayer's time on a LCD 20x4 chip and Causes a sound alarm for one minute at prayer time. It Keeps time and date even after power failure using a Real Time Clock (RTC) chip.

The prayer times are set to work for Khartoum, Sudan region.

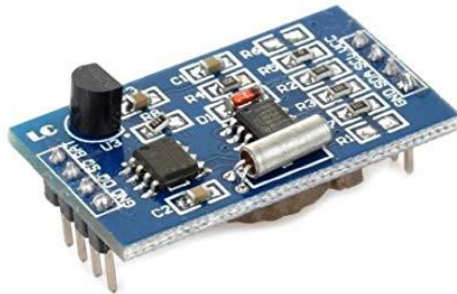
Technical

The hardware components used in this project are:

1. LCD 20x4



2. DS1307(Serial Real Time Clock)



3. Buzzer.



4. AVR ATMEGA32

(XCK/T0) PB0	1	40	PA0 (ADC0)
(T1) PB1	2	39	PA1 (ADC1)
(INT2/AIN0) PB2	3	38	PA2 (ADC2)
(OC0/AIN1) PB3	4	37	PA3 (ADC3)
(SS) PB4	5	36	PA4 (ADC4)
(MOSI) PB5	6	35	PA5 (ADC5)
(MISO) PB6	7	34	PA6 (ADC6)
(SCK) PB7	8	33	PA7 (ADC7)
RESET	9	32	AREF
VCC	10	31	GND
GND	11	30	AVCC
XTAL2	12	29	PC7 (TOSC2)
XTAL1	13	28	PC6 (TOSC1)
(RXD) PD0	14	27	PC5 (TDI)
(TXD) PD1	15	26	PC4 (TDO)
(INT0) PD2	16	25	PC3 (TMS)
(INT1) PD3	17	24	PC2 (TCK)
(OC1B) PD4	18	23	PC1 (SDA)
(OC1A) PD5	19	22	PC0 (SCL)
(ICP1) PD6	20	21	PD7 (OC2)

5. Potentiometer.

6. Bread board.

7. Crystal (quartz Crystal).

Programming for RTC DS1307

Initially, while using RTC first time, we have to set the clock and calendar values, then RTC always keep updating this clock and calendar values.

We will set the RTC clock and calendar values in 1st step and in 2nd step we will read these value.

Step1: Setting Clock and Calendar to RTC DS1307

- In RTC coding, we require first RTC device address (slave address) through which microcontroller wants to communicate with the DS1307.
- DS1307 RTC device address is 0xD0 (given in data sheet).
- Initialize I2C in ATmega16 /32.
- Start I2C communication with device write address i.e. 0xD0.
- If address is matched we get acknowledgement signal.
- Send the Register address of seconds which is 0x00, then send the value of seconds to write in RTC. RTC address gets auto incremented so next, we only have to send the values of minutes, hours, day, date, month and year.
- And then stop the I2C communication.

Step2: Reading Time and Date value from RTC DS1307

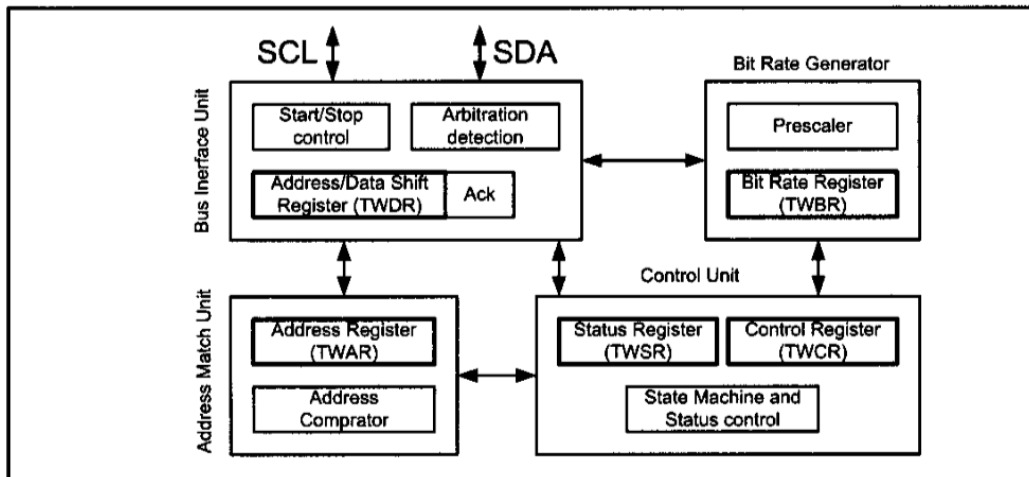
- In the second step we learn how to read the data from the RTC, i.e. second, minute, hours, etc.
- Start the I2C communication with device write address i.e. 0xD0.
- Then write the register value from where we have to read the data (we read from location 00 i.e. read the second).
- Then repeated start I2C with device read address i.e. 0xD1.
- Now Read the data with acknowledgement from location 00.

- For read the last location always read with the negative acknowledgement, then device will understand this is the last data read from the device.
- For read next location of register address will get auto incremented.

First of all the communication between the AVR microcontroller chip and the RTC “real time clock” is established using the i2c communication protocol .

The communication is exchanged through the data & clock pins in the RTC chip.

TWI (I2C) in the AVR chip:



The RTC has 5 registers used in the control & data exchange:

TWI Control Register (TWCR)

TWCR controls the operation of the TWI. In Figure 18-13 you see each bit of TWCR and a short description of it. Here we will describe some of these bits in more detail.

TWINT	TWEA	TWSTA	TWSTO	TWWC	TWEN	-	TWIE
-------	------	-------	-------	------	------	---	------

Bit 7 – TWINT: TWI Interrupt

This bit is set by hardware when the TWI module has finished its current job. If the TWI and general interrupt are enabled, changing TWINT to one will cause the MCU to jump to the TWI interrupt vector. Clearing this flag starts the operation of the TWI. TWINT must be cleared by software.

Bit 6 – TWEA: TWI Enable Acknowledge

Making this bit HIGH will enable the generation of ACK when needed in slave or receiver mode.

Bit 5 – TWSTA: TWI START condition Bit

Making this bit HIGH will generate a START condition if the bus is free; otherwise, the TWI module waits for the bus to become free and then generates a START condition

Bit 4 – TWSTO: TWI STOP condition bit

In master mode, making this bit HIGH causes the TWI to generate a STOP condition. This bit is cleared by hardware when the STOP condition is transmitted.

Bit 3 – TWWC: TWI Write Collision Flag

Bit 2 – TWEN: TWI Enable

Making this bit HIGH enables the TWI module.

Bit 0 – TWIE: TWI Interrupt Enable

Making this bit HIGH enables the TWI interrupt if the general interrupt is enabled.

TWI Data Register (TWDR)

In Receive mode, the last received byte will be in the TWDR, and in Transmit mode, you should write the next byte into TWDR to be transmitted. As we mentioned before, you can access the TWDR only when the TWIE is set to one otherwise collision happens. This means the Data Register cannot be initialized by the user before the first interrupt occurs.

TWI Address Register (TWAR)

TWAR contains the 7-bit slave address to which the TWI will respond when working as slave. The eighth bit (LSB) of TWAR is TWGCE (TWI General Call Recognition Enable). It controls recognition of general call address (00). If this bit is set to one, receiving of a general call address will cause an interrupt request.

TWI Status Register (TWSR)

As you see in Figure 18-12, five bits of TWSR are dedicated to show the status of the TWI logic and bus. Notice that if you read TWSR, you will read both the status bits and the prescaler value. To check the status bits, you should mask the two LSB bits (prescaler values) to zero. In this book we do not list all of the status codes and their meanings, but we will cover some of more common ones. To see the complete list of status register codes, you should refer to the data sheet of the chip. Next we will see how to use these bits when we want to program the AVR to use the TWI module.

Algorithms

Steps to program the AVR in master operating mode:

1_Initzilation:

To initialize the TWI module to operate in master operating mode, we should do the following steps:

1. Set the TWI module clock frequency by setting the values of the TWBR register and the TWPS bus in the TWSR register.

2. Enable the TWI module by setting the TWEN bit in the TWCR register to one.

To start data transfer in master operating mode, we must transmit a START condition. This is done by setting the TWEN, TWSTA ,and TWINT bit of TWCR to one.

***send data:**

To send data:

1. Copy the data byte to the TWDR.
2. Set the TWEN and TWINT bits of the TWCR register to one to start sending the byte.
3. Poll the TWINT flag in the TWCR register to see whether the byte transmitted completely.

***receive data:**

To receive data:

1. Set TWEN and TWINT bits of the TWCR register to one to start receiving a byte.
2. Poll the TWINT flag in the TWCR register to see whether a byte has been received completely.
3. Copy the received byte from the TWDR to another register to save it.

To stop the transfer we must transmit a STOP condition.

Steps to program the AVR in slave operating mode:

In slave mode we can't transmit START or STOP conditions. A slave device should listen to the bus and wait to be addressed by a master device or general call.

Initialization:

1. Set the slave address by setting the values for TWAR registers. The upper seven bits of TWAR are the slave address, and the eight bit is TWGCE.
2. Enable the TWI module by setting the TWEN bit in the TWCR register to one.
3. Set the TWEN, TWINT, and TEA bits of TWCR to one to enable the TWI and acknowledge generation.

After initialization the slave should listen to the bus to detect when it is addressed by a master device . When the TWI module detects its own address on the bus , it returns ACK and then sets the TWINT flag in the TWCR register to one.

***send data:**

1. Copy the data byte to the TWDR .
2. Set the TWEN , TEA, TWINT bits of the TWCR register to one to start sending the byte.
3. Poll the TWINT flag in the TWCR register to see when the byte is completely transmitted.

***receive data:**

After being addressed by a master do the following:

1. Set the TWEN , TEA, TWINT bits of the TWCR register to one to start sending the byte.

2. Poll the TWINT flag in the TWCR register to see when the byte is received completely.
3. Copy the received byte from the TWDR to another register to save it.

Now from the LCD datasheet we have the instruction table:

Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	Execution Time (Max)
Clear Display	0	0	0	0	0	0	0	0	0	1	Clears entire display and sets DD RAM address 0 in address counter.	1.64 ms
Return Home	0	0	0	0	0	0	0	0	0	1 -	Sets DD RAM address 0 as address counter. Also returns display being shifted to original position. DD RAM contents remain unchanged.	1.64 ms
Entry Mode Set	0	0	0	0	0	0	0	1	1/D	S	Sets cursor move direction and specifies shift of display. These operations are performed during data write and read.	40 μ s
Display On/Off Control	0	0	0	0	0	0	0	1	D	C B	Sets On/Off of entire display (D), cursor On/Off (C), and blink of cursor position character (B).	40 μ s
Cursor or Display Shift	0	0	0	0	0	0	1	S/C	R/L	- -	Moves cursor and shifts display without changing DD RAM contents.	40 μ s
Function Set	0	0	0	0	0	1	DL	N	F	- -	Sets interface data length (DL), number of display lines (L), and character font (F).	40 μ s
Set CG RAM Address	0	0	0	1						AGC	Sets CG RAM address. CG RAM data is sent and received after this setting.	40 μ s
Set DD RAM Address	0	0	1							ADD	Sets DD RAM address. DD RAM data is sent and received after this setting.	40 μ s
Read Busy Flag & Address	0	1	BF							AC	Reads Busy flag (BF) indicating internal operation is being performed and reads address counter contents.	40 μ s
Write Data CG or DD RAM	1	0								Write Data	Writes data into DD or CG RAM.	40 μ s
Read Data CG or DD RAM	1	1								Read Data	Reads data from DD or CG RAM.	40 μ s

And we have address ranges for the LCD 20x4 as:

20 × 4 LCD	Line 1:	80	81	82	83	through 93
	Line 2:	C0	C1	C2	C3	through D3
	Line 3:	94	95	96	97	through A7
	Line 4:	D4	D5	D6	D7	through E7

***Note:**

That the LCD receives one character at time but the TRC gets the time as multiple characters so we are using an unpacked BCD exchange as shown in the code.

The algorithms for sending commands and data to LCD:

We are using 4-bit mode to save bits, we send the high nibbles first then the lower nibbles ; the data got from the RTC is in BCD formula so we have a conversion function that:

- Get the number In BCD from RTC
- Obtains the higher nibble by anding with 0xf0
- Then adds with 30 to convert it to ascii so the LCD could show it correctly
- Do the same but for the lowest nibble

We have PORTA and PORTB as outputs; specifically pins 4,5,6,7 for the data transfer in PORTA. And 5, 6, and 7 for RS, RW and EN respectively in PORTB

We first initialize the LCD:

We put in the 4-bit mode, the following sequence of commands should be sent to the LCD: 33, 32 and 28 in hex to tell the LCD to go into 4-bit mode

To send commands:

We make pins RS and R/W = 0 and put the command number on the data pins. Then we send a high to low pulse to the EN pin to enable the internal latch of the LCD. After each command we set a delay of 100 microseconds to let the LCD module run command.

To send data to the LCD:

We make the pins RS = 1 and R/W = 0. Then we put the data on the data pins and send a high to low pulse to the EN pin to enable the internal latch of the LCD. We set a delay of 100 microseconds to the LCD module write the data on the screen.

To know which the next prayer is:

The function take the current time as parameters, then it has five comparisons between each two prayers to define which is next.

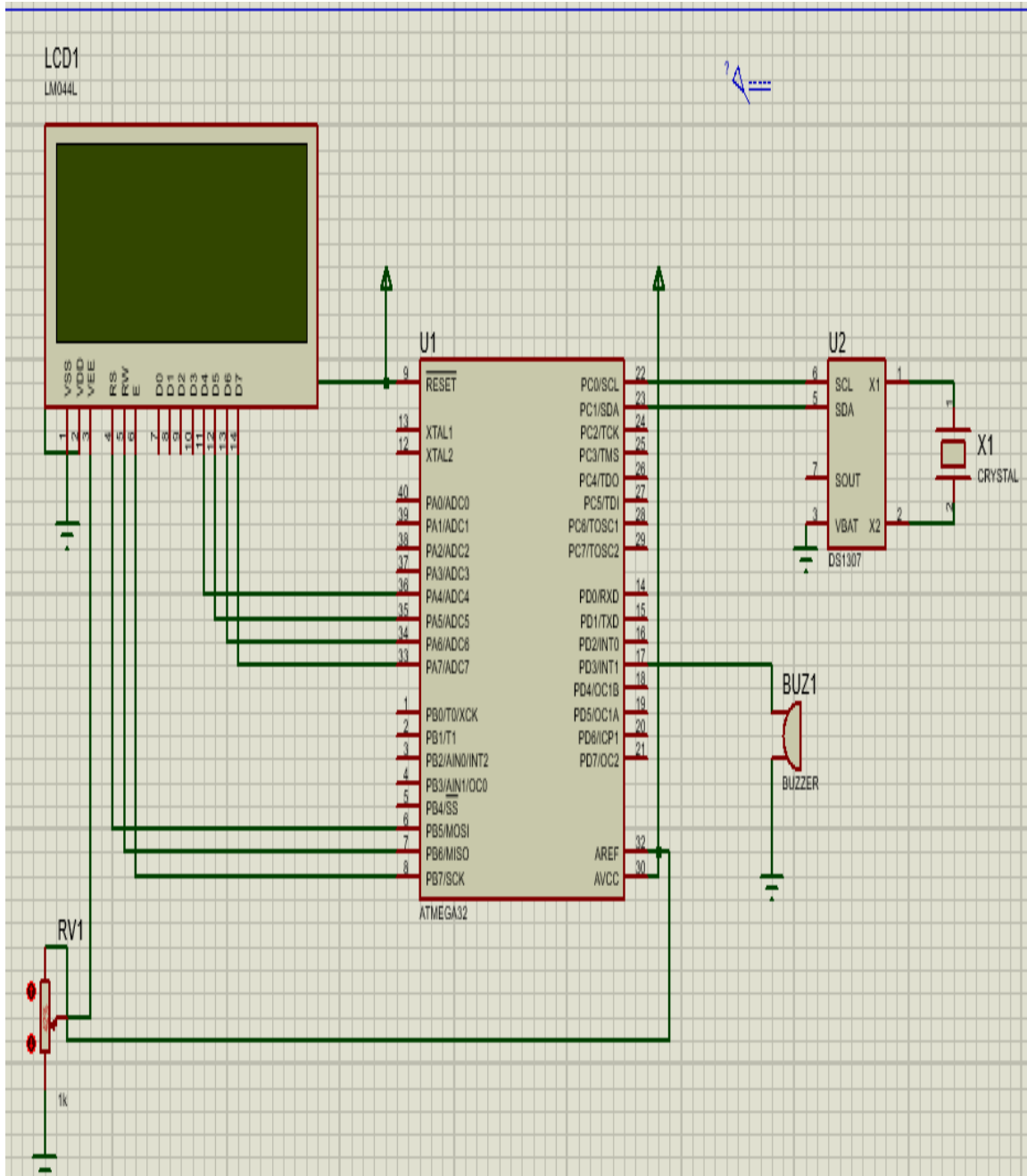
It firstly compares the hours between two hours at a time, if the minutes is less the next prayer is the last if greater it goes to the prayer after the one being compared.

And if the hours is less than the compared then the compared is the next prayer.

The Alarm:

It keeps on comparing the current time with 5 conditional ifs if antmatch it sets the buzzer until the minute is over.

Hardware Design and outline



Code

```
1  #define F_CPU 1000000UL
2
3  #include <inttypes.h>
4  #include <avr/io.h>
5  #include <avr/interrupt.h>
6  #include <util/delay.h>
7
8
9  #define LCD_DataPRT PORTA
10 #define LCD_DataDDR DDRA
11 #define LCD_RS 5
12 #define LCD_RW 6
13 #define LCD_EN 7
14 #define LCD_SETUPRT PORTB
15 #define LCD_SETUPDRPRT DDRB
16
17
18 /////////////// prayer's time ///////////////////
19 unsigned char FAJR_H=0x04;
20 //unsigned char FAJR_HH='0';
21 //unsigned char FAJR_ML='0';
22 unsigned char FAJR_M=0x30;
23
24 unsigned char DOHR_H=0x12;
25 //unsigned char DOHR_HL='2';
26 //unsigned char DOHR_MH='0';
27 unsigned char DOHR_M=0x00;
28
29 unsigned char ASR_H=0x16;
30 //unsigned char ASR_HH='1';
31 //unsigned char ASR_ML='0';
32 unsigned char ASR_M=0x00;
33
34 unsigned char MAG_H=0x17;
```

```

35 //unsigned char MAG_HL='7';
36 //unsigned char MAG_MH='0';
37 unsigned char MAG_M=0x050;
38
39 unsigned char ESHA_H=0x19;
40 //unsigned char ESHA_HL='9';
41 //unsigned char ESHA_MH='0';
42 unsigned char ESHA_M=0x00;
43
44
45
46 □ void i2c_init (void){
47     TWSR = 0x00;
48     TWBR = 0x47;
49     TWCR = 0x04;
50 }
51
52 void i2c_start()
53 □{
54     TWCR = (1<<TWINT) | (1<<TWSTA) | (1<<TWEN);
55     while(!(TWCR & (1<<TWINT)));
56 }
57
58 □ void i2c_write(unsigned char data){
59     TWDR = data;
60     TWCR = (1<<TWINT) | (1<<TWEN);
61     while(!(TWCR & (1<<TWINT)));
62 }
63 □ unsigned char i2c_read (unsigned char ackVal){
64     TWCR = (1<<TWINT) | (1<<TWEN) | (ackVal<<TWEA);
65     while(!(TWCR & (1<<TWINT)));
66     return TWDR;
67 }

```

```

68
69 void i2c_stop () {
70     TWCR = (1<<TWINT) | (1<<TWEN) | (1<<TWSTO);
71     int k;
72     for (k = 0; k<100; k++);
73 }
74
75 void rtc_init() {
76     i2c_init();    //initialize I2C module
77     i2c_start();   //transmit START condition
78     i2c_write(0xD0); //address DS1307 for write
79     i2c_write(0x07); //set register pointer to 7
80     i2c_write(0x00); //set value of location 7 to 0
81     i2c_stop();   //transmit STOP condition
82
83 }
84
85 void rtc_setTime(unsigned char h, unsigned char m, unsigned char s) {
86     i2c_start();   //transmit START condition
87     i2c_write(0xD0); //address DS1307 for write
88     i2c_write(0);   //set register pointer to 0
89     i2c_write(s);   //set seconds
90     i2c_write(m);   //set minutes
91     i2c_write(h);   //set hour
92     i2c_stop();   //transmit STOP condition
93 }
94
95 void rtc_setDate(unsigned char y, unsigned char m, unsigned char d) {
96     i2c_start();   //transmit START condition
97     i2c_write(0xD0); //address DS1307 for write
98     i2c_write(0x04); //set register pointer to 0
99     i2c_write(d);   //set seconds
100    i2c_write(m);   //set minutes
101    i2c_write(y);   //set hour

```



```

102     i2c_stop();    //transmit STOP condition
103 }
104
105 void rtc_getTime(unsigned char *h,unsigned char *m,unsigned char *s){
106     i2c_start();    //transmit START condition
107     i2c_write(0xD0); //address DS1307 for write
108     i2c_write(0);    //set register pointer to 0
109     i2c_stop();    //transmit STOP condition
110
111     i2c_start();    //transmit START condition
112     i2c_write(0xD1); //address DS1307 for write
113     *h = i2c_read(1); //read second, return ACK
114     *m = i2c_read(1); //read minute, return ACK
115     *s = i2c_read(0); //read hour, return ACK
116     i2c_stop();    //transmit STOP condition
117 }
118
119 void rtc_getDate(unsigned char *y,unsigned char *m,unsigned char *d){
120     i2c_start();    //transmit START condition
121     i2c_write(0xD0); //address DS1307 for write
122     i2c_write(0x04); //set register pointer to 0
123     i2c_stop();    //transmit STOP condition
124
125     i2c_start();    //transmit START condition
126     i2c_write(0xD1); //address DS1307 for write
127     *d = i2c_read(1); //read day, return ACK
128     *m = i2c_read(1); //read month, return ACK
129     *y = i2c_read(0); //set year, return NACK
130     i2c_stop();    //transmit STOP condition
131 }
132
133 /*char * get_ASCII(unsigned char data){
134     static char r[2] ;
135     r[0] = '0' + (data>>4);

```

```

135     r[0] = '0' + (data>>4);
136     r[1]= '0' + (data & 0xF);
137
138     return r;
139
140     */
141
142
143     ////////////////////////////////////////lcd
144
145 void Send_LCD_Com(unsigned char Command){
146     LCD_DataPRT=Command & 0xF0;
147     LCD_SETPRT &=~(1<<LCD_RS);           /*RS=0 for command    */
148     LCD_SETPRT &=~(1<<LCD_RW);           /*RW=0 for write      */
149     LCD_SETPRT |= (1<<LCD_EN);           /*EN=1                */
150     _delay_us(1);
151     LCD_SETPRT &=~(1<<LCD_EN);           /*EN=0                */
152     _delay_us(100);
153
154     LCD_DataPRT=Command<<4;
155     LCD_SETPRT |= (1<<LCD_EN);           /*EN=1                */
156     _delay_us(1);
157     LCD_SETPRT &=~(1<<LCD_EN);           /*EN=0                */
158     _delay_us(100);
159
160 }
161
162 void LCD_Init(){
163     LCD_DataADDR=0xFF;
164     LCD_SETDDRPRT=0xFF;
165
166     LCD_SETPRT &=~(1<<LCD_EN);
167     _delay_us(2000);
168     Send_LCD_Com(0x33);

```

```

169         Send_LCD_Com(0x32);
170         Send_LCD_Com(0x28);
171         Send_LCD_Com(0x0C);
172         Send_LCD_Com(0x01);
173         _delay_us(2000);
174         Send_LCD_Com(0x06);
175     }
176
177     void Send_DATA_TO_LCD(unsigned char data){
178         LCD_DataPRT=data & 0xF0;
179
180         LCD_SETPRT |=(1<<LCD_RS);           /*RS=0 for command    */
181         LCD_SETPRT &=~(1<<LCD_RW);          /*RW=0 for write       */
182         LCD_SETPRT |=(1<<LCD_EN);           /*EN=1                 */
183         _delay_us(1);
184         LCD_SETPRT &=~(1<<LCD_EN);          /*EN=0                 */
185         _delay_us(100);
186
187         LCD_DataPRT=data<<4;
188         LCD_SETPRT |=(1<<LCD_EN);           /*EN=1                 */
189         _delay_us(1);
190         LCD_SETPRT &=~(1<<LCD_EN);          /*EN=0                 */
191         _delay_us(100);
192
193     }
194
195
196     void lcd_gotoxy (unsigned char x , unsigned char y)
197     {
198         unsigned char firstCharAdr[] = {0x80, 0xC0, 0x94 , 0xD4};
199         Send_LCD_Com(firstCharAdr[y-1] + x - 1);
200         _delay_us(100);
201     }
202

```

```

203 void LCD_Print(char* str){
204     unsigned char i=0;
205     while (str[i]!=0)
206     {
207
208         Send_DATA_TO_LCD(str[i]);
209         i++;
210     }
211
212 }
213 //////////////////////////////////////////////////main
214
215 void show_Time(unsigned char h,unsigned char m,unsigned char s){
216     unsigned sh=(s>>4)+'0';
217     unsigned sl=(s & 0x0f)+'0';
218
219     unsigned mh=(m>>4)+'0';
220     unsigned ml=(m & 0x0f)+'0';
221
222     unsigned hh=(h>>4)+'0';
223     unsigned hl=(h & 0x0f)+'0';
224
225     Send_DATA_TO_LCD(hh);
226     Send_DATA_TO_LCD(hl);
227     Send_DATA_TO_LCD(':');
228     Send_DATA_TO_LCD(mh);
229     Send_DATA_TO_LCD(ml);
230     Send_DATA_TO_LCD(':');
231     Send_DATA_TO_LCD(sh);
232     Send_DATA_TO_LCD(sl);
233 }
234
235 void show_Date(unsigned char d,unsigned char mon,unsigned char y){
236     unsigned dh=(d>>4)+'0';

```

```

237     unsigned dl=(d & 0x0f)+'0';
238
239     unsigned monh=(mon>>4)+'0';
240     unsigned monl=(mon & 0x0f)+'0';
241
242     unsigned yh=(y>>4)+'0';
243     unsigned yl=(y & 0x0f)+'0';
244
245
246     LCD_Print("20");
247     Send_DATA_TO_LCD(dh);
248     Send_DATA_TO_LCD(dl);
249     Send_DATA_TO_LCD(':');
250     Send_DATA_TO_LCD(monh);
251     Send_DATA_TO_LCD(monl);
252     Send_DATA_TO_LCD(':');
253     Send_DATA_TO_LCD(yh);
254     Send_DATA_TO_LCD(yl);
255
256 }
257
258
259 int next_Prayer_Time(unsigned char h,unsigned char m){
260     if(h>=ESHA_H || h<=FAJR_H){
261         if(h==FAJR_H && m<FAJR_M) return 1;
262         else if(h==FAJR_H && m>FAJR_M) return 2;
263         else if(h<FAJR_H || h>ESHA_H) return 1;}
264
265     if(h>=FAJR_H && h<=DOHR_H ){
266         if(h==DOHR_H && m<DOHR_M) return 2;
267         else if(h==DOHR_H && m>DOHR_M) return 3;
268         else if(h<DOHR_H) return 2;}
269
270     if(h>=DOHR_H && h<=ASR_H ) {

```

```

271         if(h==ASR_H && m<ASR_M) return 3;
272         else if(h==ASR_H && m>ASR_M ) return 4;
273                 else if(h<ASR_H) return 3;}
274
275 □ if(h>=ASR_H && h<=MAG_H ) {
276         if(h=MAG_H && m<MAG_M) return 4;
277         else if(h=MAG_H && m>MAG_M) return 5;
278                 else if(h<MAG_H) return 4;}
279
280 □ if(h>=MAG_H && h<=ESHA_H){
281         if(h==ESHA_H && m<ESHA_M) return 5;
282         if(h==ESHA_H && m>ESHA_M) return 1;
283                 if(h<ESHA_H) return 5;
284         }
285
286
287
288 }
289
290 □ void alaram(unsigned char h,unsigned char m){
291 □     if(h==FAJR_H && m==FAJR_M){
292         PORTD |= 0x08;}
293 □     else if(h==DOHR_H && m==DOHR_M) {
294         PORTD |= 0x08;}
295 □     else if(h==ASR_H && m==ASR_M) {
296         PORTD |= 0x08;}
297 □     else if(h==MAG_H && MAG_M) {
298         PORTD |= 0x08;}
299 □     else if(h==ESHA_H && m==ESHA_M) {
300         PORTD |= 0x08;}
301         else PORTD &= 0xF7;
302     }
303 □ void buzzer_config(){
304     DDRD |= 0x08; //PD3 as output

```

```
305     PORTD &= 0xF7; // turn off buzzer
306 }
307
308 int main()
309 {
310     buzzer_config();
311
312     LCD_Init();
313
314     unsigned char s,m,h;
315     unsigned char d,mon,y;
316
317     rtc_init();
318     rtc_setTime(0x18,0x59,0x55);
319     rtc_setDate(0x18,0x9,0x20);
320
321
322
323
324     while (1){
325
326         rtc_getTime(&s,&m,&h);
327         rtc_getDate(&d,&mon,&y);
328
329         alaram(h,m);
330
331
332         lcd_gotoxy(1 , 1);
333         LCD_Print("TIME ");
334         show_Time(h,m,s);
335
336         lcd_gotoxy(1 , 2);
337         LCD_Print("DATE ");
338         show_Date(d,mon,y);
```

```

339
340
341
342 int npt;
343 npt=next_Prayer_Time(h,m);
344 lcd_gotoxy(1,3);
345 LCD_Print("NEXT PRAYER ");
346
347
348 //lcd_gotoxy(1,4);
349 switch (npt)
350 {
351     case 1:
352         show_Time(FAJR_H,FAJR_M,0x00);
353         break;
354     case 2:
355         show_Time(DOHR_H,DOHR_M,0x00);
356         break;
357     case 3:
358         show_Time(ASR_H,ASR_M,0x00);
359         break;
360     case 4:
361         show_Time(MAG_H,MAG_M,0x00);
362         break;
363     case 5:
364         show_Time(ESHA_H,ESHA_M,0x00);
365         break;
366
367 }
368
369
370
371 }
372
373 return 0;
374 }

```