



ADVANCED MICROCONTROLLER PROJECT COMPETITION

TOPIC:-GLCD Interfacing using LPC2148



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Brief Information about the project

- Interfacing LED with LPC2148
- Flash a LED using the LPC2148 Development Board. It works by turning ON a LED & then turning it OFF & then looping back to START. However, the operating speed of the microcontroller is very high so the flashing frequency will also be very fast to be detected by human eye.

COMPONENTS:-

1.LPC 2148

2. Connecting Wires

3. KEIL Software version4

4. GLCD Display



DESCRIPTION

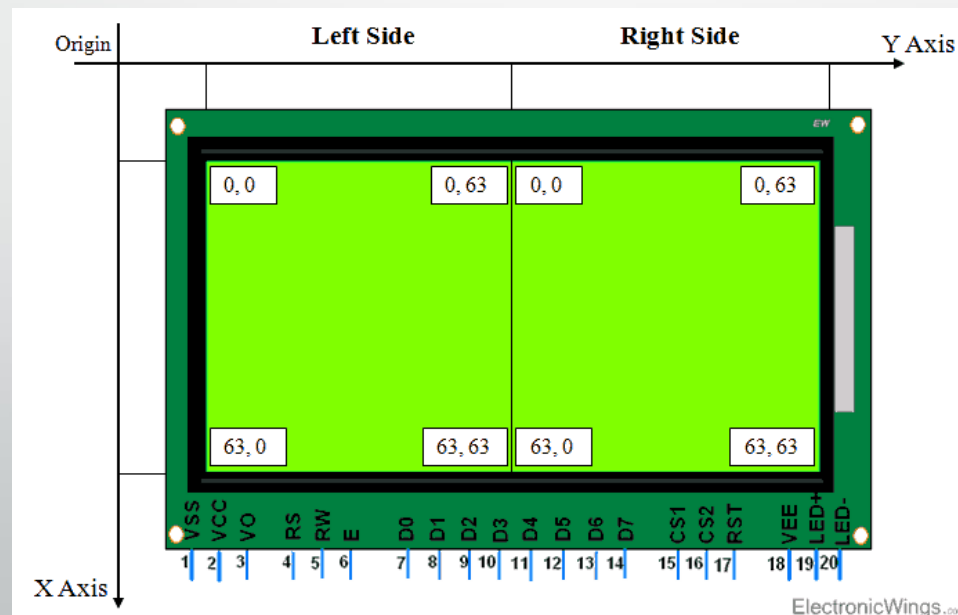
- **LPC2148:-** The LPC2148 microcontroller is designed by Philips (NXP Semiconductor) with several in-built features & peripherals. Due to these reasons, it will make more reliable as well as the efficient option for an application developer. LPC2148 is a 16-bit or 32-bit microcontroller based on ARM7 family.



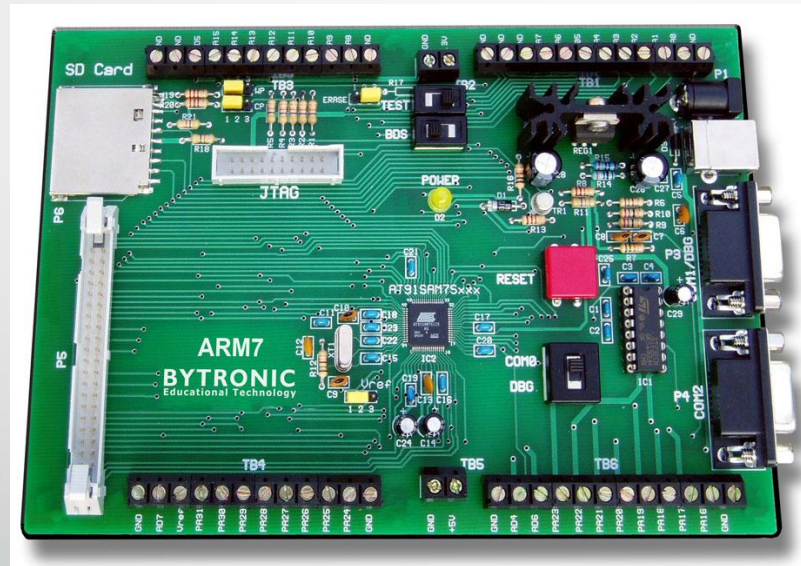
- **Features of LPC2148**

- The main features of LPC2148 include the following.
- The LPC2148 is a 16 bit or 32 bit ARM7 family based microcontroller and available in a small LQFP64 package.
- ISP (in system programming) or IAP (in application programming) using on-chip boot loader software.
- On-chip static RAM is 8 kB-40 kB, on-chip flash memory is 32 kB-512 kB, the wide interface is 128 bit, or accelerator allows 60 MHz high-speed operation.
- It takes 400 milliseconds time for erasing the data in full chip and 1 millisecond time for 256 bytes of programming.
- Embedded Trace interfaces and Embedded ICE RT offers real-time debugging with high-speed tracing of instruction execution and on-chip Real Monitor software.
- It has 2 kB of endpoint RAM and USB 2.0 full speed device controller. Furthermore, this microcontroller offers 8kB on-chip RAM nearby to USB with DMA.
- One or two 10-bit ADCs offer 6 or 14 analogs i/ps with low conversion time as 2.44 μ s/ channel.
- Only 10 bit DAC offers changeable analog o/p.
- External event counter/32 bit timers-2, PWM unit, & watchdog.
- Low power RTC (real time clock) & 32 kHz clock input.

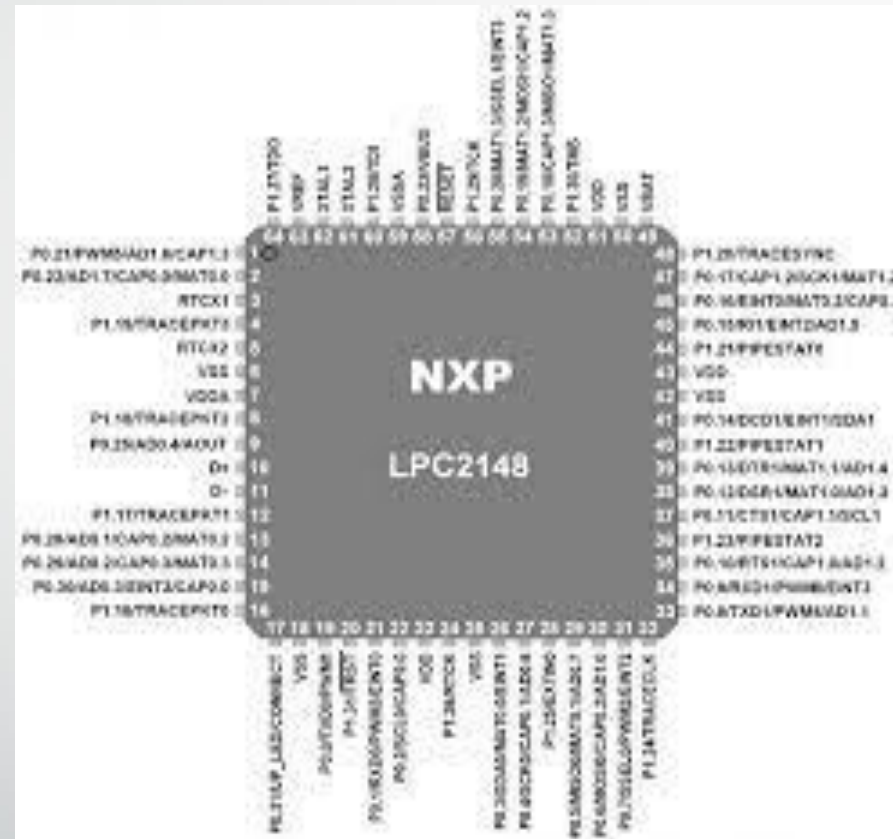
- **GLCD Display:-** Graphical LCD has 128x64 pixels. It has 128 columns and 64 row segments.
It uses two display segment drivers. Since segment driver has 64 channel, GLCD module contains two segment drivers to drive 128 column segments.
- It has one common driver which drives 64 row segments as well as it generates clock and control signals for two segment drivers



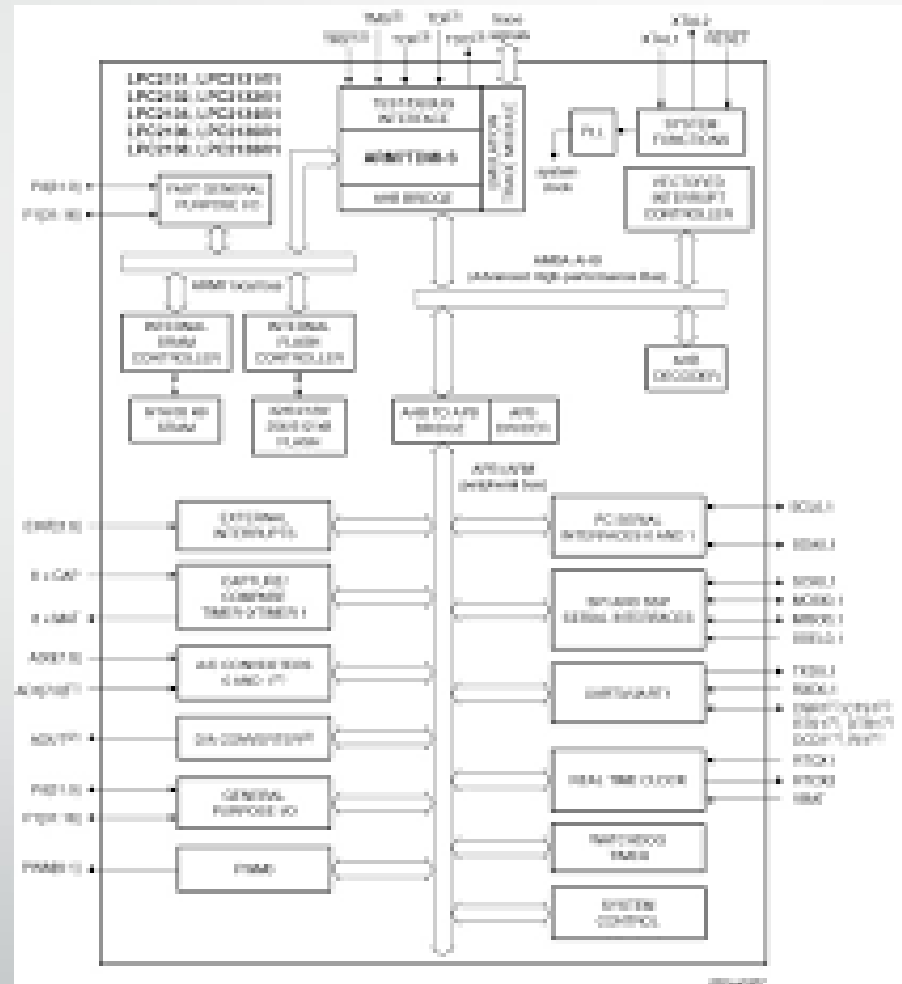
- **ARM 7:-**ARM7 processor is commonly used in embedded system applications. Also, it is a balance among classic as well as new-Cortex sequence. This processor is tremendous in finding the resources existing on the internet with excellence documentation offered by NXP Semiconductors. It suits completely for an apprentice to obtain in detail hardware & software design implementation.



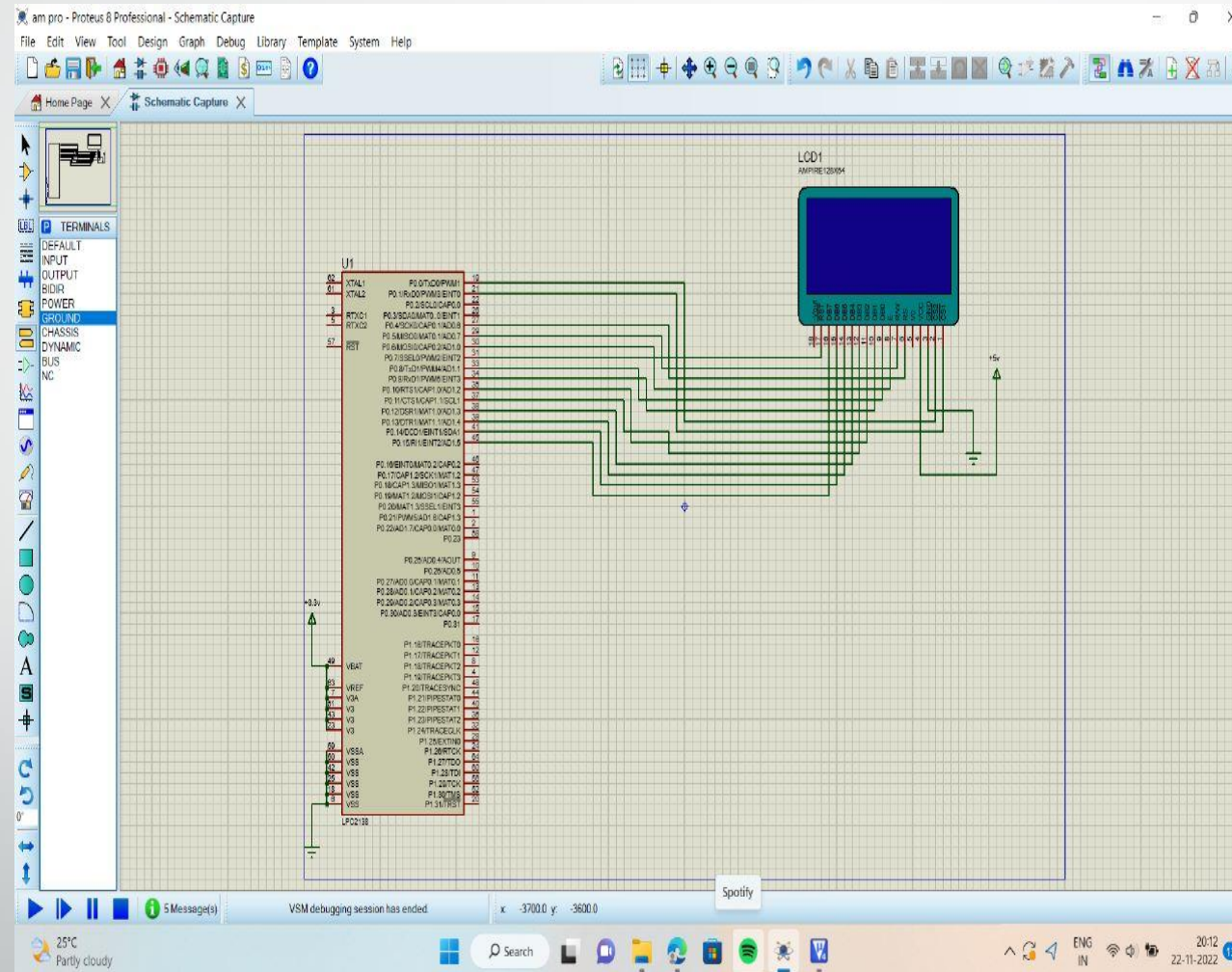
PIN DIAGRAM OF LPC2148



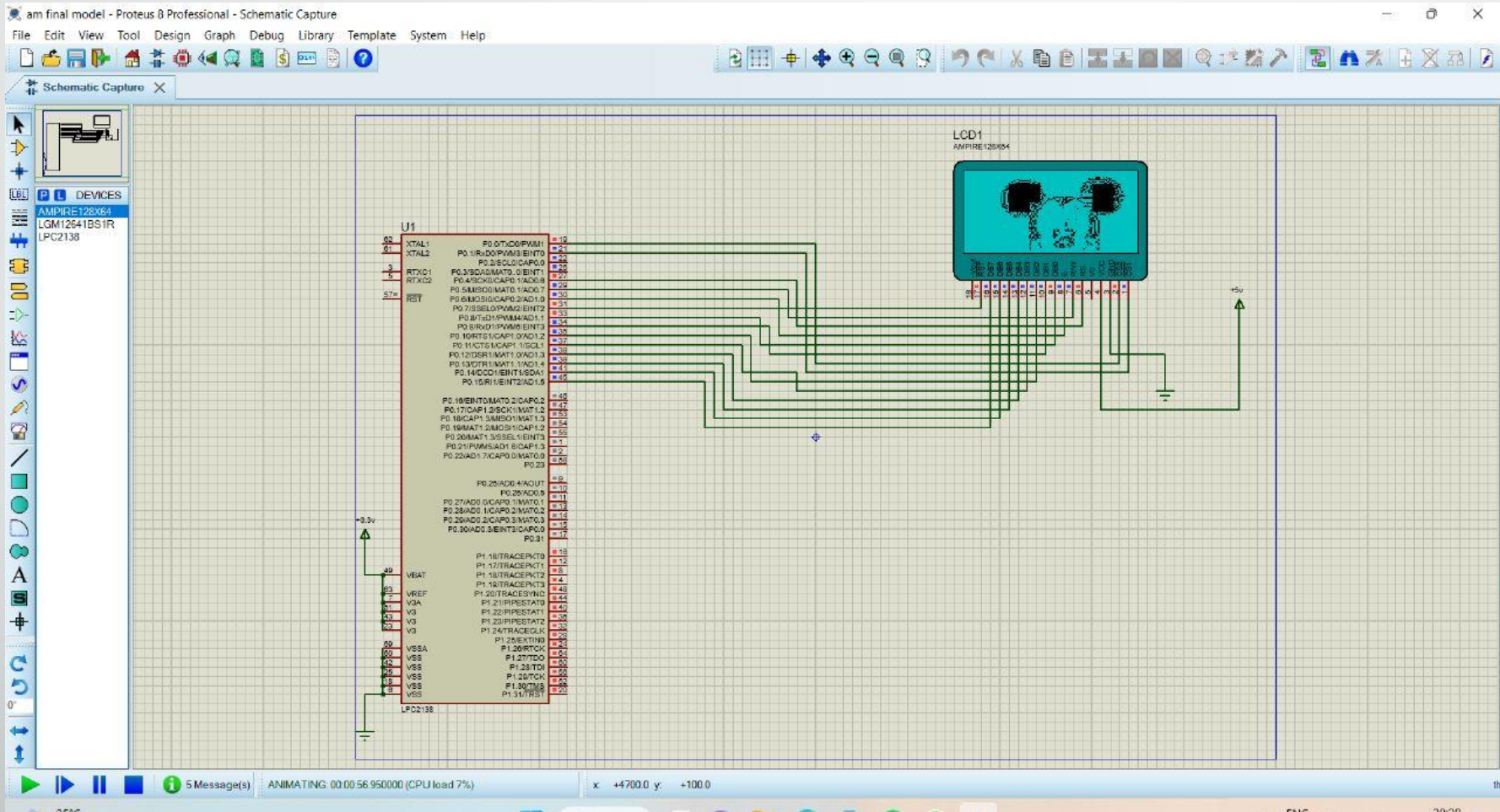
BLOCK DIAGRAM OF LPC2148



CIRCUIT DIAGRAM



OUTPUT



REFERENCES

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THANK YOU