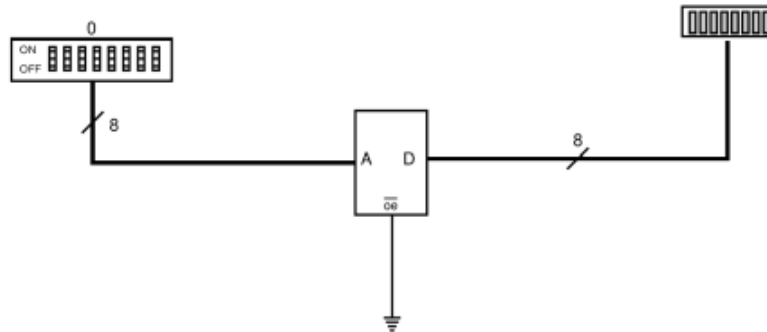# The LNM Institute of Information Technology, Jaipur

Computer Organization and Architecture

Lab Assignment: CPU Simulation using TkGate

1. **Part 1: Simple module design**

   Design and simulate given ROM circuit with memory file (test.mem) file.

Assignment

2. **Part 2:**

   Instruction execution: Design an instruction execution system by modifying previous circuit, where instruction are stored in ROM and memory file.