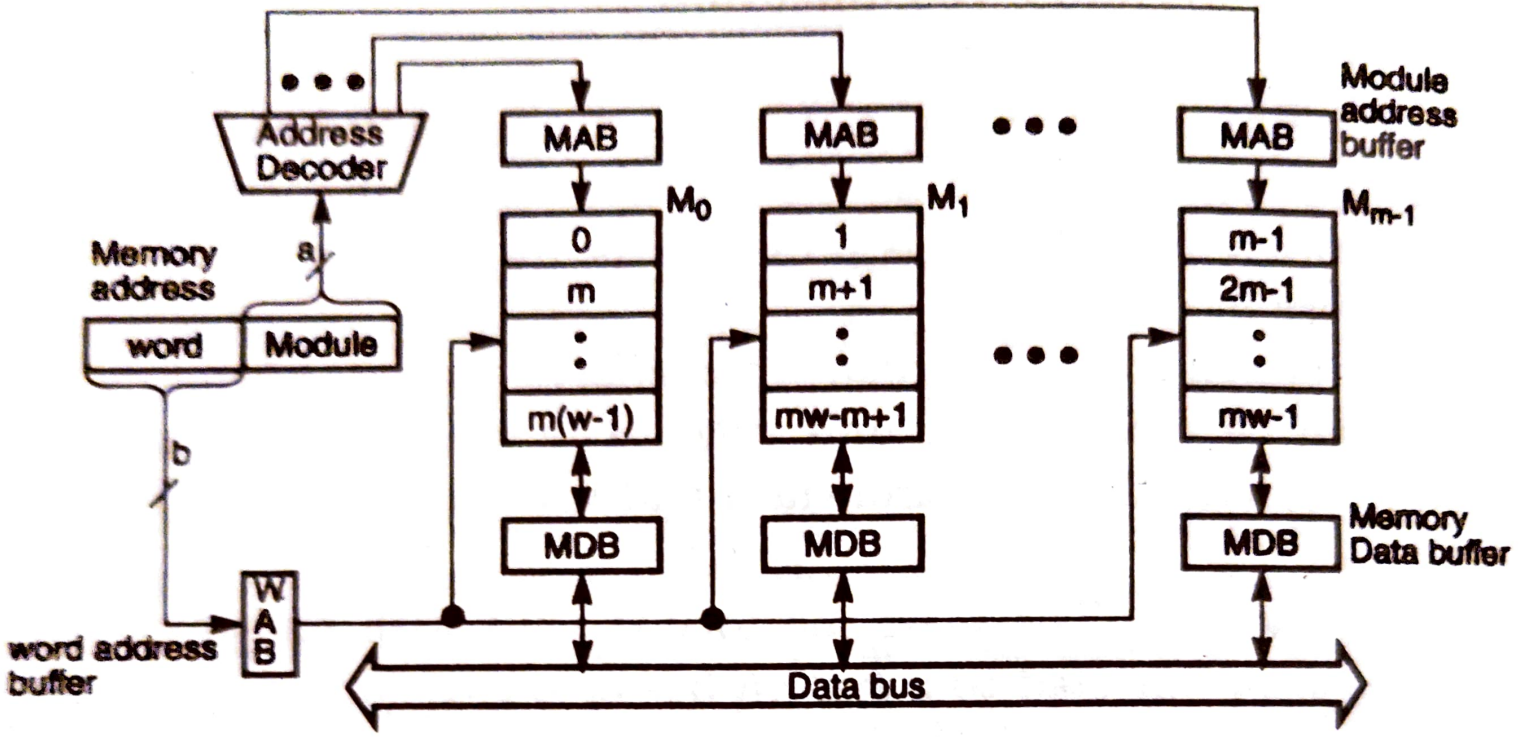
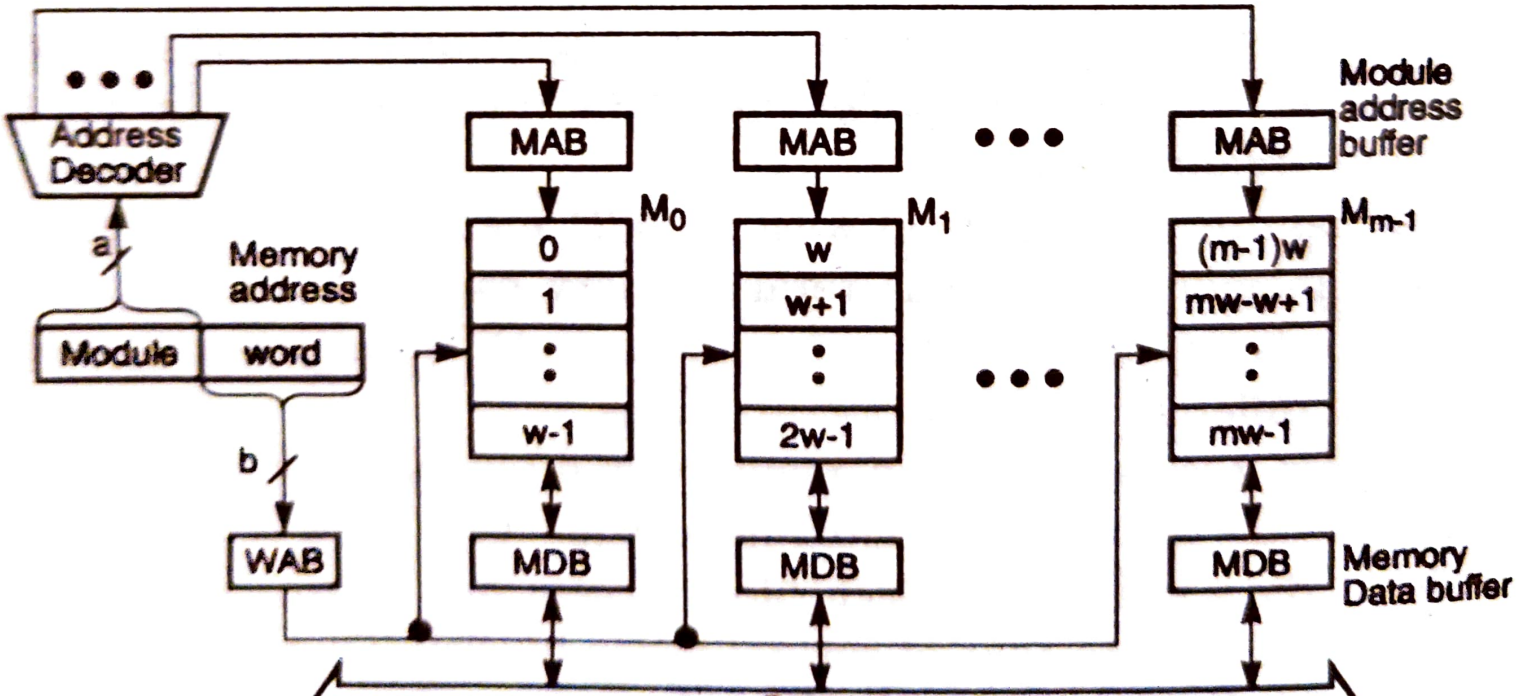
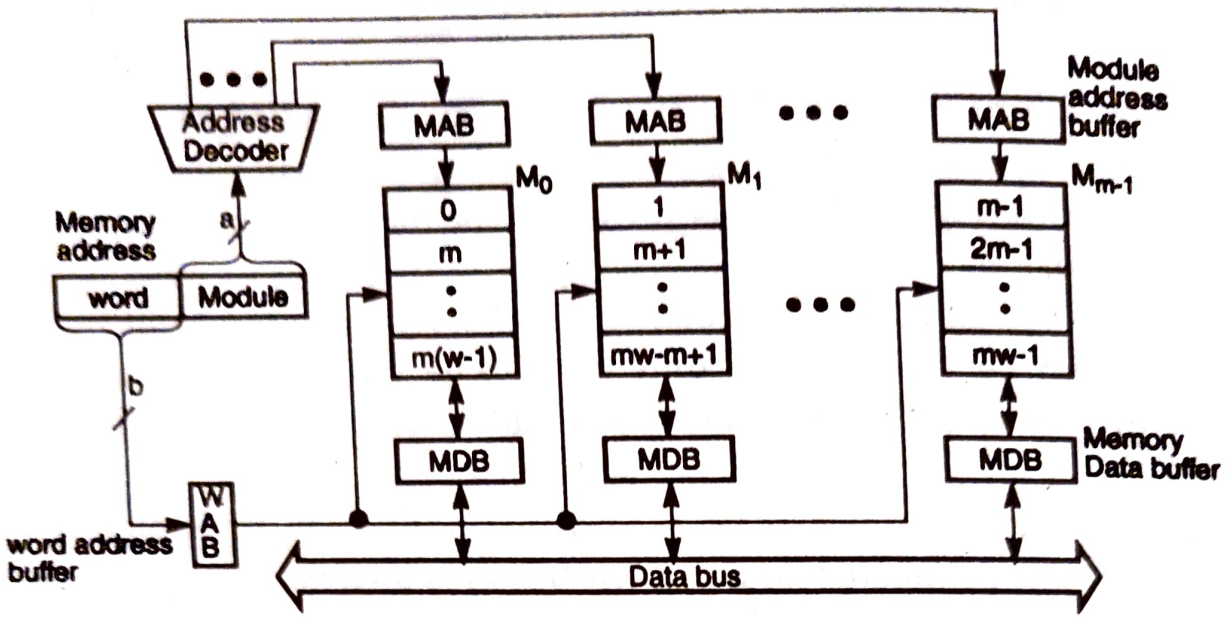
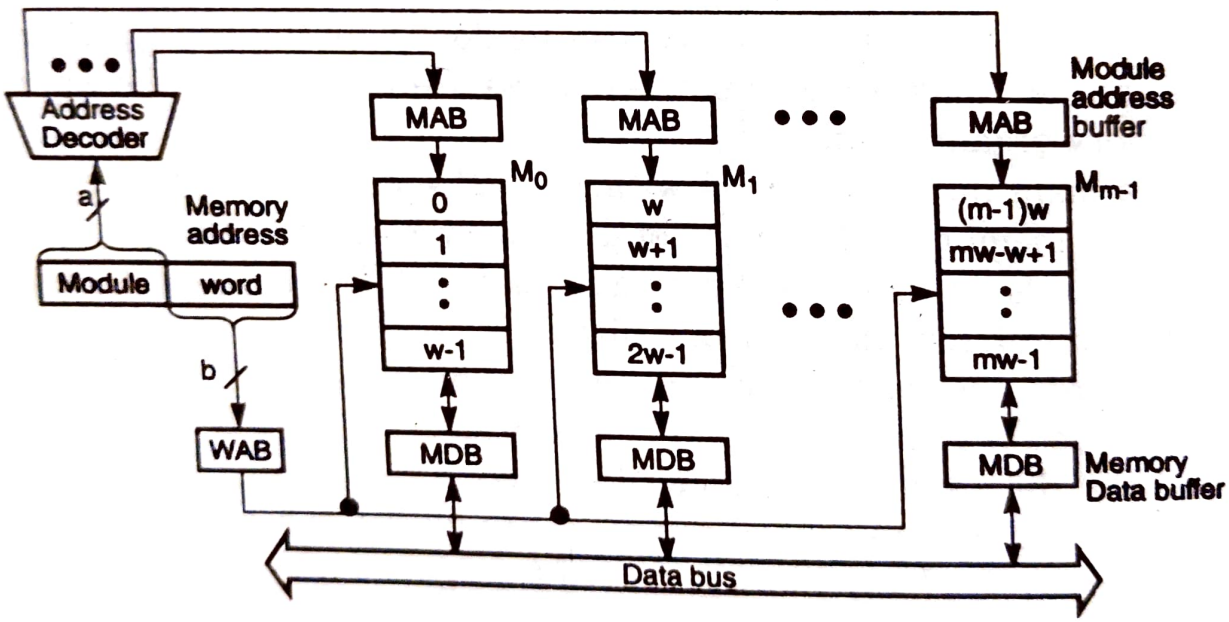



(a) Low-order $m$-way interleaving (the C-access memory scheme)






(a) Low-order $m$-way interleaving (the C-access memory scheme)




(b) High-order $m$-way interleaving

**Figure 5.15** **Two interleaved memory organizations with $m = 2^a$ modules and $w = 2^b$ words per module** (word addresses shown in boxes).

subdivided into $m$ *minor cycles*.

An eight-way interleaved memory (with $m = 8$ and $w = 8$ and thus $a = b = 3$) is shown in Fig. 5.16a. Let $\theta$ be the major cycle and $\tau$ the minor cycle. These two cycle times are related as follows:

$$\tau = \frac{\theta}{} \tag{5.4}$$