

# COL215: Digital Logic and System Design

Special Laboratory Semester, AY 2021-22  
Department of Computer Science & Engineering

## Course Instructions

**Laboratory Classes:** 25th April to 31st May (5 Weeks)

**Instructors:** Prof. Vireshwar Kumar, Prof. Anshul Kumar, and Prof. M. Balakrishnan

**Venue:** Room-502, Digital Hardware Design Lab, Bharti School Building

**Lab Schedule:** The class will be divided into six groups which will follow the following schedule.

Group	Session-1 (10 AM - 1 PM)	Session-2 (2 PM - 5 PM)
Group 1	Monday	Thursday
Group 2	Tuesday	Friday
Group 3	Wednesday	Saturday
Group 4	Thursday	Monday
Group 5	Friday	Tuesday
Group 6	Saturday	Wednesday

### Grading:

Component	Number	Weight	Total Weight
Lab Assignment	9	10% Code (4%) Demo (4%) Report (2%)	90%
Viva	2	5%	10%

### General Instructions:

- Students will work in teams of two. The link to share team details will be shared separately. Either you or your lab partner (not both) should submit the information about your team.
- Each group will have a maximum of 15 teams, i.e., 30 students.
- Each group will conduct two lab sessions per week.
- There will be nine lab assignments in this course, and you will be expected to complete an assignment after each session.
- In each lab assignment, you will be expected to use the XILINX design software and come to the lab session with your design that has already been simulated on the software. The lab session will be used to validate your design by downloading on the given kit and demonstrating the same to the instructor. Finally, you will submit a lab report.