

# DIGITAL LOGIC & SYSTEM DESIGN

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## Submission Instructions

Only one of the partner needs to submit. Also, you need to mention the partners name, entry ID (while doing the submission).

1. Name the submission file as entryNumber1\_entryNumber2.zip
2. Go to the submission tab.
3. Upload the zip file containing following files
  - a. Source file (.vhd)
  - b. Constraint file (.xdc)
  - c. Bit file (.bit)
  - d. Report (.pdf) - stating what all you have done in lab, pasting screenshots of simulations, giving resource count like number of Flipflops, LUTs, BRAMs and DSPs
4. Click submit. Mention your name (and entry ID) , your partner's name (and entry ID) in the comments.

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Participants


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 **Submission Instructions**

 Setup Instructions

 Master Constraint File (.xdc file)

 Lab Software Installation Instructions

 Instructor Information

 BASYS3 Reference manual (web version)

 BASYS3 Reference manual (pdf file)

Assignment Information

Assignments

Viva

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You are logged in as Mohit Sharma (Log out)

2102-COL215

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