

DIGITAL LOGIC & SYSTEM DESIGN

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Submission Instructions

Only one of the partner needs to submit. Also, you need to mention the partners name, entry ID (while doing the submission).

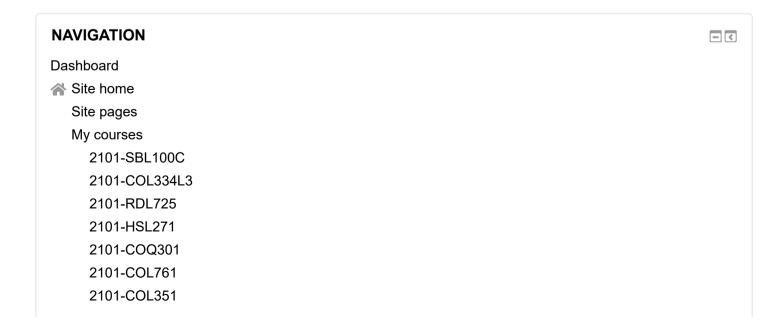
- 1. Name the submission file as entryNumber1 entryNumber2.zip
- 2. Go to the submission tab.
- 3. Upload the zip file containing following files
 - a. Source file (.vhd)
 - b. Constraint file (.xdc)
 - c. Bit file (.bit)
- d. Report (.pdf) stating what all you have done in lab, pasting screenshots of simulations, giving resource count like number of Flipflops, LUTs, BRAMs and DSPs
- 4. Click submit. Mention your name (and entry ID), your partner's name (and entry ID) in the comments.

Last modified: Monday, 25 April 2022, 12:08 PM

■ Course Schedule



Setup Instructions ▶



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2102-COL215
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