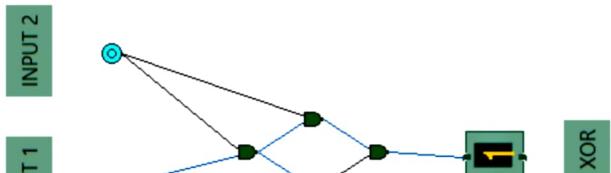
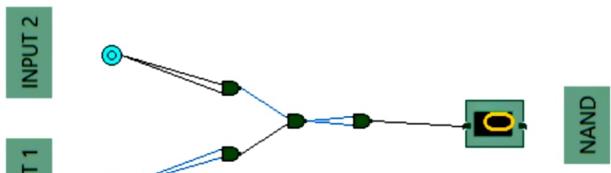


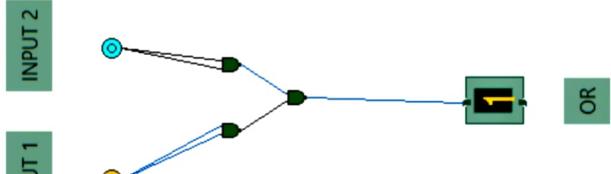
XNOR



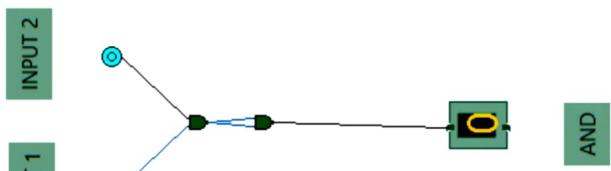
XOR



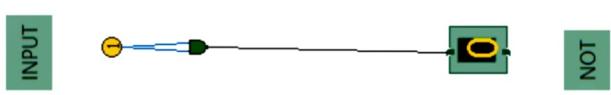
NAND



OR



AND



NOT

ALL LOGIC GATE USING UNIVERSAL NAND

