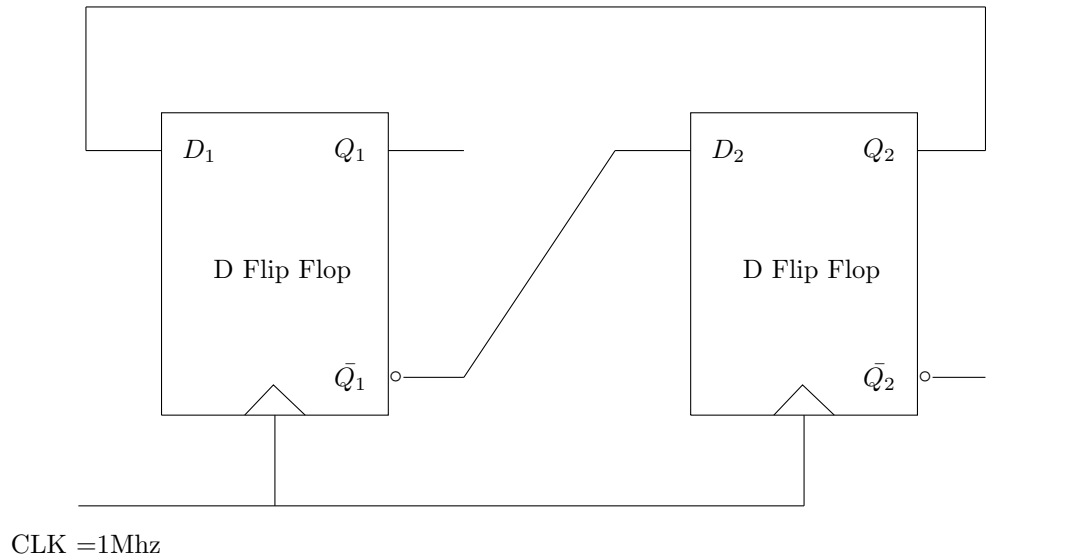


GATE EC 2023

Mohiuddin

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1. In a given sequential circuit, initial states are $Q_1 = 1$ and $Q_2 = 0$. For a clock frequency of 1 MHz, the frequency of signal Q_2 in kHz, is (rounded off to the nearest integer)



(GATE EC 2023)