# SWC\_LED

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## **Data Structure Index**

## **Data Structures**

Here are the data structu	res with brief descriptions:
LBTY_tuniPort16	
LED_tstrConfig	(: type define of structure for Led Configuration
)	

# **File Index**

## **File List**

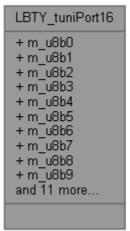
Here is a list of all files with brief descriptions:

LED_cfg.c		9
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-	paces/MCU Drivers/ATmega32/MCAL/SWC_BSW/LCTY_int.h	

## **Data Structure Documentation**

## LBTY\_tuniPort16 Union Reference

#include <LBTY\_int.h>
Collaboration diagram for LBTY\_tuniPort16:



#### **Data Fields**

- struct {
- <u>u8 m\_u8b0</u>:1
- <u>u8 m\_u8b1</u>:1
- <u>u8 m u8b2</u>:1
- <u>u8 m\_u8b3</u>:1
- <u>u8 m u8b4</u>:1
- <u>u8 m\_u8b5</u>:1
- <u>u8 m u8b6</u>:1
- <u>u8 m u8b7</u>:1
- <u>u8 m\_u8b8</u>:1
- <u>u8 m u8b9</u>:1
- <u>u8 m\_u8b10</u>:1
- <u>u8 m u8b11</u>:1
  <u>u8 m\_u8b12</u>:1
- <u>u8 m\_u8b13</u>:1
- <u>u8 m\_u8b13</u>.1
- <u>u8 m\_u8b15</u>:1
- } <u>sBits</u>
- struct {
- <u>u8</u> <u>m\_u8low</u>
- <u>u8</u> <u>m</u> u8high
- } sBytes
- <u>u16 u u16Word</u>

## **Field Documentation**

```
u8 m_u8b0
u8 m_u8b1
u8 m_u8b10
u8 m_u8b11
u8 m_u8b12
u8 m_u8b13
u8 m_u8b14
u8 m_u8b15
u8 m_u8b2
u8 m_u8b3
u8 m_u8b4
<u>u8</u> m_u8b5
u8 m_u8b6
u8 m_u8b7
u8 m_u8b8
u8 m_u8b9
u8 m_u8high
u8 m_u8low
struct { ... } sBits
struct { ... } sBytes
<u>u16</u> u_u16Word
```

### The documentation for this union was generated from the following file:

• H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC\_BSW/<u>LBTY int.h</u>

## LBTY\_tuniPort8 Union Reference

#include <LBTY\_int.h> Collaboration diagram for LBTY\_tuniPort8:



#### **Data Fields**

- struct {
- <u>u8 m\_u8b0</u>:1
- <u>u8 m u8b1</u>:1
- <u>u8 m\_u8b2</u>:1
- <u>u8 m u8b3</u>:1
- <u>u8 m\_u8b4</u>:1
- <u>u8 m\_u8b5</u>:1 <u>u8</u> <u>m</u> <u>u8b6</u>:1
- <u>u8 m\_u8b7</u>:1
- } sBits
- $u8 u_u8Byte$

## **Detailed Description**

Union Byte bit by bit

#### **Field Documentation**

```
      u8 m_u8b0

      u8 m_u8b1

      u8 m_u8b2

      u8 m_u8b3

      u8 m_u8b4

      u8 m_u8b5

      u8 m_u8b6

      u8 m_u8b7

      struct {...} sBits

      u8 u_u8Byte
```

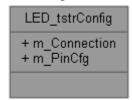
The documentation for this union was generated from the following file:

• H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC\_BSW/<u>LBTY\_int.h</u>

## LED\_tstrConfig Struct Reference

: type define of structure for Led Configuration

#include <LED\_int.h>
Collaboration diagram for LED\_tstrConfig:



#### **Data Fields**

- <u>LED\_tenuConnection m\_Connection</u>
- GPIO\_tstrPinConfig m PinCfg

#### **Detailed Description**

: type define of structure for Led Configuration

**Type**: struct **Unit**: None

#### **Field Documentation**

**LED\_tenuConnection** m\_Connection

Push Active

GPIO\_tstrPinConfig m\_PinCfg

Pin Configuration

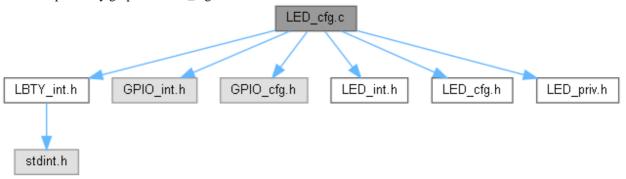
The documentation for this struct was generated from the following file:

LED int.h

## **File Documentation**

## LED\_cfg.c File Reference

```
#include "LBTY_int.h"
#include "GPIO_int.h"
#include "GPIO_cfg.h"
#include "LED_int.h"
#include "LED_cfg.h"
#include "LED_priv.h"
Include dependency graph for LED_cfg.c:
```



#### **Variables**

• const LED tstrConfig kau8LEDConfiguration LGB [LED Num]

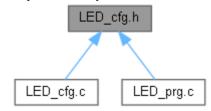
#### **Variable Documentation**

#### const <u>LED tstrConfig</u> kau8LEDConfiguration\_LGB[<u>LED Num</u>]

```
Initial value:= {
          {.m Connection = LEDO CON, .m PinCfg =
         {.m Port = LEDO PORT, .m Pin = LEDO PIN, .m Dir = PIN OUTPUT, .m Value =
        , { .m Connection = LED1 CON, .m PinCfg =
         {.m Port = LED1 PORT, .m Pin = LED1 PIN, .m Dir = PIN OUTPUT, .m Value =
LED1 CON}}
        , { .m_Connection = LED2 CON, .m PinCfg =
         {.m Port = LED2 PORT, .m Pin = LED2 PIN, .m Dir = PIN OUTPUT, .m Value =
LED2 CON}}
        , { .m Connection = LED3 CON, .m PinCfg =
         {.m_Port = LED3 PORT, .m_Pin = LED3 PIN, .m_Dir = PIN_OUTPUT, .m_Value =
         , \{.m\_Connection = \underline{LED4} \ \underline{CON}, .m\_PinCfg =
         {.m_Port = LED4 PORT, .m_Pin = LED4 PIN, .m_Dir = PIN_OUTPUT, .m_Value =
LED4 CON}}
         , \{.m\_Connection = \underline{LED5\_CON}, .m\_PinCfg = 
         {.m_Port = LED5 PORT, .m_Pin = LED5 PIN, .m_Dir = PIN_OUTPUT, .m_Value =
LED5 CON}}}
```

## LED\_cfg.h File Reference

This graph shows which files directly or indirectly include this file:



#### **Macros**

- #define LED0 0
- #define <u>LED0 PORT</u> B
- #define <u>LED0 PIN</u> 0
- #define <u>LED0 CON</u> <u>LED Forward</u>
- #define <u>LED1</u> 1
- #define <u>LED1\_PORT</u> B
- #define <u>LED1 PIN</u> 1
- #define LED1\_CON LED\_Forward
- #define <u>LED2</u> 2
- #define <u>LED2\_PORT</u> B
- #define <u>LED2\_PIN\_\_</u> 2
- #define <u>LED2 CON</u> <u>LED Forward</u>
- #define LED3 3
- #define <u>LED3 PORT</u> B
- #define <u>LED3\_PIN</u> 3
- #define <u>LED3\_CON</u> <u>LED\_Forward</u>
- #define <u>LED4</u> 4
- #define <u>LED4\_PORT</u> B
- #define LED4 PIN 4
- #define <u>LED4\_CON</u> <u>LED\_Forward</u>
- #define <u>LED5</u> 5
- #define <u>LED5 PORT</u> B
- #define <u>LED5\_PIN</u> 5
- #define <u>LED5 CON</u> <u>LED Forward</u>
- #define <u>LED6</u> 6
- #define <u>LED6 PORT</u> B
- #define <u>LED6 PIN</u> 6
- #define <u>LED6\_CON</u> <u>LED\_Forward</u>
- #define <u>LED7</u> 7
- #define <u>LED7\_PORT</u> B
- #define <u>LED7 PIN</u> 7
- #define <u>LED7 CON</u> <u>LED Forward</u>

#### **Macro Definition Documentation**

```
#define LED0 0
#define LED0_CON LED Forward
#define LED0_PIN 0
#define LED0_PORT B
#define LED1 1
#define LED1_CON LED_Forward
#define LED1_PIN 1
#define LED1_PORT B
#define LED2 2
#define LED2_CON LED_Forward
#define LED2_PIN 2
#define LED2_PORT B
#define LED3 3
#define LED3_CON LED_Forward
#define LED3_PIN 3
#define LED3_PORT B
#define LED4 4
#define LED4_CON LED Forward
#define LED4_PIN 4
#define LED4_PORT B
#define LED5 5
#define LED5_CON LED_Forward
#define LED5_PIN 5
#define LED5_PORT B
#define LED6 6
```

#define LED6\_CON <u>LED\_Forward</u>

#define LED6\_PIN 6

#define LED6\_PORT B

#define LED7 7

#define LED7\_CON LED\_Forward

#define LED7\_PIN 7

#define LED7\_PORT B

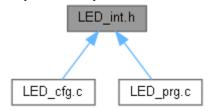
## LED\_cfg.h

```
Go to the documentation of this file.1 /*
********************
3 /* **********
4 /* File Name : LED_cfg.h
11
12 #ifndef LED_CFG_H_
13 #define LED CFG H
14
18
21 /* ***********************
22
23 #if defined(AMIT KIT)
24
25 #define LED0
                   0
26 #define LED0_PORT D
27 #define LED0_PIN AMIT_LED0
28 #define LED0_CON LED_Forward
29
30 #define LED1
                D
AMIT_LED1
LED_Forward
31 #define LED1 PORT
32 #define LED1 PIN
33 #define LED1 CON
34
35 #define LED2
36 #define LED2 PORT D
37 #define LED2 PIN AMIT LED2
38 #define LED2_CON LED_Forward
39
40 #elif defined(ETA32 KIT)
41
42 #define LED0
               0
B
Eta32_LED_R
LED_Forward
43 #define LED0 PORT
44 #define LEDO PIN
45 #define LEDO CON
46
47 #define LED1
47 #define LED1 1
48 #define LED1_PORT A
49 #define LED1 PIN Eta32 LED G
50 #define LED1_CON LED_Forward
51
52 #define LED2
53 #define LED2 PORT A
54 #define LED2 PIN Eta32 LED B
55 #define LED2 CON LED_Forward
56
57 #define LED3
57 #define LED3_PORT A
59 #define LED3_PIN Eta32_LED_Y
60 #define LED3_CON LED Forward
61
62 #elif defined(ETA32 MINI KIT)
63
                   0
64 #define LED0
65 #define LEDO_PORT C
66 #define LEDO PIN Eta32 mini LED R
67 #define LEDO_CON LED_Forward
68
69 #define LED1
70 #define LED1_PORT C
71 #define LED1_PIN Eta32_mini_LED_G
72 #define LED1_CON LED_Forward
```

```
73
74 #define LED2
75 #define LED2 PORT
             Eta32_mini_LED_B
LED_Forward
76 #define LED2_PIN
77 #define LED2 CON
78
79 #else
80
81 #define LED0
82 #define LED0 PORT
83 #define LEDO PIN
               0
84 #define LEDO_CON
               LED Forward
85
86 #define LED1
              1
87 #define LED1_PORT
88 #define LED1_PIN
89 #define LED1 CON
90
91 #define LED2
92 #define LED2_PORT
93 #define LED2_PIN
              В
94 #define LED2 CON
               LED Forward
95
96 #define LED3
97 #define LED3_PORT
              В
98 #define LED3 PIN
99 #define LED3 CON
               LED Forward
100
101 #define LED4
102 #define LED4 PORT
                В
103 #define LED4 PIN
104 #define LED4 CON
                LED Forward
105
106 #define LED5
107 #define LED5 PORT
108 #define LED5_PIN
109 #define LED5_CON
                LED Forward
110
111 #define LED6
112 #define LED6 PORT
                В
113 #define LED6_PIN
114 #define LED6_CON
                LED Forward
115
116 #define LED7
117 #define LED7 PORT
118 #define LED7_PIN
119 #define LED7_CON
                LED Forward
120
121 #endif
122
126
127 /* ***********************
130
134
135
```

## LED\_int.h File Reference

This graph shows which files directly or indirectly include this file:



#### **Data Structures**

struct LED\_tstrConfig: type define of structure for Led Configuration

#### **Enumerations**

- enum <u>LED\_tenuConnection</u> { <u>LED\_Forward</u> = (u8)0u, <u>LED\_Reverse</u> }
- enum LED\_tenuStatus { LED\_OFF = (u8)0u, LED\_ON }

#### **Functions**

- void <u>LED vidInit</u> (<u>u8</u> u8LedNum)
- void <u>LED\_vidInitAll</u> (void)
- <u>LBTY tenuErrorStatus LED u8SetON</u> (<u>u8</u> u8LedNum)
- LBTY\_tenuErrorStatus LED\_u8SetOFF (u8 u8LedNum)
- <u>LBTY tenuErrorStatus</u> <u>LED u8Toggle</u> (<u>u8</u> u8LedNum)

#### **Enumeration Type Documentation**

#### enum <u>LED\_tenuConnection</u>

#### **Enumerator:**

```
LED_Forward

LED_Reverse

19 {
20    LED Forward = (u8) 0u,
21    LED Reverse
22 } LED tenuConnection;
```

#### enum <u>LED\_tenuStatus</u>

#### **Enumerator:**

#### **Function Documentation**

#### LBTY\_tenuErrorStatus LED\_u8SetOFF (u8 u8LedNum)

#### LBTY tenuErrorStatus LED\_u8SetON (u8 u8LedNum)

### LBTY\_tenuErrorStatus LED\_u8Toggle (u8 u8LedNum)

#### void LED\_vidInit (u8 u8LedNum)

#### void LED\_vidInitAll (void )

```
60
61    for(<u>u8</u> i = <u>LED Num</u>; i--;){
62         GPIO_u8PinInit(<u>kau8LEDConfiguration LGB</u>[i].m_PinCfg);
63    }
64 }
```

## LED\_int.h

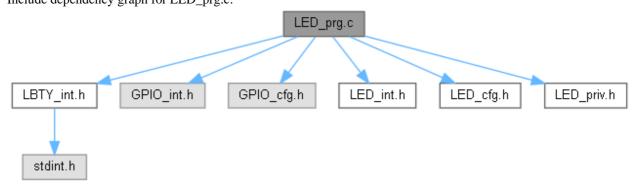
```
Go to the documentation of this file.1 /*
***********************
3 /* ************
4 /* File Name : LED_int.h
5 /* Author : MAAM
6 /* Version : v01.2
7 /* date : Apr 8, 2023
8 /* *************
11
12 #ifndef LED INT H
13 #define LED INT H
14
18
19 typedef enum{
 LED Forward = (u8)0u,
LED Reverse
20
21
22 } LED tenuConnection;
23
24 typedef enum{

\begin{array}{ccc}
25 & \underline{\text{LED OFF}} = (\underline{u8}) 0u, \\
150 & \underline{\text{LED ON}}
\end{array}

26
   LED ON
27 }LED tenuStatus;
28
29
/*********
**********
30
33 typedef struct{
34 LED tenuConnection m Connection;
35 GPIO_tstrPinConfig m PinCfg;
36 }LED tstrConfig;
37
38 /* *****
 /* ********************** MACRO/DEFINE SECTION ****************** */
39
41
42 /*
43 /* ***************************** CONST SECTION ******************************
44 /* **************
45
46 /* ******
49
50 /* *****************
53
55\ / \star Description : Initialize the LED Button direction
56 /* Input
       : u8LedNum : void
                                             */
57 /* Return
58 /* ********************************
59 extern void LED vidInit(u8 u8LedNum);
60
62 /* Description : Initialize the All LED Button with Configurations
63 /* Input : void
64 /* Return : void
65 /* *************
66 extern void LED vidInitAll(void);
67
68 /* ************
69 /* Description : Set the LED ON
70 /* Input : u8LedNum
71 /* Return : LBTY_tenuErrorStatus
                                            */
```

## LED\_prg.c File Reference

```
#include "LBTY_int.h"
#include "GPIO_int.h"
#include "GPIO_cfg.h"
#include "LED_int.h"
#include "LED_cfg.h"
#include "LED_priv.h"
Include dependency graph for LED_prg.c:
```



#### **Functions**

- void <u>LED vidInit</u> (<u>u8</u> u8LedNum)
- void <u>LED\_vidInitAll</u> (void)
- <u>LBTY tenuErrorStatus LED u8SetON (u8 u8LedNum)</u>
- <u>LBTY\_tenuErrorStatus\_LED\_u8SetOFF (u8\_u8LedNum)</u>
- LBTY tenuErrorStatus LED u8Toggle (u8 u8LedNum)

#### **Variables**

• const LED\_tstrConfig kau8LEDConfiguration\_LGB [LED\_Num]

#### **Function Documentation**

#### LBTY tenuErrorStatus LED u8SetOFF (u8 u8LedNum)

#### LBTY\_tenuErrorStatus LED\_u8SetON (u8 u8LedNum)

#### LBTY\_tenuErrorStatus LED\_u8Toggle (u8 u8LedNum)

#### void LED\_vidInit (u8 u8LedNum)

## void LED\_vidInitAll (void )

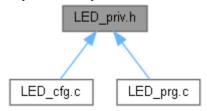
```
60
61     for(u8 i = LED Num ; i-- ; ) {
62         GPIO_u8PinInit(kau8LEDConfiguration LGB[i].m_PinCfg);
63     }
64 }
```

#### **Variable Documentation**

const <a href="LED\_tstrConfig">LED\_tstrConfig</a> kau8LEDConfiguration\_LGB[LED\_Num][extern]

## LED\_priv.h File Reference

This graph shows which files directly or indirectly include this file:



#### **Enumerations**

• enum <u>LED\_tenuLedNum</u> { <u>LED\_Num</u> }

#### **Enumeration Type Documentation**

#### enum <u>LED\_tenuLedNum</u>

#### **Enumerator:**

```
LED_Num
19
20 #ifdef LED0
21
        LED 0 = (u8) 0u
22 #endif
23 #ifdef <u>LED1</u>
24
        ,LED_1
25 #endif
26 #ifdef LED2
27 ,LED 2
        ,LED_2
28 #endif
29 #ifdef LED3
30 ,LED_3
31 #endif
32 #ifdef LED4
        , LED_4
33
34 #endif
35 #ifdef <u>LED5</u>
36 ,LE
37 #endif
        ,LED_5
38 #ifdef <u>LED6</u>
         ,LED_6
39
40 #endif
41 #ifdef LED7
42
        ,LED_7
43 #endif
44 , LED Num
45 }LED tenuLedNum;
```

## LED\_priv.h

```
Go to the documentation of this file.1 /*
****************
3 /* ************
4 /* File Name : LED_priv.h
11
12 #ifndef LED PRIV H
13 #define LED PRIV H
14
18
19 typedef enum{
20 #ifdef LED0
21
 LED 0 = (u8) 0u
22 #endif
23 #ifdef LED1
24
 ,LED_1
25 #endif
26 #ifdef LED2
 ,LED_2
27
28 #endif
29 #ifdef <u>LED3</u>
30
 ,LED_3
31 #endif
32 #ifdef LED4
 , LED_4
33
34 #endif
35 #ifdef LED5
36
 ,LED_5
37 #endif
38 #ifdef LED6
39
 ,LED_6
40 #endif
41 #ifdef LED7
42
 ,LED_7
43 #endif
44 , LED Num
45 }LED tenuLedNum;
46
47 /* ***
49
50
51 /*
54
55 /* *************
58
59 /* *************************
62
```

## main.c File Reference

# H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC\_BSW/LBIT\_int.h File Reference

#### **Macros**

- #define <u>BV</u>(bit) (1u<<(bit))
- #define <u>SET\_BIT</u>(REG, bit) ((REG) |= (1u<<(bit)))
- #define CLR BIT(REG, bit) ((REG) &=  $\sim$ (1u<<(bit)))
- #define TOG\_BIT(REG, bit) ((REG) ^= (1u<<(bit)))
- #define  $\underline{SET} \underline{BYTE}(REG, bit)$  ((REG) |= (0xFFu << (bit)))
- #define  $\underline{\text{CLR\_BYTE}}(\text{REG}, \text{ bit}) ((\text{REG}) \&= \sim (0xFFu << (\text{bit})))$
- #define TOG BYTE(REG, bit) ((REG) ^= (0xFFu<<(bit)))
- #define <u>SET\_MASK(REG, MASK)</u> ((REG) |= (MASK))
- #define CLR\_MASK(REG, MASK) ((REG) &= ~(MASK))
- #define TOG\_MASK(REG, MASK) ((REG) ^= (MASK))
- #define <u>GET\_MASK(REG, MASK)</u> ((REG) & (MASK))
- #define  $\underline{SET}_REG(REG)$  ((REG) =  $\sim$ (0u))
- #define CLR REG(REG) ((REG) = (0u))
- #define  $\underline{TOG\_REG}(REG)$  ((REG)  $^= \sim (0u)$ )
- #define GET\_BIT(REG, bit) (((REG)>>(bit)) & 0x01u)
- #define GET\_NIB(REG, bit) (((REG)>>(bit)) & 0x0Fu)
- #define <u>GET\_BYTE</u>(REG, bit) (((REG)>>(bit)) & 0xFFu)
- #define <u>ASSIGN\_BIT</u>(REG, bit, value)  $((REG) = ((REG) \& \sim (0x01u << (bit))) | (((value) \& 0x01u) << (bit)))$
- #define <u>ASSIGN\_NIB</u>(REG, bit, value)  $((REG) = ((REG) \& \sim (0x0Fu << (bit))) | (((value) \& 0x0Fu) << (bit)))$
- #define  $\underline{ASSIGN\_BYTE}(REG, bit, value)$  ((REG) = ((REG) & ~(0xFFu<<(bit))) (((value) & 0xFFu)<<(bit)))
- #define CON\_u8Bits(b7, b6, b5, b4, b3, b2, b1, b0)

#### (0b##b7##b6##b5##b4##b3##b2##b1##b0)

• #define <u>CON\_u16Bits</u>(b15, b14, b13, b12, b11, b10, b9, b8, b7, b6, b5, b4, b3, b2, b1, b0)

(0b##b15##b14##b13##b12##b11##b10##b9##b8##b7##b6##b5##b4##b3##b2##b1##b0)

#### **Macro Definition Documentation**

```
#define BV(bit) (1u<<(bit))
#define ASSIGN_BIT( REG, bit, value) ((REG) = ((REG) & \sim(0x01u<<(bit)))
                                                                            I
(((value) & 0x01u)<<(bit)))
#define ASSIGN BYTE( REG, bit, value) ((REG) = ((REG) & ~(0xFfu<<(bit)))
                                                                            Τ
(((value) & 0xFFu)<<(bit)))
#define ASSIGN_NIB( REG, bit, value) ((REG) = ((REG) & \sim(0x0Fu<<(bit)))
                                                                            I
(((value) & 0x0Fu)<<(bit)))
#define CLR_BIT( REG, bit) ((REG) &= ~(1u<<(bit)))
#define CLR_BYTE( REG, bit) ((REG) &= ~(0xFFu<<(bit)))
#define CLR_MASK( REG, MASK) ((REG) &= ~(MASK))
#define CLR_REG( REG) ((REG) = (0u))
#define CON_u16Bits( b15, b14, b13, b12, b11, b10, b9, b8, b7, b6, b5,
b4, b3, b2, b1, b0)
       (0b##b15##b14##b13##b12##b11##b10##b9##b8##b7##b6##b5##b4##b3##b2##
b1##b0)
#define CON_u8Bits( b7, b6, b5, b4, b3, b2, b1, b0)
      (0b##b7##b6##b5##b4##b3##b2##b1##b0)
#define GET_BIT( REG, bit) (((REG)>>(bit)) & 0x01u)
#define GET_BYTE( REG, bit) (((REG)>>(bit)) & 0xFFu)
#define GET_MASK( REG, MASK) ((REG) & (MASK))
#define GET_NIB( REG, bit) (((REG)>>(bit)) & 0x0Fu)
#define SET_BIT( REG, bit) ((REG) |= (1u<<(bit)))
   Bitwise Operation
```

#define SET\_BYTE( REG, bit) ((REG) |= (0xFFu<<(bit)))

#define SET\_MASK( REG, MASK) ((REG) |= (MASK))

#define SET\_REG( REG) ((REG) = ~(0u))

#define TOG\_BIT( REG, bit) ((REG) ^= (1u<<(bit)))

#define TOG\_BYTE( REG, bit) ((REG) ^= (0xFFu<<(bit)))

#define TOG\_MASK( REG, MASK) ((REG) ^= (MASK))

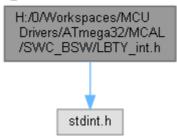
#define TOG\_REG( REG) ((REG) ^= ~(0u))

#### LBIT\_int.h

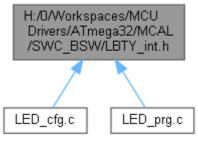
```
Go to the documentation of this file.1 /*
2 /* ************************* FILE DEFINITION SECTION ************************
3 /* **********
4 /* File Name : LBIT_int.h
5 /* Author : MAAM
6 /* Version : v01
7 /* date : Mar 24, 2023
8 \ /* \ description : Bitwise Library
9 /* *********
11 /* ***********
12
13 #ifndef LBIT INT H
14 #define LBIT INT H
15
17 /* ***************** TYPE DEF/STRUCT/ENUM SECTION ***************** */
19
23
24 #define _BV(bit)
                                                (1u<<(bit))
25
27 #define SET BIT(REG, bit)
                                             ((REG) \mid = (1u << (bit)))
28 #define CLR BIT(REG, bit)
                                             ((REG) &= ~(1u<<(bit)))
29 #define TOG_BIT(REG, bit)
                                             ((REG) ^= (1u<<(bit)))
30
                                            ((REG) |= (0xFFu<<(bit)))
((REG) &= ~(0xFFu<<(bit)))
31 #define SET_BYTE(REG, bit)
32 #define CLR BYTE (REG, bit)
33 #define TOG BYTE (REG, bit)
                                             ((REG) ^= (0xFFu<<(bit)))
34
                                             ((REG) |= (MASK))
35 #define SET MASK (REG, MASK)
36 #define CLR MASK (REG, MASK)
                                             ((REG) &= ~(MASK))
                                             ((REG) ^= (MASK))
((REG) & (MASK))
37 #define TOG_MASK(REG, MASK)
38 #define GET MASK(REG, MASK)
39
                                             ((REG) = \sim (0u))
((REG) = (0u))
40 #define SET REG(REG)
41 #define CLR REG(REG)
42 #define TOG REG(REG)
                                             ((REG) ^= \sim (Ou))
43
44 #define GET BIT(REG, bit)
                                             (((REG) >> (bit)) \& 0x01u)
45 #define GET NIB(REG, bit)
                                             (((REG)>>(bit)) & 0x0Fu)
46 #define GET BYTE (REG, bit)
                                             (((REG)>>(bit)) & 0xFFu)
47
48 #define ASSIGN BIT (REG, bit, value)
                                            ((REG) = ((REG) \& \sim (0x01u << (bit)))
| (((value) \& 0x01u) << (bit)))
49 #define ASSIGN NIB(REG, bit, value)
                                            ((REG) = ((REG) \& \sim (0x0Fu << (bit)))
| (((value) & 0x0Fu)<<(bit)))
50 #define ASSIGN_BYTE(REG, bit, value)
                                            ((REG) = ((REG) & \sim (0xFFu << (bit)))
| (((value) & 0xFFu) << (bit)))
51
52 /*
53 #define ASSIGN BIT(REG, bit, value)
                                             do{
54
                                              REG &= \sim (0 \times 01 u << bit);
55
                                              REG \mid = ((value & 0x01u)<<bit);
56
                                             }while(0)
57 */
58
        bits together in an u8 register
59 /*
60 #define CON_u8Bits(b7, b6, b5, b4, b3, b2, b1, b0)
61
(0b##b7##b6##b5##b4##b3##b2##b1##b0)
            bits together in an u16 register
64 #define CON u16Bits(b15, b14, b13, b12, b11, b10, b9, b8, b7, b6, b5, b4, b3, b2, b1,
b0) \
```

# H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC\_BSW/LBTY\_int.h File Reference

#include <stdint.h>
Include dependency graph for LBTY\_int.h:



This graph shows which files directly or indirectly include this file:



#### **Data Structures**

• union LBTY tuniPort8union LBTY tuniPort16

#### **Macros**

- #define \_\_IO volatile
- #define \_\_O volatile
- #define <u>I</u> volatile const
- #define <u>LBTY\_u8vidNOP()</u>
- #define <u>LBTY NULL</u> ((void \*) 0U)
- #define LBTY\_u8ZERO ((u8)0x00U)
- #define <u>LBTY u8MAX</u> ((<u>u8</u>)0xFFU)
- #define LBTY  $\underline{\text{S8MAX}}$  (( $\underline{\text{s8}}$ )0x7F)
- #define <u>LBTY\_s8MIN</u> ((<u>s8</u>)0x80)
- #define <u>LBTY u16ZERO</u> ((<u>u16</u>)0x0000U)
- #define <u>LBTY\_u16MAX</u> ((<u>u16</u>)0xFFFFU)
- #define LBTY s16MAX ((u16)0x7FFF)
- #define LBTY s16MIN ((u16)0x8000)
- #define <u>LBTY u32ZERO</u> ((<u>u32</u>)0x0000000UL)
- #define <u>LBTY u32MAX</u> ((<u>u32</u>)0xFFFFFFFUL)
- #define <u>LBTY\_s32MAX</u> ((<u>u32</u>)0x7FFFFFFL)
- #define <u>LBTY s32MIN</u> ((<u>u32</u>)0x80000000L)
- #define <u>LBTY\_u64ZERO</u> ((<u>u64</u>)0x000000000000000ULL)
- #define <u>LBTY u64MAX</u> ((<u>u64</u>)0xFFFFFFFFFFFFFFULL)
- #define <u>LBTY\_s64MAX</u> ((<u>u64</u>)0x7FFFFFFFFFFFFFLL)
- #define <u>LBTY\_s64MIN</u> ((u64)0x8000000000000000LL)

### **Typedefs**

- typedef uint8\_t <u>u8</u>
- typedef uint16\_t <u>u16</u>
- typedef uint32\_t <u>u32</u>
- typedef uint64\_t <u>u64</u>
- typedef int8\_t s8
- typedef int16\_t <u>s16</u>
- typedef int32\_t <u>s32</u>
- typedef int64\_t <u>s64</u>
- typedef float <u>f32</u>
- typedef double <u>f64</u>
- typedef <u>u8</u> \* <u>pu8</u>
- typedef <u>u16</u> \* <u>pu16</u>
- typedef <u>u32</u> \* <u>pu32</u>
- typedef <u>u64</u> \* <u>pu64</u>
- typedef  $\underline{s8} * \underline{ps8}$
- typedef <u>s16</u> \* <u>ps16</u>
- typedef  $\frac{1}{832}$  \*  $\frac{1}{9832}$
- typedef <u>s64</u> \* <u>ps64</u>

#### **Enumerations**

- enum <u>LBTY\_tenuFlagStatus</u> { <u>LBTY\_RESET</u> = 0, <u>LBTY\_SET</u> = !LBTY\_RESET }
- enum <u>LBTY tenuBoolean</u> { <u>LBTY TRUE</u> = 0x55, <u>LBTY FALSE</u> = 0xAA }
- enum <u>LBTY\_tenuErrorStatus</u> { <u>LBTY\_OK</u> = (u16)0, <u>LBTY\_NOK</u>, <u>LBTY\_NULL\_POINTER</u>, <u>LBTY\_INDEX\_OUT\_OF\_RANGE</u>, <u>LBTY\_NO\_MASTER\_CHANNEL</u>, <u>LBTY\_READ\_ERROR</u>, <u>LBTY\_WRITE\_ERROR</u>, <u>LBTY\_UNDEFINED\_ERROR</u>, <u>LBTY\_IN\_PROGRESS</u> }

#### **Macro Definition Documentation**

```
#define I volatile const
#define __IO volatile
#define O volatile
#define LBTY_NULL ((void *) 0U)
#define LBTY_s16MAX ((u16)0x7FFF)
#define LBTY_s16MIN ((u16)0x8000)
#define LBTY_s32MAX ((u32)0x7FFFFFFL)
#define LBTY_s32MIN ((<u>u32</u>)0x80000000L)
#define LBTY_s64MAX ((u64)0x7FFFFFFFFFFFLL)
#define LBTY s64MIN ((u64)0x800000000000000LL)
#define LBTY_s8MAX ((s8)0x7F)
#define LBTY_s8MIN ((s8)0x80)
#define LBTY_u16MAX ((u16)0xFFFFU)
#define LBTY_u16ZERO ((<u>u16</u>)0x0000U)
#define LBTY_u32MAX ((u32)0xFFFFFFFUL)
#define LBTY_u32ZERO ((<u>u32</u>)0x0000000UL)
#define LBTY_u64MAX ((u64)0xFFFFFFFFFFFFFULL)
#define LBTY_u64ZERO ((<u>u64</u>)0x00000000000000ULL)
#define LBTY_u8MAX ((u8)0xFFU)
#define LBTY_u8vidNOP()
#define LBTY_u8ZERO ((u8)0x00U)
   Data Types Limitation
```

#### **Typedef Documentation**

#### typedef float f32

Standard Real Decimal number

```
typedef double f64
typedef s16* ps16
typedef s32* ps32
typedef <u>s64</u>* <u>ps64</u>
typedef s8* ps8
   Standard Pointer to Signed Byte/Word/Long_Word
typedef u16* pu16
typedef u32* pu32
typedef u64* pu64
typedef u8* pu8
   Standard Pointer to Unsigned Byte/Word/Long_Word
typedef int16_t s16
typedef int32_t s32
typedef int64_t s64
typedef int8_t s8
   Standard Signed Byte/Word/Long_Word
typedef uint16_t u16
typedef uint32_t u32
typedef uint64_t u64
typedef uint8_t u8
   Data Types New Definitions Standard Unsigned Byte/Word/Long_Word
```

#### **Enumeration Type Documentation**

#### enum <u>LBTY\_tenuBoolean</u>

Boolean type

#### **Enumerator:**

```
LBTY_TRUE

LBTY_FALSE

96 {
97  LBTY TRUE = 0x55,
98  LBTY FALSE = 0xAA
99 } LBTY tenuBoolean;
```

#### enum <u>LBTY\_tenuErrorStatus</u>

Error Return type

#### **Enumerator:**

```
LBTY_OK
       LBTY_NOK
  LBTY_NULL_PO
            INTER
  LBTY_INDEX_O
   UT_OF_RANGE
   LBTY_NO_MAS
   TER_CHANNEL
  LBTY_READ_ER
              ROR
  LBTY_WRITE_E
             RROR
  LBTY_UNDEFIN
       ED_ERROR
  LBTY_IN_PROG
             RESS
102
103 LBTY OK = (u16)0,
104 LBTY NOK,
105 LBTY NULL POINTER,
106 LBTY INDEX OUT OF RANGE,
107 LBTY NO MASTER CHANNEL,
107 LBTY NO MASTER CHANNEL,
108 LBTY READ ERROR,
      LBTY WRITE ERROR,
LBTY UNDEFINED ERROR,
109
110
111 LBTY IN PROGRESS
                                /* Error is not available, wait for availability */
112 } LBTY tenuErrorStatus;
```

#### enum <u>LBTY\_tenuFlagStatus</u>

Flag Status type

#### **Enumerator:**

```
LBTY_RESET

LBTY_SET

90 {
91    LBTY RESET = 0,
92    LBTY SET = !LBTY RESET
93 } LBTY_tenuflagStatus;
```

#### LBTY\_int.h

```
Go to the documentation of this file.1 /*
2 /* ************************* FILE DEFINITION SECTION ************************
3 /* ***********
4 /* File Name : LBTY_int.h
5 /* Author : MAAM
6 /* Version : v01
7 /* date : Mar 23, 2023
8 /* description : Basic Library
9 /* **********
11 /* ************
12
13 #ifndef _LBTY_INT_H_
14 #define _LBTY_INT_H_
15
16 #include <stdint.h>
17
21
                <u>u8</u>;
<u>u16</u>;
<u>u32</u>;
<u>u64</u>;
24 typedef uint8 t
25 typedef uint1\overline{6} t
26 typedef uint32 t
27 typedef uint64_t
28
               <u>sb</u>
<u>s16;</u>
<u>s32;</u>
<u>s64</u>
30 typedef int8 t
31 typedef int16_t
32 typedef int32 t
33 typedef int64_t
34
36 typedef float
37 typedef double
                  <u>f64</u>;
38
40 typedef u8*
               pu16;
pu32;
pu64;
41 typedef u16*
42 typedef \overline{u32}*
43 typedef <u>u64</u>*
44
46 typedef s8*
                 ps8 ;
47 typedef <u>s16</u>*
               <u>ps16;</u>
<u>ps32;</u>
<u>ps64</u>;
48 typedef \frac{1}{832}*
49 typedef <u>s64</u>*
50
54
60
61 #define LBTY u8vidNOP()
62 #define LBTY NULL
                      ((void *) OU)
63
65 #define LBTY_u8ZERO ((u8)0x00U)
66 #define LBTY_u8MAX ((u8)0xFFU)
67 #define LBTY_s8MAX ((s8)0x7F)
68 #define LBTY_s8MIN ((s8)0x80)
69
70 #define LBTY_u16ZERO ((u16)0x0000U)
71 #define LBTY_u16MAX ((u16)0xFFFFU)
72 #define LBTY_s16MAX ((u16)0x7FFF)
73 #define LBTY_s16MIN ((u16)0x8000)
74
75 #define LBTY_u32ZERO ((u32)0x00000000UL)
76 #define LBTY_u32MAX ((u32)0xFFFFFFFFUL)
77 #define LBTY_s32MAX ((u32)0x7FFFFFFFFL)
77 #define LBTY_s32MAX
78 #define LBTY_s32MIN
                      ((u32)0x7FFFFFFFL)
                   ((u32)0x7FFFFFFFL)
((u32)0x80000000L)
79
```

```
80 #define LBTY u64ZERO ((u64)0x000000000000000ULL)
81 #define LBTY_u64MAX ((u64)0xFFFFFFFFFFFFFFFLLL)

82 #define LBTY_s64MAX ((u64)0x7FFFFFFFFFFFFLL)

83 #define LBTY_s64MIN ((u64)0x8000000000000000LL)
84
87 /* **************
88
90 typedef enum {
    LBTY RESET = 0,
LBTY SET = !LBTY RESET
91
92
93 } LBTY tenuFlagStatus;
94
96 typedef enum {
97 LBTY TRUE = 0x55,
98 \overline{LBTY FALSE} = 0xAA
99 } LBTY_tenuBoolean;
100
102 typedef enum {
     \underline{LBTY OK} = (\underline{u16}) 0,
103
104 <u>LBTY NOK</u>,
105 LBTY NULL POINTER,
106 LBTY INDEX OUT OF RANGE,
107 LBTY NO MASTER CHANNEL,
108 LBTY READ ERROR,
      LBTY READ ERROR,
109 LBTY WRITE ERROR,
110 LBTY UNDEFINED ERROR,
111 LBTY IN PROGRESS
                              /* Error is not available, wait for availability */
112 } LBTY tenuErrorStatus;
113
116 /* ****************
117
119 typedef union {
120 struct {
                       // LSB
      <u>u8</u> <u>m u8b0</u> :1;
121
      <u>u8</u> <u>m u8b1</u> :1;
<u>u8</u> <u>m u8b2</u> :1;
122
123
124
       <u>u8</u> <u>m u8b3</u> :1;
<u>u8</u> <u>m u8b4</u> :1;
125
126
       u8 m u8b5 :1;

    u8
    m
    u8b6
    :1;

    u8
    m
    u8b7
    :1;

127
128
                          // MSB
129 } sBits;
130 <u>u8 u u8Byte</u>;
131 } LBTY tuniPort8;
132
133 typedef union {
134 struct {
    <u>u8</u> <u>m</u> u8b0
       <u>u8</u> <u>m u8b0</u> :1;
u8 <u>m u8b1</u> :1;
135
                            // LSB
136
                  :1;
      u8 m u8b2
u8 m u8b3
137
138
                   :1;
139
     u8 m u8b4 :1;
       <u>u8</u> <u>m u8b5</u>
<u>u8</u> <u>m u8b6</u>
140
                   :1;
                  :1;
141
142
       <u>u8</u> <u>m u8b7</u>
                  :1;
143
        u8 m u8b8
                   :1;
144
       u8 m u8b9 :1;
145
      <u>u8</u> <u>m_u8b10</u> :1;
        u8 m u8b11 :1;
146
     u8 m u8b12 :1;
u8 m u8b13 :1;
u8 m u8b14 :1;
147
148
149
       <u>u8</u> <u>m u8b15</u> :1;
                          // MSB
150
151 } sBits;
152 struct {
    u8 m u8low;
u8 m u8high;
153
154
155 } sBytes;
156
      u16 u u16Word;
157 } LBTY tuniPort16;
158
159 /* ************************
```

# H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC\_BSW/LCTY\_int.h File Reference

#### **Macros**

- #define <a href="https://example.com/LCTY\_PROGMEM">LCTY\_PROGMEM</a> \_\_attribute\_\_((\_\_progmem\_\_))
- #define <u>LCTY\_PURE</u> \_\_attribute\_\_((\_\_pure\_\_))
- #define <u>LCTY INLINE</u> \_\_attribute\_\_((always\_inline)) static inline
- #define <u>LCTY\_INTERRUPT</u> \_\_attribute\_\_((interrupt))
- #define <a href="mailto:ctty">CTY PACKED</a> \_\_attribute\_\_((\_\_packed\_\_))
- #define <u>LCTY\_CONST</u> \_\_attribute\_\_((\_\_const\_\_))
- #define <u>LCTY\_DPAGE</u> \_\_attribute\_\_((dp))
- #define <u>LCTY NODPAGE</u> \_\_attribute\_\_((nodp))
- #define LCTY\_SECTION(section) \_\_attribute\_\_((section( # section)))
- #define LCTY\_ASM(cmd) \_\_asm\_\_ \_volatile\_\_ ( # cmd ::)

#### **Macro Definition Documentation**

```
#define CTY_PACKED __attribute__((__packed__))

#define LCTY_ASM( cmd) __asm____volatile__ ( # cmd ::)

#define LCTY_CONST __attribute__((__const__))

#define LCTY_DPAGE __attribute__((dp))

#define LCTY_INLINE __attribute__((always_inline)) static inline

#define LCTY_INTERRUPT __attribute__((interrupt))

#define LCTY_NODPAGE __attribute__((nodp))

#define LCTY_PROGMEM __attribute__((__progmem__))

#define LCTY_PURE __attribute__((_pure__))

#define LCTY_SECTION( section) __attribute__((section( # section)))
```

## LCTY\_int.h

```
Go to the documentation of this file.1 /*
3 /* ***********
4 /* File Name : LCTY_int.h
5 /* Author : MAAM
6 /* Version : v00
7 /* date : Apr 26, 2023
8 /* description : Compiler Library
9 /* ***********
11 /* ************
12
13 #ifndef LCTY INT H
14 #define LCTY INT H
15
17 /* ***************** TYPE DEF/STRUCT/ENUM SECTION ***************** */
19
23
24 /* prog memory attribute */
25 #define LCTY PROGMEM
                   attribute (( progmem ))
26
27 /* pure attribute */
28 #define LCTY PURE
                   __attribute__((__pure__))
29
30 /* Abstraction for inlining */
31 //#define LCTY_INLINE
                   static inline
32 #define LCTY INLINE
                   __attribute__((always_inline)) static inline
33
34 /* define function as interrupt handler */
                   __attribute__((interrupt))
35 #define LCTY INTERRUPT
36
37 /* Memory packed to pass Memory padding */
38 #define CTY PACKED
                  __attribute__((__packed ))
39
40 /* Const attribute */
41 #define LCTY CONST
                   __attribute__((__const__))
42
43 /* place variable in direct page */
44 #define LCTY_DPAGE
                    attribute ((dp))
45
46 /* do not place variable in direct page */
47 #define LCTY_NODPAGE __attribute__((nodp))
48
49 /* Sections */
50 #define LCTY SECTION(section)
                  attribute ((section( # section)))
51
52 /* Abstraction for assembly command */
53 # define LCTY_ASM(cmd) __asm___volatile__ ( # cmd ::)
54
55 /* *****************
58
62
66
67
68 #endif /* LCTY INT H */
```