SWC_STEPPER

Version v1.0 10/16/2023 5:30:00 PM

Table of Contents

Data Structure Index	2
File Index	3
Data Structure Documentation	4
LBTY_tuniPort16	
LBTY_tuniPort8	
File Documentation	
main.c	
STEPPER_cfg.h	
STEPPER_int.h	
STEPPER_prg.c	
STEPPER_priv.h	
H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC_BSW/LBIT_int.h	
H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC_BSW/LBIT_int.h	
H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC_BSW/LBTY_int.h	
H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC_BSW/LBTY_int.h	
H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC_BSW/LCTY_int.h	32
H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC_BSW/LCTY_int.h	
Index Error! Bookmark no	

Data Structure Index

Data Structures

Here are the data structur	es with brief descriptions:
LBTY_tuniPort16	
LRTV tuniPort8	

File Index

File List

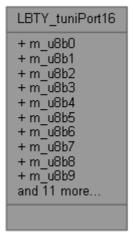
Here is a list of all files with brief descriptions:

8
9
12
15
17
19
24
32

Data Structure Documentation

LBTY tuniPort16 Union Reference

#include <LBTY int.h> Collaboration diagram for LBTY_tuniPort16:



Data Fields

- struct {
- <u>u8</u> <u>m_u8b0</u>:1
- <u>u8 m_u8b1</u>:1
- <u>u8 m u8b2</u>:1
- u8 m_u8b3:1
- <u>u8 m u8b4</u>:1
- u8 m_u8b5:1
- <u>u8 m u8b6</u>:1
- <u>u8 m u8b7</u>:1
- u8 m_u8b8:1
- <u>u8 m u8b9</u>:1
- <u>u8</u> <u>m_u8b10</u>:1
- u8 m u8b11:1 <u>u8 m_u8b12:1</u>
- u8 m_u8b13:1
- <u>u8 m u8b14</u>:1
- <u>u8 m_u8b15</u>:1
- } sBits
- struct {
- u8 m_u8low
- u8 m u8high
- } sBytes
- u16 u u16Word

Field Documentation

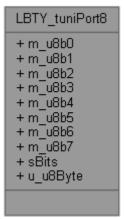
```
u8 m_u8b0
u8 m_u8b1
u8 m_u8b10
u8 m_u8b11
u8 m_u8b12
u8 m_u8b13
u8 m_u8b14
u8 m_u8b15
u8 m_u8b2
u8 m_u8b3
u8 m_u8b4
<u>u8</u> m_u8b5
u8 m_u8b6
u8 m_u8b7
u8 m_u8b8
u8 m_u8b9
u8 m_u8high
u8 m_u8low
struct { ... } sBits
struct { ... } sBytes
<u>u16</u> u_u16Word
```

The documentation for this union was generated from the following file:

• H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC_BSW/<u>LBTY int.h</u>

LBTY_tuniPort8 Union Reference

#include <LBTY_int.h> Collaboration diagram for LBTY_tuniPort8:



Data Fields

- struct {
- <u>u8 m_u8b0</u>:1
- <u>u8 m u8b1</u>:1
- <u>u8 m_u8b2</u>:1
- <u>u8 m u8b3</u>:1
- <u>u8 m_u8b4</u>:1
- <u>u8 m_u8b5</u>:1
- <u>u8</u> <u>m</u> <u>u8b6</u>:1 <u>u8 m_u8b7</u>:1
- } sBits
- $u8 u_u8Byte$

Detailed Description

Union Byte bit by bit

Field Documentation

```
      u8 m_u8b0

      u8 m_u8b1

      u8 m_u8b2

      u8 m_u8b3

      u8 m_u8b4

      u8 m_u8b5

      u8 m_u8b6

      u8 m_u8b7

      struct {...} sBits

      u8 u_u8Byte
```

The documentation for this union was generated from the following file:

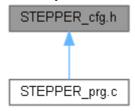
• H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC_BSW/<u>LBTY_int.h</u>

File Documentation

main.c File Reference

STEPPER_cfg.h File Reference

This graph shows which files directly or indirectly include this file:



Macros

- #define STEPPER_COIL_STEP STEPPER_Full_Step
- #define <u>STEPPER_COIL_NORMAL_PIN_Low</u>
- #define <u>STEPPER COIL MAX</u> 4u
- #define <u>STEPPER_PORT_COIL0</u> C
- #define <u>STEPPER PIN COIL0</u> 0u
- #define <u>STEPPER_PORT_COIL1</u> C
- #define <u>STEPPER_PIN_COIL1</u> 1u
- #define STEPPER_PORT_COIL2 C
- #define STEPPER PIN COIL2 2u
- #define <u>STEPPER_PORT_COIL3</u> C
- #define <u>STEPPER_PIN_COIL3</u> 3u
- #define <u>STEPPER_STEP_ANGLE_</u> 10.0f
- #define <u>STEPPER_MIN_STEP_DELAY</u> 125u
- #define <u>STEPPER NUM DELAY</u> 130u

Macro Definition Documentation

#define STEPPER COIL MAX 4u

#define STEPPER_COIL_NORMAL PIN_Low

#define STEPPER_COIL_STEP STEPPER_Full_Step

#define STEPPER_MIN_STEP_DELAY 125u

#define STEPPER_NUM_DELAY 130u

#define STEPPER_PIN_COIL0 0u

#define STEPPER_PIN_COIL1 1u

#define STEPPER_PIN_COIL2 2u

#define STEPPER_PIN_COIL3 3u

#define STEPPER_PORT_COIL0 C

#define STEPPER_PORT_COIL1 C

#define STEPPER_PORT_COIL2 C

#define STEPPER_PORT_COIL3 C

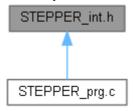
#define STEPPER_STEP_ANGLE 10.0f

STEPPER_cfg.h

```
Go to the documentation of this file.1 /*
3 /* **********
4 /* File Name : STEPPER_cfg.h
11
12 #ifndef STEPPER_CFG_H_
13 #define STEPPER CFG H
14
16 /* ****************** TYPE DEF/STRUCT/ENUM SECTION **************** */
18
22
23 #define STEPPER_COIL_STEP STEPPER_Full_Step 24 #define STEPPER_COIL_NORMAL PIN_Low
25 #define STEPPER COIL MAX
             411
26
27 #define STEPPER_PORT_COIL0
28 #define STEPPER_PIN_COIL0
29
30 #define STEPPER_PORT_COIL1
31 #define STEPPER PIN COIL1
32
33 #define STEPPER_PORT_COIL2
34 #define STEPPER_PIN_COIL2
35
36 #define STEPPER PORT COIL3
            3u
37 #define STEPPER PIN COIL3
38
39 #define STEPPER_STEP_ANGLE 10.0f //1.8f
40 #define STEPPER MIN STEP DELAY 125u
41 #define STEPPER NUM DELAY 130u
42
46
47 /* ***
50
51 /* **
 54
55
```

STEPPER_int.h File Reference

This graph shows which files directly or indirectly include this file:



Enumerations

enum <u>STEPPER_tenuDirection</u> { <u>STEPPER_WithClockWise</u> = (u8)0u,
 <u>STEPPER_OppositClockWise</u>, <u>STEPPER_Stop</u> }

Functions

- void <u>STEPPER_vidInit</u> (void)
- void <u>STEPPER vidCycle</u> (<u>u16</u> u16DelayValue, <u>u8</u> u8Direction)
- void <u>STEPPER_vidStep</u> (<u>u16</u> u16DelayValue, <u>u8</u> u8Direction)

Enumeration Type Documentation

enum STEPPER_tenuDirection

Enumerator:

```
STEPPER_WithCl
ockWise

STEPPER_Opposi
tClockWise

STEPPER_Stop

19 {
20    STEPPER WithClockWise = (u8) 0u,
21    STEPPER OppositClockWise,
22    STEPPER Stop
23 }STEPPER tenuDirection;
```

Function Documentation

void STEPPER_vidCycle (u16 u16DelayValue, u8 u8Direction)

```
73
      switch (u8Direction) {
74
          case STEPPER WithClockWise :
75
          case STEPPER OppositClockWise :
76
             for (u16 i = (360u / STEPPER STEP ANGLE); i--; ) {
                  STEPPER vidStep(ul6DelayValue, u8Direction);
77
78
79
              break;
80
          case STEPPER Stop :
81
          default:
82
              return;
83
84 }
```

Here is the call graph for this function:

```
STEPPER_vidCycle STEPPER_vidStep
```

void STEPPER_vidInit (void)

```
for (u8 i = STEPPER NUM; i--;) {
    GPIO_tstrPinConfig* pTemp =
    (GPIO_tstrPinConfig*) &kau8STEPPERConfiguration LGB[i];
    GPIO_u8PinInit(*pTemp);
    GPIO_u8SetPinValue(pTemp->m_Port, pTemp->m_Pin, pTemp->m_Value);
}
```

void STEPPER_vidStep (u16 u16DelayValue, u8 u8Direction)

```
static u8 u8Coil;
93
94
      switch (u8Direction) {
95
         case STEPPER WithClockWise :
96
             u8Coil = (u8Coil + 1) % ku8StepperNum;
97
               break;
           case STEPPER OppositClockWise :
    u8Coil = u8Coil ? (u8Coil - 1) : (ku8StepperNum - 1);
98
99
100
                break;
             case STEPPER Stop :
101
             default:
102
103
                u8Coil = LBTY u8ZERO;
104
                 return;
105
       }
106
      u8 u8Temp = kau8StepperStep[u8Coil];
GPIO_tstrPinConfig* pTemp =
107
108
(GPIO_tstrPinConfig*) kau8STEPPERConfiguration LGB;
109 for (\underline{u8} i = \underline{STEPPER NUM}; i--; pTemp++) {
110
            GPIO_u8SetPinValue(pTemp->m_Port, pTemp->m_Pin, ((u8Temp & (1<<i))) ?
STEPPER COIL Set : STEPPER COIL Reset));
111
112
113
        vidMyDelay ms(u16DelayValue);
114 }
```

Here is the caller graph for this function:



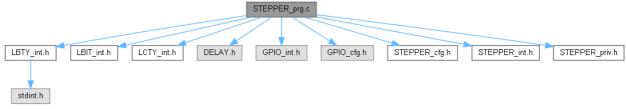
STEPPER_int.h

```
Go to the documentation of this file.1 /*
3 /* ************
4 /* File Name : STEPPER_int.h
11
12 #ifndef STEPPER_INT_H_
13 #define STEPPER INT H
14
18
19 typedef enum{
 STEPPER WithClockWise = (u8)0u,
STEPPER OppositClockWise,
STEPPER Stop
20
21
22
23 }STEPPER tenuDirection;
24
25 /* *
 /* *********************** MACRO/DEFINE SECTION *********************************
26
27 /* ***
28
29 /*
32
36
37 /*
40
41 /*
*/
46 extern void STEPPER vidInit(void);
47
48 /* *****************
49 /* Description : Stepper Run One Cycle
50 /* Input : u16DelayValue, u8Direction
51 /* Return : void
53 extern void <a href="STEPPER vidCycle">STEPPER vidCycle</a> (u16 u16DelayValue, u8 u8Direction);
54
55 /* ********
56 /* Description : Stepper Run Value
57 /* Input :
         u16DelayValue, u8Direction
58 /* Return
         void
59 /* *****************
60 extern void <a href="STEPPER vidStep">STEPPER vidStep</a> (u16 u16DelayValue, u8 u8Direction);
61
62
```

STEPPER_prg.c File Reference

```
#include "LBTY_int.h"
#include "LBIT_int.h"
#include "LCTY_int.h"
#include "DELAY.h"
#include "GPIO_int.h"
#include "GPIO_cfg.h"
#include "STEPPER_cfg.h"
#include "STEPPER_int.h"
#include "STEPPER_int.h"
#include "STEPPER_priv.h"
```

Include dependency graph for STEPPER_prg.c:



Functions

- void <u>STEPPER_vidInit</u> (void)
- void <u>STEPPER vidCycle</u> (<u>u16</u> u16DelayValue, <u>u8</u> u8Direction)
- void <u>STEPPER_vidStep</u> (<u>u16</u> u16DelayValue, <u>u8</u> u8Direction)

Variables

- const GPIO_tstrPinConfig kau8STEPPERConfiguration LGB [STEPPER NUM]
- const <u>u8</u> <u>kau8StepperStep</u> [<u>STEPPER_NUM</u> *2u]
- const <u>u8</u> <u>ku8StepperNum</u>

Function Documentation

void STEPPER_vidCycle (u16 u16DelayValue, u8 u8Direction)

```
73
       switch (u8Direction) {
74
         case STEPPER WithClockWise :
75
           case STEPPER OppositClockWise :
               for(u16 i = (360u / STEPPER STEP ANGLE) ; i-- ; ){
76
77
                   STEPPER vidStep (u16DelayValue, u8Direction);
78
79
              break;
80
          case STEPPER Stop :
81
          default:
82
               return;
83
84 }
```

Here is the call graph for this function:

```
STEPPER_vidCycle STEPPER_vidStep
```

void STEPPER_vidInit (void)

```
64 }
65 }
```

void STEPPER_vidStep (u16 u16DelayValue, u8 u8Direction)

```
static u8 u8Coil;
93
94
      switch (u8Direction) {
95
         case STEPPER WithClockWise :
              u8Coil = (u8Coil + 1) % ku8StepperNum;
96
97
               break;
           case STEPPER OppositClockWise :
   u8Coil = u8Coil ? (u8Coil - 1) : (ku8StepperNum - 1);
98
99
100
                break;
            case STEPPER Stop :
101
102
            default:
103
                u8Coil = LBTY u8ZERO;
104
                 return;
105
       }
106
107
108
      u8 u8Temp = kau8StepperStep[u8Coil];
GPIO_tstrPinConfig* pTemp =
(GPIO_tstrPinConfig*) kau8STEPPERConfiguration LGB;
for (u8 i = STEPPER NUM ; i-- ; pTemp++) {
            GPIO_u8SetPinValue(pTemp->m_Port, pTemp->m_Pin, ((u8Temp & (1<<i))) ?
110
STEPPER COIL Set : STEPPER COIL Reset));
111
112
        vidMyDelay ms(u16DelayValue);
113
114 }
```

Here is the caller graph for this function:



Variable Documentation

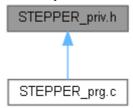
const GPIO_tstrPinConfig
kau8STEPPERConfiguration_LGB[STEPPER_NUM][extern]

const u8 kau8StepperStep[STEPPER_NUM *2u][extern]

const u8 ku8StepperNum [extern]

STEPPER_priv.h File Reference

This graph shows which files directly or indirectly include this file:



Macros

- #define STEPPER_NUM 4u
- #define <u>STEPPER Half Step</u> Ou
- #define <u>STEPPER Full Step</u> 1u

Enumerations

 enum <u>STEPPER tenuState</u> { <u>STEPPER COIL Reset</u> = STEPPER_COIL_NORMAL, <u>STEPPER_COIL_Set</u> = !STEPPER_COIL_Reset }

Macro Definition Documentation

#define STEPPER_Full_Step 1u

#define STEPPER_Half_Step 0u

#define STEPPER_NUM 4u

Enumeration Type Documentation

enum <u>STEPPER_tenuState</u>

Enumerator:

```
STEPPER_COIL_
Reset

STEPPER_COIL_
Set

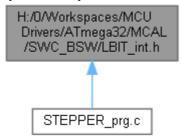
19 {
20 STEPPER COIL Reset = STEPPER COIL NORMAL,
21 STEPPER COIL Set = !STEPPER COIL Reset
22 }STEPPER tenuState;
```

STEPPER_priv.h

```
Go to the documentation of this file.1 /*
3 /* **********
4 /* File Name : STEPPER_priv.h
11
12 #ifndef STEPPER_PRIV_H_
13 #define STEPPER PRIV H
14
18
19 typedef enum{
STEPPER COIL Reset = STEPPER COIL NORMAL,
STEPPER COIL Set = !STEPPER COIL Reset
20
21
22 }STEPPER tenuState;
23
24 #define STEPPER NUM
25 #define STEPPER_Half_Step
       0u
26 #define STEPPER Full Step
27
31
34 /* ****************
35
39
40 /* *********************** */
43
```

H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC_BSW/LBIT_int.h File Reference

This graph shows which files directly or indirectly include this file:



Macros

- #define _BV(bit) (1u<<(bit))
- #define <u>SET_BIT(REG</u>, bit) ((REG) |= (1u<<(bit)))
- #define CLR BIT(REG, bit) ((REG) &= \sim (1u<<(bit)))
- #define TOG BIT(REG, bit) ((REG) ^= (1u<<(bit)))
- #define $\underline{SET}\underline{BYTE}(REG, bit)$ ((REG) |= (0xFFu << (bit)))
- #define $\underline{\text{CLR_BYTE}}(\text{REG}, \text{ bit}) ((\text{REG}) \&= \sim (0xFFu << (\text{bit})))$
- #define TOG BYTE(REG, bit) ((REG) ^= (0xFFu<<(bit)))
- #define SET_MASK(REG, MASK) ((REG) |= (MASK))
- #define CLR MASK(REG, MASK) ((REG) &= ~(MASK))
- #define TOG_MASK(REG, MASK) ((REG) ^= (MASK))
- #define GET_MASK(REG, MASK) ((REG) & (MASK))
- #define SET REG(REG) ((REG) = \sim (0u))
- #define $\underline{CLR}_REG(REG)$ ((REG) = (0u))
- #define \underline{TOG} REG(REG) ((REG) $^=$ ~(0u))
- #define GET_BIT(REG, bit) (((REG)>>(bit)) & 0x01u)
- #define GET_NIB(REG, bit) (((REG)>>(bit)) & 0x0Fu)
- #define GET BYTE(REG, bit) (((REG)>>(bit)) & 0xFFu)
- #define <u>ASSIGN_BIT</u>(REG, bit, value) $((REG) = ((REG) \& \sim (0x01u << (bit))) | (((value) \& 0x01u) << (bit)))$
- #define <u>ASSIGN_NIB</u>(REG, bit, value) $((REG) = ((REG) \& \sim (0x0Fu << (bit))) | (((value) \& 0x0Fu) << (bit)))$
- #define <u>ASSIGN BYTE</u>(REG, bit, value) $((REG) = ((REG) \& \sim (0xFFu << (bit))))$ (((value) & 0xFFu) << (bit)))
- #define CON u8Bits(b7, b6, b5, b4, b3, b2, b1, b0)

(0b##b7##b6##b5##b4##b3##b2##b1##b0)

• #define <u>CON_u16Bits</u>(b15, b14, b13, b12, b11, b10, b9, b8, b7, b6, b5, b4, b3, b2, b1, b0)

(0b##b15##b14##b13##b12##b11##b10##b9##b8##b7##b6##b5##b4##b3##b2##b11##b0)

Macro Definition Documentation

```
#define BV(bit) (1u<<(bit))
#define ASSIGN_BIT( REG, bit, value) ((REG) = ((REG) & \sim(0x01u<<(bit)))
                                                                            I
(((value) & 0x01u)<<(bit)))
#define ASSIGN BYTE( REG, bit, value) ((REG) = ((REG) & ~(0xFfu<<(bit)))
                                                                            Τ
(((value) & 0xFFu)<<(bit)))
#define ASSIGN_NIB( REG, bit, value) ((REG) = ((REG) & \sim(0x0Fu<<(bit)))
                                                                            I
(((value) & 0x0Fu)<<(bit)))
#define CLR_BIT( REG, bit) ((REG) &= ~(1u<<(bit)))
#define CLR_BYTE( REG, bit) ((REG) &= ~(0xFFu<<(bit)))
#define CLR_MASK( REG, MASK) ((REG) &= ~(MASK))
#define CLR_REG( REG) ((REG) = (0u))
#define CON_u16Bits( b15, b14, b13, b12, b11, b10, b9, b8, b7, b6, b5,
b4, b3, b2, b1, b0)
       (0b##b15##b14##b13##b12##b11##b10##b9##b8##b7##b6##b5##b4##b3##b2##
b1##b0)
#define CON_u8Bits( b7, b6, b5, b4, b3, b2, b1, b0)
      (0b##b7##b6##b5##b4##b3##b2##b1##b0)
#define GET_BIT( REG, bit) (((REG)>>(bit)) & 0x01u)
#define GET_BYTE( REG, bit) (((REG)>>(bit)) & 0xFFu)
#define GET_MASK( REG, MASK) ((REG) & (MASK))
#define GET_NIB( REG, bit) (((REG)>>(bit)) & 0x0Fu)
#define SET_BIT( REG, bit) ((REG) |= (1u<<(bit)))
   Bitwise Operation
```

```
#define SET_BYTE( REG, bit) ((REG) |= (0xFFu<<(bit)))

#define SET_MASK( REG, MASK) ((REG) |= (MASK))

#define SET_REG( REG) ((REG) = ~(0u))

#define TOG_BIT( REG, bit) ((REG) ^= (1u<<(bit)))

#define TOG_BYTE( REG, bit) ((REG) ^= (0xFFu<<(bit)))

#define TOG_MASK( REG, MASK) ((REG) ^= (MASK))

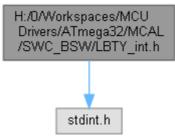
#define TOG_REG( REG) ((REG) ^= ~(0u))
```

LBIT_int.h

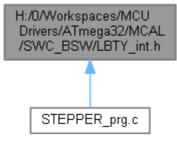
```
Go to the documentation of this file.1 /*
3 /* **********
4 /* File Name : LBIT_int.h
5 /* Author : MAAM
6 /* Version : v01
7 /* date : Mar 24, 2023
8 \ /* \ description : Bitwise Library
9 /* *********
11 /* ***********
12
13 #ifndef LBIT INT H
14 #define LBIT INT H
15
17 /* ***************** TYPE DEF/STRUCT/ENUM SECTION **************** */
19
23
24 #define _BV(bit)
                                              (1u<<(bit))
25
27 #define SET BIT(REG, bit)
                                           ((REG) \mid = (1u << (bit)))
28 #define CLR BIT(REG, bit)
                                           ((REG) &= ~(1u<<(bit)))
29 #define TOG_BIT(REG, bit)
                                           ((REG) ^= (1u<<(bit)))
30
                                          ((REG) |= (0xFFu<<(bit)))
((REG) &= ~(0xFFu<<(bit)))
31 #define SET_BYTE(REG, bit)
32 #define CLR BYTE (REG, bit)
33 #define TOG BYTE (REG, bit)
                                           ((REG) ^= (0xFFu<<(bit)))
34
                                           ((REG) |= (MASK))
35 #define SET MASK (REG, MASK)
36 #define CLR MASK (REG, MASK)
                                           ((REG) &= ~(MASK))
                                           ((REG) ^= (MASK))
((REG) & (MASK))
37 #define TOG_MASK(REG, MASK)
38 #define GET MASK(REG, MASK)
39
                                           ((REG) = \sim (0u))
((REG) = (0u))
40 #define SET REG(REG)
41 #define CLR REG(REG)
42 #define TOG REG(REG)
                                           ((REG) ^= \sim (Ou))
43
44 #define GET BIT(REG, bit)
                                           (((REG) >> (bit)) \& 0x01u)
45 #define GET NIB(REG, bit)
                                           (((REG)>>(bit)) & 0x0Fu)
46 #define GET BYTE (REG, bit)
                                           (((REG)>>(bit)) & 0xFFu)
47
48 #define ASSIGN BIT (REG, bit, value)
                                          ((REG) = ((REG) \& \sim (0x01u << (bit)))
| (((value) \& 0x01u) << (bit)))
49 #define ASSIGN NIB(REG, bit, value)
                                          ((REG) = ((REG) \& \sim (0x0Fu << (bit)))
| (((value) & 0x0Fu)<<(bit)))
50 #define ASSIGN_BYTE(REG, bit, value)
                                          ((REG) = ((REG) & \sim (0xFFu << (bit)))
| (((value) & 0xFFu) << (bit)))
51
52 /*
53 #define ASSIGN BIT(REG, bit, value)
                                           do{
54
                                            REG &= \sim (0 \times 01 u << bit);
55
                                            REG \mid = ((value & 0x01u)<<bit);
56
                                           }while(0)
57 */
58
       bits together in an u8 register
59 /*
60 #define CON_u8Bits(b7, b6, b5, b4, b3, b2, b1, b0)
61
(0b##b7##b6##b5##b4##b3##b2##b1##b0)
62
63 /* bits together in an ul6 register
64 #define CON u16Bits(b15, b14, b13, b12, b11, b10, b9, b8, b7, b6, b5, b4, b3, b2, b1,
b0) \
```

H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC_BSW/LBTY_int.h File Reference

#include <stdint.h>
Include dependency graph for LBTY_int.h:



This graph shows which files directly or indirectly include this file:



Data Structures

• union LBTY tuniPort8union LBTY tuniPort16

Macros

- #define __IO volatile
- #define __O volatile
- #define __I volatile const
- #define <u>LBTY_u8vidNOP()</u>
- #define <u>LBTY NULL</u> ((void *) 0U)
- #define $\underline{LBTY_u8ZERO}$ (($\underline{u8}$)0x00U)
- #define <u>LBTY u8MAX</u> ((<u>u8</u>)0xFFU)
- #define LBTY $\underline{\text{S8MAX}}$ (($\underline{\text{s8}}$)0x7F)
- #define <u>LBTY_s8MIN</u> ((<u>s8</u>)0x80)
- #define <u>LBTY u16ZERO</u> ((<u>u16</u>)0x0000U)
- #define <u>LBTY_u16MAX</u> ((<u>u16</u>)0xFFFFU)
- #define LBTY s16MAX ((u16)0x7FFF)
- #define LBTY s16MIN ((u16)0x8000)
- #define <u>LBTY u32ZERO</u> ((<u>u32</u>)0x0000000UL)
- #define <u>LBTY u32MAX</u> ((<u>u32</u>)0xFFFFFFFUL)
- #define <u>LBTY_s32MAX</u> ((<u>u32</u>)0x7FFFFFFL)
- #define <u>LBTY s32MIN</u> ((<u>u32</u>)0x80000000L)
- #define <u>LBTY_u64ZERO</u> ((<u>u64</u>)0x000000000000000ULL)
- #define <u>LBTY u64MAX</u> ((<u>u64</u>)0xFFFFFFFFFFFFFFULL)
- #define <u>LBTY_s64MAX</u> ((<u>u64</u>)0x7FFFFFFFFFFFFFLL)
- #define <u>LBTY_s64MIN</u> ((u64)0x8000000000000000LL)

Typedefs

- typedef uint8 t u8
- typedef uint16_t <u>u16</u>
- typedef uint32_t <u>u32</u>
- typedef uint64_t <u>u64</u>
- typedef int8_t <u>s8</u>
- typedef int16_t <u>s16</u>
- typedef int32_t <u>s32</u>
- typedef int64_t s64
- typedef float <u>f32</u>
- typedef double <u>f64</u>
- typedef <u>u8</u> * <u>pu8</u>
- typedef <u>u16</u> * <u>pu16</u>
- typedef <u>u32</u> * <u>pu32</u>
- typedef <u>u64</u> * <u>pu64</u>
- typedef $\underline{s8} * \underline{ps8}$
- typedef <u>s16</u> * <u>ps16</u>
- typedef $\underline{s32} * \underline{ps32}$
- typedef <u>s64</u> * <u>ps64</u>

Enumerations

- enum <u>LBTY_tenuFlagStatus</u> { <u>LBTY_RESET</u> = 0, <u>LBTY_SET</u> = !LBTY_RESET }
- enum <u>LBTY tenuBoolean</u> { <u>LBTY TRUE</u> = 0x55, <u>LBTY FALSE</u> = 0xAA }
- enum <u>LBTY_tenuErrorStatus</u> { <u>LBTY_OK</u> = (u16)0, <u>LBTY_NOK</u>, <u>LBTY_NULL_POINTER</u>, <u>LBTY_INDEX_OUT_OF_RANGE</u>, <u>LBTY_NO_MASTER_CHANNEL</u>, <u>LBTY_READ_ERROR</u>, <u>LBTY_WRITE_ERROR</u>, <u>LBTY_UNDEFINED_ERROR</u>, <u>LBTY_IN_PROGRESS</u> }

Macro Definition Documentation

```
#define I volatile const
#define __IO volatile
#define O volatile
#define LBTY_NULL ((void *) 0U)
#define LBTY_s16MAX ((u16)0x7FFF)
#define LBTY_s16MIN ((u16)0x8000)
#define LBTY_s32MAX ((u32)0x7FFFFFFL)
#define LBTY_s32MIN ((<u>u32</u>)0x80000000L)
#define LBTY_s64MAX ((u64)0x7FFFFFFFFFFFLL)
#define LBTY s64MIN ((u64)0x800000000000000LL)
#define LBTY_s8MAX ((s8)0x7F)
#define LBTY_s8MIN ((s8)0x80)
#define LBTY_u16MAX ((u16)0xFFFFU)
#define LBTY_u16ZERO ((<u>u16</u>)0x0000U)
#define LBTY_u32MAX ((u32)0xFFFFFFFUL)
#define LBTY_u32ZERO ((<u>u32</u>)0x0000000UL)
#define LBTY_u64MAX ((u64)0xFFFFFFFFFFFFFULL)
#define LBTY_u64ZERO ((<u>u64</u>)0x00000000000000ULL)
#define LBTY_u8MAX ((u8)0xFFU)
#define LBTY_u8vidNOP()
#define LBTY_u8ZERO ((u8)0x00U)
   Data Types Limitation
```

Typedef Documentation

typedef float f32

Standard Real Decimal number

```
typedef double f64
typedef s16* ps16
typedef s32* ps32
typedef <u>s64</u>* <u>ps64</u>
typedef s8* ps8
   Standard Pointer to Signed Byte/Word/Long_Word
typedef u16* pu16
typedef u32* pu32
typedef u64* pu64
typedef u8* pu8
   Standard Pointer to Unsigned Byte/Word/Long_Word
typedef int16_t s16
typedef int32_t s32
typedef int64_t s64
typedef int8_t s8
   Standard Signed Byte/Word/Long_Word
typedef uint16_t u16
typedef uint32_t u32
typedef uint64_t u64
typedef uint8_t u8
   Data Types New Definitions Standard Unsigned Byte/Word/Long_Word
```

Enumeration Type Documentation

enum <u>LBTY_tenuBoolean</u>

Boolean type

Enumerator:

```
LBTY_TRUE

LBTY_FALSE

96 {
97  LBTY TRUE = 0x55,
98  LBTY FALSE = 0xAA
99 } LBTY tenuBoolean;
```

enum LBTY_tenuErrorStatus

Error Return type

Enumerator:

```
LBTY_OK
       LBTY_NOK
  LBTY_NULL_PO
            INTER
  LBTY_INDEX_O
   UT_OF_RANGE
   LBTY_NO_MAS
   TER_CHANNEL
  LBTY_READ_ER
              ROR
  LBTY_WRITE_E
             RROR
  LBTY_UNDEFIN
       ED_ERROR
  LBTY_IN_PROG
             RESS
102
103 LBTY OK = (u16)0,
104 LBTY NOK,
105 LBTY NULL POINTER,
106 LBTY INDEX OUT OF RANGE,
107 LBTY NO MASTER CHANNEL,
107 LBTY NO MASTER CHANNEL,
108 LBTY READ ERROR,
      LBTY WRITE ERROR,
LBTY UNDEFINED ERROR,
109
110
111 LBTY IN PROGRESS
                                /* Error is not available, wait for availability */
112 } LBTY tenuErrorStatus;
```

enum <u>LBTY_tenuFlagStatus</u>

Flag Status type

Enumerator:

```
LBTY_RESET

LBTY_SET

90 {
91    LBTY RESET = 0,
92    LBTY SET = !LBTY RESET
93 } LBTY_tenuflagStatus;
```

LBTY int.h

```
Go to the documentation of this file.1 /*
3 /* ***********
4 /* File Name : LBTY_int.h
5 /* Author : MAAM
6 /* Version : v01
7 /* date : Mar 23, 2023
8 /* description : Basic Library
9 /* **********
11 /* ************
12
13 #ifndef _LBTY_INT_H_
14 #define _LBTY_INT_H_
15
16 #include <stdint.h>
17
21
               <u>u8</u>;
<u>u16</u>;
<u>u32</u>;
<u>u64</u>;
24 typedef uint8 t
25 typedef uint1\overline{6} t
26 typedef uint32 t
27 typedef uint64_t
28
               <u>sb</u>
<u>s16;</u>
<u>s32;</u>
<u>s64</u>
30 typedef int8 t
31 typedef int16_t
32 typedef int32 t
33 typedef int64_t
34
36 typedef float
37 typedef double
                 <u>f64</u>;
38
40 typedef u8*
              pu16;
pu32;
pu64;
41 typedef u16*
42 typedef \overline{u32}*
43 typedef <u>u64</u>*
44
46 typedef s8*
                ps8 ;
47 typedef <u>s16</u>*
              <u>ps16;</u>
<u>ps32;</u>
<u>ps64</u>;
48 typedef \frac{1}{832}*
49 typedef <u>s64</u>*
50
54
60
61 #define LBTY u8vidNOP()
62 #define LBTY NULL
                    ((void *) OU)
63
65 #define LBTY_u8ZERO ((u8)0x00U)
66 #define LBTY_u8MAX ((u8)0xFFU)
67 #define LBTY_s8MAX ((s8)0x7F)
68 #define LBTY_s8MIN ((s8)0x80)
69
70 #define LBTY_u16ZERO ((u16)0x0000U)
71 #define LBTY_u16MAX ((u16)0xFFFFU)
72 #define LBTY_s16MAX ((u16)0x7FFF)
73 #define LBTY_s16MIN ((u16)0x8000)
74
75 #define LBTY_u32ZERO ((u32)0x00000000UL)
76 #define LBTY_u32MAX ((u32)0xFFFFFFFFUL)
77 #define LBTY_s32MAX ((u32)0x7FFFFFFFFL)
77 #define LBTY_s32MAX
78 #define LBTY_s32MIN
                     ((u32)0x7FFFFFFFL)
                  ((u32)0x7FFFFFFFL)
((u32)0x80000000L)
79
```

```
80 #define LBTY u64ZERO ((u64)0x000000000000000ULL)
81 #define LBTY_u64MAX ((u64)0xFFFFFFFFFFFFFFFLUL)

82 #define LBTY_s64MAX ((u64)0x7FFFFFFFFFFFFLL)

83 #define LBTY_s64MIN ((u64)0x80000000000000000LL)
84
87 /* ***************
88
90 typedef enum {
    LBTY RESET = 0,
LBTY SET = !LBTY RESET
91
92
93 } LBTY tenuFlagStatus;
94
96 typedef enum {
97 LBTY TRUE = 0x55,
98 \overline{LBTY FALSE} = 0xAA
99 } LBTY_tenuBoolean;
100
102 typedef enum {
     \underline{LBTY OK} = (\underline{u16}) 0,
103
104 <u>LBTY NOK</u>,
105 LBTY NULL POINTER,
106 LBTY INDEX OUT OF RANGE,
107 LBTY NO MASTER CHANNEL,
108 LBTY READ ERROR,
      LBTY READ ERROR,
109 LBTY WRITE ERROR,
110 LBTY UNDEFINED ERROR,
111 LBTY IN PROGRESS
                              /* Error is not available, wait for availability */
112 } LBTY tenuErrorStatus;
113
116 /* ****************
117
119 typedef union {
120 struct {
                       // LSB
      <u>u8</u> <u>m u8b0</u> :1;
121
      <u>u8</u> <u>m u8b1</u> :1;
<u>u8</u> <u>m u8b2</u> :1;
122
123
124
       <u>u8</u> <u>m u8b3</u> :1;
<u>u8</u> <u>m u8b4</u> :1;
125
126
       u8 m u8b5 :1;

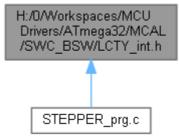
    u8
    m
    u8b6
    :1;

    u8
    m
    u8b7
    :1;

127
128
                         // MSB
129 } sBits;
130 <u>u8 u u8Byte</u>;
131 } LBTY tuniPort8;
132
133 typedef union {
134 struct {
    <u>u8</u> <u>m</u> u8b0
       <u>u8</u> <u>m u8b0</u> :1;
u8 <u>m u8b1</u> :1;
135
                           // LSB
136
                  :1;
      u8 m u8b2
u8 m u8b3
137
138
                   :1;
139
     u8 m u8b4 :1;
       <u>u8</u> <u>m u8b5</u>
<u>u8</u> <u>m u8b6</u>
140
                   :1;
                  :1;
141
142
       <u>u8</u> <u>m u8b7</u>
                  :1;
143
       u8 m u8b8
                  :1;
144
       u8 m u8b9 :1;
145
      <u>u8</u> <u>m_u8b10</u> :1;
       u8 m u8b11 :1;
146
<u>u8</u> <u>m u8b15</u> :1;
                          // MSB
150
151 } sBits;
152 struct {
    u8 m u8low;
u8 m u8high;
153
154
155 } sBytes;
156
      u16 u u16Word;
157 } LBTY tuniPort16;
158
159 /* ***************************
```

H:/0/Workspaces/MCU Drivers/ATmega32/MCAL/SWC_BSW/LCTY_int.h File Reference

This graph shows which files directly or indirectly include this file:



Macros

- #define LCTY_PROGMEM __attribute__((__progmem__))
- #define <u>LCTY_PURE</u> __attribute__((__pure__))
- #define <u>LCTY_INLINE</u> __attribute__((always_inline)) static inline
- #define <u>LCTY INTERRUPT</u> __attribute__((interrupt))
- #define <u>CTY_PACKED</u> __attribute__((__packed__))
- #define LCTY_CONST __attribute__((__const__))
- #define <u>LCTY_DPAGE</u> __attribute__((dp))
- #define <u>LCTY_NODPAGE</u> __attribute__((nodp))
- #define <u>LCTY_SECTION</u>(section) __attribute__((section(# section)))
- #define LCTY_ASM(cmd) __asm__ _volatile__ (# cmd ::)

Macro Definition Documentation

```
#define CTY_PACKED __attribute__((__packed__))

#define LCTY_ASM( cmd) __asm___volatile__ ( # cmd ::)

#define LCTY_CONST __attribute__((__const__))

#define LCTY_DPAGE __attribute__((dp))

#define LCTY_INLINE __attribute__((always_inline)) static inline

#define LCTY_INTERRUPT __attribute__((interrupt))

#define LCTY_NODPAGE __attribute__((nodp))

#define LCTY_PROGMEM __attribute__((__progmem__))

#define LCTY_PURE __attribute__((_pure__))

#define LCTY_SECTION( section) __attribute__((section( # section)))
```

LCTY_int.h

```
Go to the documentation of this file.1 /*
3 /* ***********
4 /* File Name : LCTY_int.h
5 /* Author : MAAM
6 /* Version : v00
7 /* date : Apr 26, 2023
8 /* description : Compiler Library
9 /* ***********
11 /* ************
12
13 #ifndef LCTY INT H
14 #define LCTY INT H
15
17 /* ***************** TYPE DEF/STRUCT/ENUM SECTION **************** */
19
21 /* ****************** MACRO/DEFINE SECTION **********************************
23
24 /* prog memory attribute */
25 #define LCTY PROGMEM
                    attribute (( progmem ))
26
27 /* pure attribute */
28 #define LCTY PURE
                    __attribute__((__pure__))
29
30 /* Abstraction for inlining */
31 //#define LCTY_INLINE
                    static inline
32 #define LCTY INLINE
                    __attribute__((always_inline)) static inline
33
34 /* define function as interrupt handler */
                    __attribute__((interrupt))
35 #define LCTY INTERRUPT
36
37 /* Memory packed to pass Memory padding */
38 #define CTY PACKED
                   __attribute__((__packed ))
39
40 /* Const attribute */
41 #define LCTY CONST
                    __attribute__((__const__))
42
43 /* place variable in direct page */
44 #define LCTY_DPAGE
                     attribute ((dp))
45
46 /* do not place variable in direct page */
47 #define LCTY_NODPAGE __attribute__((nodp))
48
49 /* Sections */
50 #define LCTY SECTION(section)
                   attribute ((section( # section)))
51
52 /* Abstraction for assembly command */
53 # define LCTY_ASM(cmd) __asm____volatile__ ( # cmd ::)
54
55 /* *****************
58
62
66
67
68 #endif /* LCTY INT H */
```