Soft Buses

**Transceivers** 

PCle

## **DDR3** Controller DDR3 Controller Fast prefabricated wires with timing Physical distance 100s of bitsclosure guarantees affects speed to I/Os Bus 1 Module 1 Module 1 Hard routers Controller **Ethernet Controller** switch data Pipeline Module 4 efficiently Regs ansceive Module 4 Ethernet Bus 3 Flexible interface adapts width, PCle frequency and **5 5** 100 protocol between 100 Module 2 Module 2 Module 3 Module 3 NoC and FPGA Bus 2 Direct links to I/O interfaces **DDR3** Controller **DDR3** Controller

## Embedded NoC