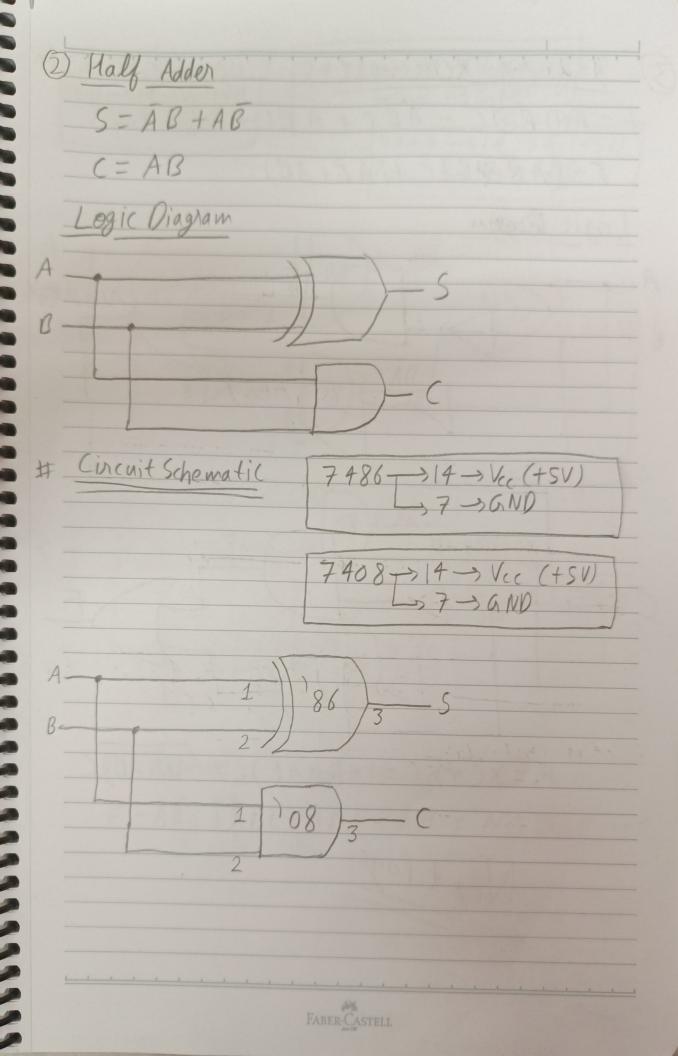
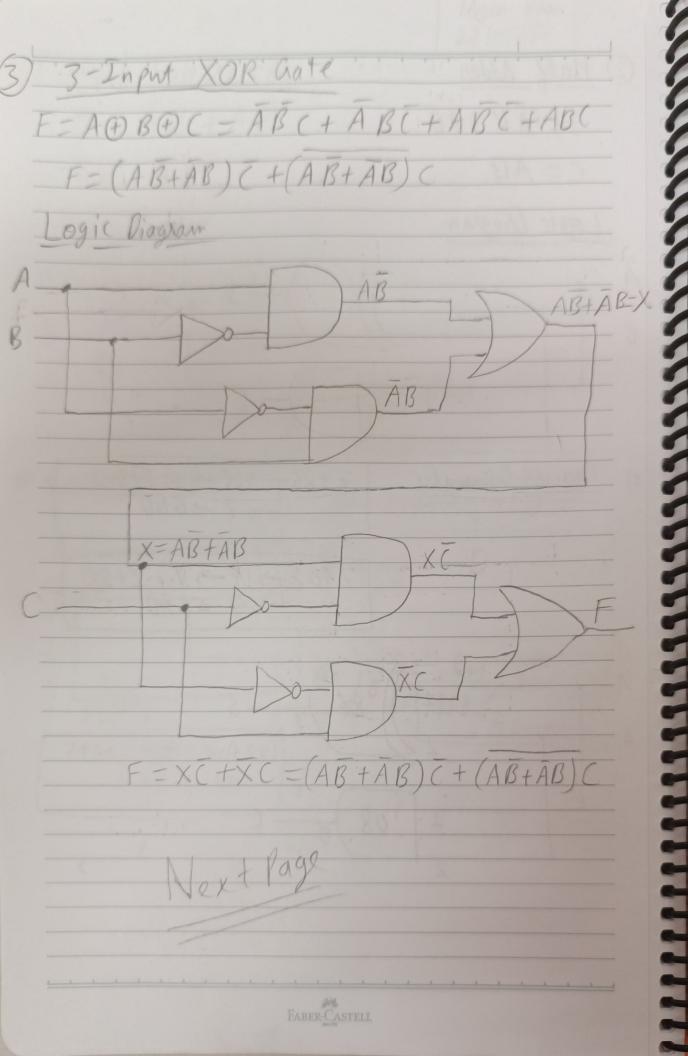
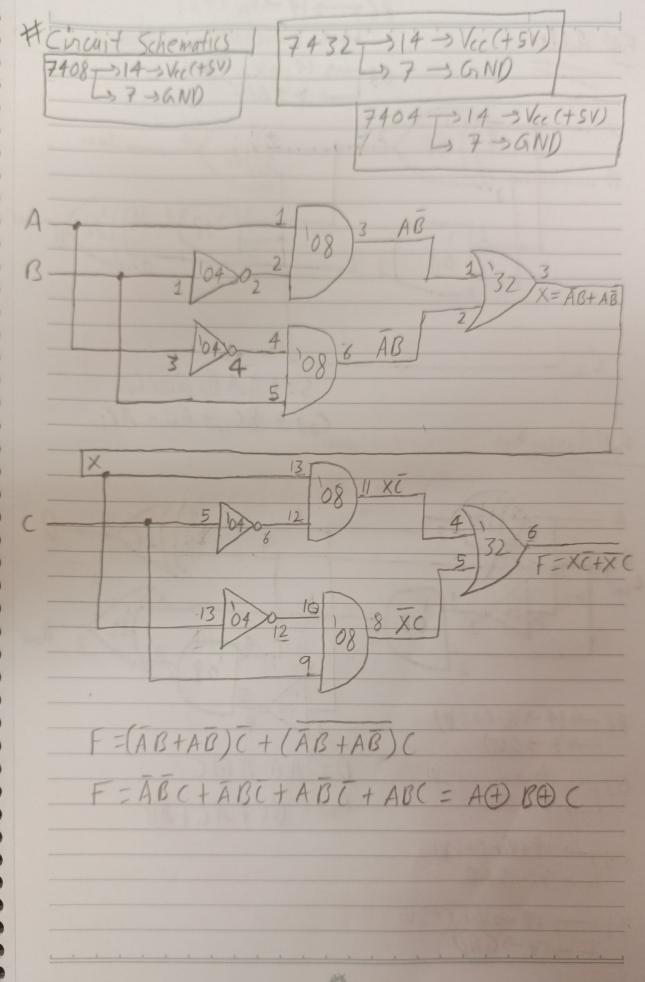
Moin Khan 22101287 Half Subtractor D = AB + AB Bout = AB Logic Diagram A Bout # Circuit Schematic 7486 186 3 908 ->14-> Vec(+5V) >7->6ND FABER-CASTELL







FABER-CASTELL