Accelerator for Event-based Failure Prediction Subtitle

Master's Thesis submitted to the

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Master of Science in Informatics
Embedded Systems Design

presented by Simon Maurer

under the supervision of Prof. Miroslaw Malek

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I certify that except where due acknowledgement has been given, the work presented in this thesis is that of the author alone; the work has not been submitted previously, in whole or in part, to qualify for any other academic award; and the content of the thesis is the result of work which has been carried out since the official commencement date of the approved research program.

Simon Maurer Lugano, 29. Janaury 2014

Abstract

Acknowledgements

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Contents

Co	nten	ts	vii
Lis	st of l	Figures	ix
Lis	st of T	Tables	хi
1	Intr	oduction	1
	1.1	Problem Statement	1
	1.2	Motivation	1
	1.3	Contributions	1
	1.4	Document Structure	1
2	Stat	e of the Art	3
	2.1	Failure Prediction	3
	2.2	Accelerator	3
3	Evei	nt-based Failure Prediction	5
	3.1	Data Processing	5
	3.2	Model Training	5
	3.3	Sequence Processing	6
	3.4	Classification	7
4	Acce	eleration	9
	4.1	Theoretical Analysis	10
		4.1.1 Pre-Computation of Known Parameters	10
		4.1.2 Serial Implementation	11
		4.1.3 Parallelization	13
		4.1.4 Precision	13
	4.2	Choice of Accelerator Type	13
		4.2.1 CPU	13
		4.2.2 GPU	13
		4.2.3 FPGA	13
		4.2.4 ASIC	14
		4.2.5 Conclusion	14
	4.3	Implementation	14

viii	Contents

5	Testing and Verification 5.1 Log Standard	15 15				
6	Results 6.1 Speedup	17 17 17				
7	Conclusion 7.1 Achievements	19 19 19				
A	Some material	21				
Bil	Bibliography					

Figures

x Figures

Tables

xii Tables

Introduction

1.1 Problem Statement

1.2 Motivation

The email of Felix left some doubts to whether the acceleration of the algorithm is useful. The following list will give some arguments to justify the work.

Too many parameters to be identified, estimated and set

Considering an embedded system, this is usually not a problem because the parameters are defined during the design phase and will never be changed afterwards.

Limited performance scalability

There are studies available claiming otherwise. The discussion of Neumanns work will provide some arguments against this statement.

Industry trends point towards cloud

In embedded systems it will still be beneficial to predict failures of single nodes. It is however important to keep the power and computational footprint low. This will be one of the major challenges. On the other hand, I think it would also be possible to also use this algorithm to monitor a distributed system and predict failures. It is only a matter of getting defining the events to feed to the algorithm.

1.3 Contributions

1.4 Document Structure

State of the Art

- 2.1 Failure Prediction
- 2.2 Accelerator

2.2 Accelerator

Event-based Failure Prediction

This section provides a brief overview of the computational steps done by the proposed algorithm [20].

brief description of the idea behind the algorithm, HSMM, Events, etc

To be able to understand the formal expression of the algorithm, first a definition of the used parameters is provided.

- N: number of states
- M: number of observation symbols
- L: observation sequence length
- R: number of cumulative probability distributions (kernels)

The delay of the event at time t_k with respect to the event at time t_{k-1} is described as

$$d_k = t_k - t_{k-1} (3.1)$$

3.1 Data Processing

3.2 Model Training

One part of the algorithm is the model training. This part is not described here. The features to be trained by the model training are however important in this context because they are used by the adapted forward algorithm. Following the features:

- π_i , forming the initial state probability vector π of size N
- $b_i(o_i)$, forming the emission probability matrix B of size $N \times M$
- p_{ij} , forming the matrix of limiting transmission probabilities P of size $N \times N$
- $\omega_{ii,r}$, the weights of the kernel r
- $\theta_{ii,r}$, the parameters of the kernel r

3.3 Sequence Processing

The following description will provide a complete blueprint of the adapted forward algorithm, that allows to implement it, but without any explanations or proofs related to the formulation. The adapted forward algorithm is defined as follows:

$$\alpha_0(i) = \pi_i b_s(O_0) \tag{3.2}$$

$$\alpha_k(j) = \sum_{i=1}^{N} \alpha_{k-1}(i) \nu_{ij}(d_k) b_{s_j}(O_k); \quad 1 \le k \le L$$
 (3.3)

where

$$v_{ij}(d_k) = \begin{cases} p_{ij}d_{ij}(d_k) & \text{if } j \neq i \\ 1 - \sum_{\substack{h=1\\h \neq i}}^{N} p_{ih}d_{ih}(d_k) & \text{if } j = i \end{cases}$$
 (3.4)

with

$$d_{ij}(d_k) = \sum_{r=1}^{R} \omega_{ij,r} \kappa_{ij,r} (d_k | \theta_{ij,r})$$
(3.5)

forming the matrix of cumulative transition duration distribution functions $D(d_k)$ of size $N \times L$.

For simplification reasons, only one kernel is used. Due to this, the kernel weights can be ignored. Equation 3.5 can then be simplified:

$$d_{ij}(d_k) = \kappa_{ij}(d_k|\theta_{ij}) \tag{3.6}$$

Choosing the gaussian cumulative distribution results in the kernel parameters μ_{ij} and σ_{ij} :

$$\kappa_{ij,gauss}(d_k|\mu_{ij},\sigma_{ij}) = 0.5 \left[1 + \frac{2}{\pi} \left(1 - \exp\left(-\frac{d_k - \mu_{ij}}{\sqrt{2}\sigma_{ij}} \right) \right) \right]$$
(3.7)

maybe use sequence likelihood and then explain about scaling

To prevent α from going to zero very fast, at each step of the forward algorithm a scaling is performed:

$$\alpha_k(i) = c_k \alpha_k(i) \tag{3.8}$$

with

$$c_k = \frac{1}{\sum_{i=1}^{N} \alpha_k(i)}$$
(3.9)

begin new sentence and explain log likelihood

then the sequence log-likelihood is computed:

$$\log(P(\mathbf{o}|\lambda)) = -\sum_{k=1}^{L} \log c_k \tag{3.10}$$

where $\lambda = \{\pi, P, B, D(d_k)\}.$

7 3.4 Classification

3.4 Classification

explain classification

and finally the classification is performed:

$$\operatorname{class}(s) = F \iff \max_{i=1}^{u} \left[\log P(s|\lambda_i) \right] - \log P(s|\lambda_0) > \log \theta$$
 (3.11)

with

$$\theta = \frac{(r_{\bar{F}F} - r_{\bar{F}\bar{F}})P(c_{\bar{F}})}{(r_{F\bar{F}} - r_{FF})P(c_{\bar{F}})}$$
(3.12)

To calculate θ , the following parameters need to be set:

- $P(c_{\bar{F}})$: prior of non-failure class
- $P(c_F)$: prior of failure class
- $r_{\bar{F}\bar{F}}$: true negative prediction
- r_{FF} : true positive prediction
- $r_{\bar{F}F}$: false positive prediction
- $r_{F\bar{F}}$: false negative prediction

8 3.4 Classification

Acceleration

Challanges of the acceleration

- implementation of exp and log function (LUT, Taylor, ...)
- floating points vs fixed points
- precision
- choice of accelerator (Type, Model)
- find avaliable options for parallelization

Ideas on how to accelerate the online part of the algorithm

- use high speed multiplier-accumulator (MAC) devices on a FPGA
- use MACs only on integer numbers and compute FP later manually
- minimize division (compute scaling factor once and then multiply)
- if precision allows use pipelining to precompute the factor b(j, o[k]) * v(i, j, k)
- precompute known factors and store them in order to simlify online computation (e.g. parts of the kernel, classification thershold, ...).
- ...

Possible optimizations of the algorithm:

- use a regularization term in the cost function to prevent overfitting
- incorporate the offline part of the algorithm into the online part in order to deal with model aging
- ...

4.1 Theoretical Analysis

4.1.1 Pre-Computation of Known Parameters

To reduce the computation effort of the algorithm during runtime, it may be desirable to precompute factors composed of known parameters and store them in the ROM of the accelerator. This may increase the necessary ROM on the accelerator but reduce the number of costly computations.

Considering this, the equation 3.7 can be rewritten in the following matter, by using the exponential properties:

$$\kappa_{ij,gauss}(d_k|\mu_{ij},\sigma_{ij}) = c_{ij,1} - c_{ij,2} \exp(c_{ij,3}d_k)$$
(4.1)

with the factors

$$c_{ij,1} = 0.5 + \frac{1}{\pi} \tag{4.2}$$

,

$$c_{ij,2} = \frac{1}{\pi} \exp(\frac{\mu_{ij}}{\sqrt{2}\sigma_{ij}}) \tag{4.3}$$

and

$$c_{ij,3} = -\frac{1}{\sqrt{2}\sigma_{ij}} \tag{4.4}$$

By using the equations 3.6 and 4.1, 3.4 can now be written as

$$v_{ij}(d_k) = \begin{cases} c'_{ij,1} - c'_{ij,2} \exp(c_{ij,3}d_k) & \text{if } j \neq i \\ 1 - \sum_{\substack{h=1\\h \neq i}}^{N} \left[c'_{ih,1} - c'_{ih,2} \exp(c_{ih,3}d_k)\right] & \text{if } j = i \end{cases}$$

$$(4.5)$$

with the factors

$$c'_{ij,1} = p_{ij}c_{ij,1} = p_{ij}(0.5 + \frac{1}{\pi})$$
 (4.6)

and

$$c'_{ij,2} = p_{ij}c_{ij,2} = \frac{p_{ij}}{\pi} \exp(\frac{\mu_{ij}}{\sqrt{2}\sigma_{ij}})$$
 (4.7)

The factors 4.6, 4.7 and 4.4 can be precomputed and stored in memory. Upon the computation of 4.5 the factors are read out of the ROM on the accelerator. Without pre-computation, the necessary ROM storage to provide the model parameters on the accelerator is

$$N\pi_{\#hit} + NMb_{\#hit} + N^2(p_{\#hit} + \mu_{\#hit} + \sigma_{\#hit})$$
 bit (4.8)

With the pre-computation described above

$$N\pi_{\#bit} + NMb_{\#bit} + N^{2}(c1_{\#bit}^{'} + c2_{\#bit}^{'} + c3_{\#bit})$$
 bit (4.9)

of available ROM storage is necessary.

Check speed and memory difference

The log of equation 3.12 can also be precomputed as it is used in equation 3.11 and all parameters are known before runtime.

4.1.2 Serial Implementation

```
% computation of the extended forward algorithm
  % @param N: number of states
  % @param L: number of observation symbols
  % @param PI: initial state probability vector. size N
  % @param B: matrix of emission probabilities. size N, L
  % @param C: precalculated factors to compute the extended transition
               probabilities
  % @return alpha: forward variables. size N, L
  % @return scale_coeff: coefficients. size L
   function [alpha scale_coeff] = forward_s(N, L, PI, B, C)
       k = 1:
       % initialize forward variables
12
       % read o0: index of first observation symbol
       for i=1:N,
14
           alpha(i, 1) = PI(i)*B(i, o0);
       end
16
       % scaling
17
       alpha_sum = 0;
18
       for i=1:N,
           alpha_sum += alpha(i, 1);
20
21
       end
       scale_coeff(1) = 1 / alpha_sum;
22
       for i=1:N,
23
           alpha(i, 1) *= scale_coeff(1);
24
       end
25
       % forward algorithm
       while (k < L),
27
           % read ok: index of k-th observation symbol
28
           % read dk: delay of k-th observation symbol
29
           % compute one step of forward algorithm
           [alpha(:, k+1) scale\_coeff(k+1)] = ...
31
               forward_step_s(N, dk, alpha(:, k), B(:, ok), C);
32
           k++;
33
       end
  end
35
  % computation of one step of the extended forward algorithm
  % @param N: number of states
3 % @param dk: delay of k-th observation symbol
  % @param alpha: forward variables of step k-1. size N
  % @param B: emission probabilities of step k. size N
6 % @param C: precalculated factors to compute the extended transition
               probabilities
8 % @param alpha_new: forward variables of step k. size N
9 % @return scale_coeff: coefficient of step k
```

```
function [alpha_new scale_coeff] = forward_step_s.m(N, dk, alpha, B, C)
       % compute extended forward factors
11
       v = compute_v(N, dk, C);
12
       % compute forward algorithm
13
       for j=1:N,
           alpha_new(j) = 0;
15
           for i=1:N,
16
                alpha_new(j) += alpha(i) * v(i, j);
17
           end
18
           alpha_new(j) *= B(j);
19
       end
20
       % scaling
       alpha_sum = 0;
22
       for i=1:N,
23
           alpha_sum += alpha_new(i);
24
       end
       scale_coeff = 1 / alpha_sum;
26
       for i=1:N,
           alpha_new(i) *= scale_coeff;
28
       end
29
   end
30
   % computation of the extended transition probabilities
   % @param N:
                    number of states
   % @param dk:
                    delay of k-th observation symbol
   % @param C:
                    precalculated elements
   % @return v:
                    extended transition probabilities
   function [v] = compute_v(N, dk, C)
       % compute all elements of v
       for i=1:N,
           for j=1:N,
                v(i, j) = C.c1(i, j) - C.c2(i, j)*exp(C.c3(i, j)*dk);
10
           end
11
       end
12
       % correct diagonal elemnts of v
13
       for i=1:N,
14
           for j=1:N,
15
                v_sum(i) += v(i, j);
16
           end
17
       end
       for i=1:N,
19
           v_sum(i) -= v(i, i);
           v(i, i) = 1 - v_sum(i);
21
       end
22
   end
23
```

4.1.3 Parallelization

4.1.4 Precision

4.2 Choice of Accelerator Type

4.2.1 CPU

- pro
 - fast and easy implementation
 - high precision
 - high frequency

• contra

- high power consumption
- limited parallelization
- large overhead for simple instructions
- fixed architecture (memory, computation units)

4.2.2 GPU

- pro
 - parallelization options for a low price
 - fast onboard memory
 - high frequency
 - high precision
 - simple implementation

• contra

- high power consumption
- overhead for simple instructions
- fixed architecture (memory, computation units)

4.2.3 FPGA

- pro
 - low power consumption
 - low overhead
 - flexibility
- contra

- low frequency
- parallelization is expensive
- precision is expensive
- complex implementation

4.2.4 ASIC

- pro
 - very low power consumption
 - no overhead
 - very flexible
- contra
 - very expensive
 - very complex implementation

4.2.5 Conclusion

4.3 Implementation

Testing and Verification

- 5.1 Log Standard
- 5.2 Metrics
- 5.3 Automated Log Generation
- 5.4 Online Log Generation

Results

- 6.1 Speedup
- 6.2 Accuracy

18 6.2 Accuracy

Conclusion

- 7.1 Achievements
- 7.2 Future Work

20 7.2 Future Work

Appendix A

Some material

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