

Moisés Francisco Campos Zepeda

Curriculum Vitae


Electrical Engineering Student (awaiting graduation) with a vocational emphasis on Computers and Networks. Graduation ceremony set to be in October 2020.

Personal details

Address, Barrio Lugano, Lourdes, Montes de Oca, Costa Rica.

Phone, (506) 8681-1702.

Mail, moiso.camposcr@gmail.com , moises.camposzepeda@ucr.ac.cr .

 <https://github.com/moisotico>, <https://gitlab.com/moisotico>.

in www.linkedin.com/in/moises-campos-zepeda.

Education

2015–2020 **Electrical Engineering**, *University of Costa Rica*.

Emphasis on computers and networks.

2013– **Law**, *University of Costa Rica*.

Emphasis on Telecommunications and electronic commerce.

2011–2012 **High School Diploma**, *Colegio Humanístico Costarricense de Heredia, Heredia*.

Awards

Aug 2018 **First place for BeSafe, a security device for bus stops using voice and image recognition**, *Hardwarethon: an IoT hardware design contest*, organized by Imagine XYZ, sponsored by CINDE and TEC (Technological institute of Costa Rica).

May 2020 **Winner of a non reimbursable fund by the United Nations Development Programme for Maktub**, *an app that brings 360° pictures and virtual tours to rural community tourism businesses*, HACKOVID19: an international online co-creation in teams, to respond to problems caused by the coronavirus.
organized by AUGÉ and SBD (Sistema de Banca para el Desarrollo), sponsored by the United Nations Development Programme

Work experience and Developed Projects

May 2019 - **Hewlett Packard Enterprise**, *Verification Engineering Intern*, as part of the ASIC team in R&D Costa Rica Aruba Networking department, currently designing algorithms that simplify the building and testing of hardware for computer networks using the Go programming language.

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2013 - 2014 **Amazon.com Inc.**, *Technical support*, Worked on customer service, webpage troubleshooting, described products to potential buyers. Provided technical assistance on Tablets, as well as apps and websites, solved account management related problems, such as subscription issues.

Academic experience

- **Courses & Projects** - Electrical Engineering, University of Costa Rica (2015-2020)
 - Approved **logic design** courses such as Digital Circuits I, Microelectronics.
 - Experience in **RTL design** using **Verilog** from Digital Circuits II.
 - Programmed two compression algorithms (LZW, LZ77).
 - Developing a Bottle Screen based on Noisebridge's Flaschen Taschen.
 - Organizer of Summer Workshops for EA-IEEE : a set of 2 week courses focused on programming, music and engineering for ElectrizaArte and the IEEE UCR student chapter.

Assistant in the following activities:

- IT Department - School of Mathematics, University of Costa Rica (July 2015 - March 2016):
 - Software management user support and troubleshooting.
 - General GNU/Linux administration and debugging, services: LAMP, network backup systems, GIT, SSH, CMS, bash.
- Computing Department - Faculty of Engineering, University of Costa Rica (July 2017 - January 2019):
 - Basic debugging in SQL, and JavaScript applied to the inventory webpage on the Bootstrap framework.
 - Structured cabling and hardware support.
- Computing Department - Electrical Engineering School, University of Costa Rica (July 2018 - February 2018).
 - Electric machinery, software management and troubleshooting support and installation.
 - Web page support for the school of Electrical Engineering.
- MATEM Project - School of Mathematics (March 2016 -July 2016).
 - Coordination of both volunteering students and teachers in the application of the Pre-calculus and Calculus exam.

Pedagogy

- Instructor of the General Physics (I, II, III) and Calculus (I, II, III) at the Estudiaderos CASE for the Faculty of Sciences, University of Costa Rica 2016-2017.

Volunteering activities

- August 2018 - Project coordinator and assistant in "ElectrizaArte", volunteering project by the March 2019 School of Electrical Engineering, at the University of Costa Rica, in which we seek to mix engineering and art:
- Participated in talks given to students in different rural communities about access to the university and development of technological projects.
 - Coordinated 8 art/music related electronic/IoT projects.
 - Assigned new students to projects and taught them basic skills in electronics and programming.

Computer skills

Languages & Tools GO, VERILOG, SYSTEMVERILOG, DOCKER, C, C++, ARDUINO, PYTHON, GIT, ASSEMBLY FOR MIPS, BASH SCRIPTING, ANSIBLE, RASPBERRY PI, GNU/LINUX SYSTEM ADMINISTRATION, \LaTeX , MATLAB AND SIMULINK, RUST, JAVA, HTML, CSS, JAVASCRIPT.

Research

- 2019– BioCode:
- Coauthor of the paper " Behavioral tracking gets real in the wild", submitted to the Conference: " New Frontiers in the Study of Animal Behaviour".
 - Robotics and AI applications for computational biology
 - Facilitating quantification of collective behaviour
- 2018– PRIS-Lab:
- Worked on Python applications using Markov Chains, and implemented parallelism on a genetic algorithm using MPI protocol on C++ for a set of courses taught by MSc. Gustavo Ramírez.
 - Recently entered a project regarding development of costa rican region-based speech recognition systems under Dr. Marvin Coto Jiménez.

Languages

Spanish **Native Language**
English **Advanced (CEFR C1 English)**
Mandarin **Basic (HSK1)**
Chinese

References

- Dr. Yu Lo, Lucky Lochi. lochi.yu@ucr.ac.cr. Headmaster of Electrical Engineering School, University of Costa Rica.
- M.Sc. Willink Castro, Teodoro. teodoro.willinkcastro@ucr.ac.cr. Head of Electronics and Telecommunications Department, Electrical Engineering School, University of Costa Rica.
- Msc. Priscilla Castro Muñoz. priscilla.castromunoz@ucr.ac.cr. Network Administrator for the Faculty of Engineering, University of Costa Rica.
- Arturo Espinoza Soley. arturo.espinoza-soley@hpe.com. Hardware Design Manager. HPE.