EmbedUno Variant: [No Variations]

08/01/2021

7

RELEASED 08-JAN-2020

Page	Index	Page	Index	Page	Index	Page	Index
1	COVER PAGE	11		21		31	
2	BLOCK DIAGRAM	12		22		32	
3	EMBEDUNO SCHEMATIC	13		23		33	
4	REVISION HISTORY	14		24		34	
5		15		25		35	
6		16		26		36	
7		17		27		37	
8		18		28		38	
9		19		29		39	
10		20		30		40	

Cannot open file 28Pins3D.png

DESIGN CONSIDERATIONS

DESIGN NOTE: Example text for informational design notes.

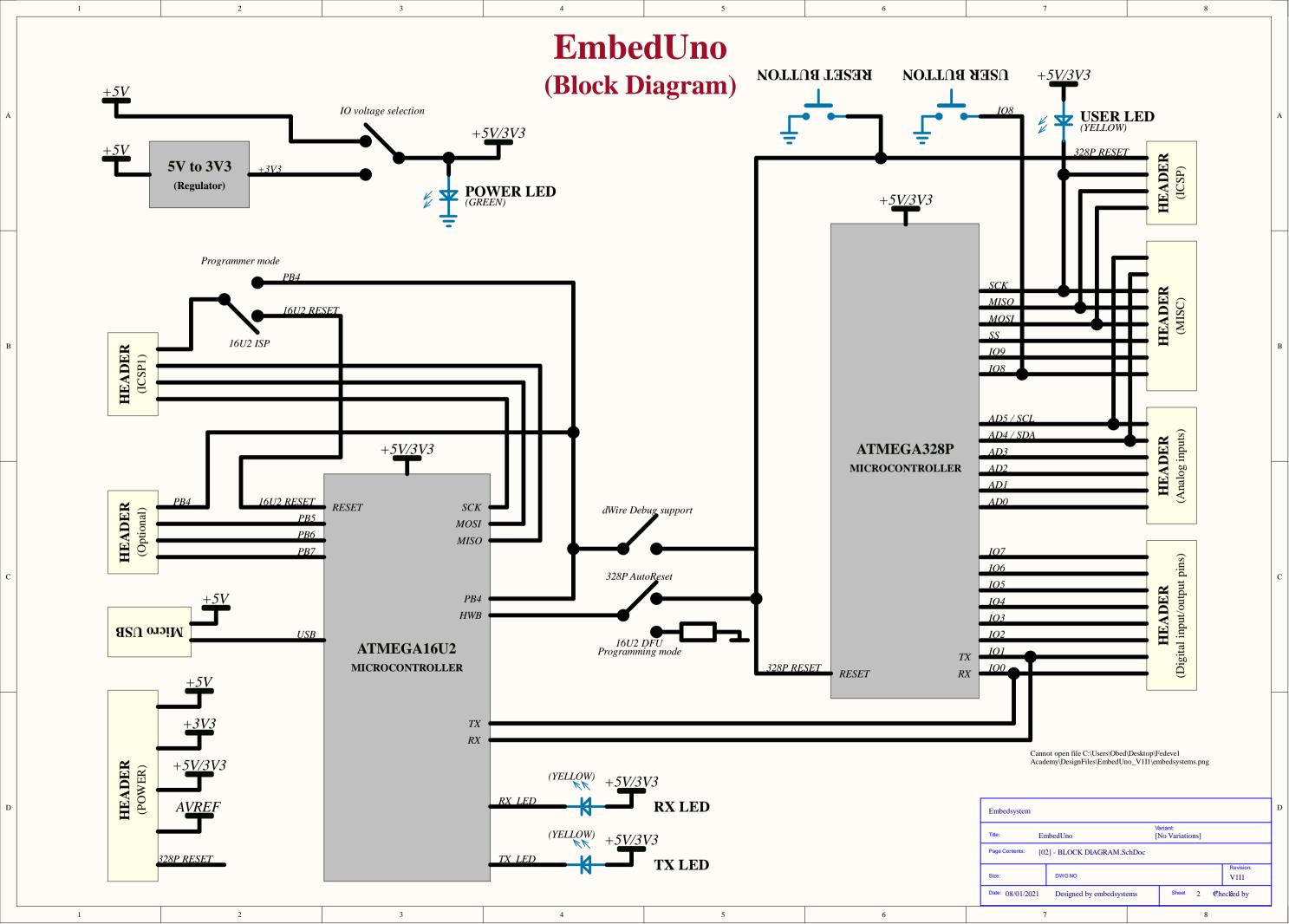
DESIGN NOTE: Example text for cautionary Example text for critical

LAYOUT NOTE: Example text for critical Cannot open file C:\Users\Obed\Desktop\Fedeve1 Academy\DesignFiles\EmbedUno_V1I1\embedsystems.png

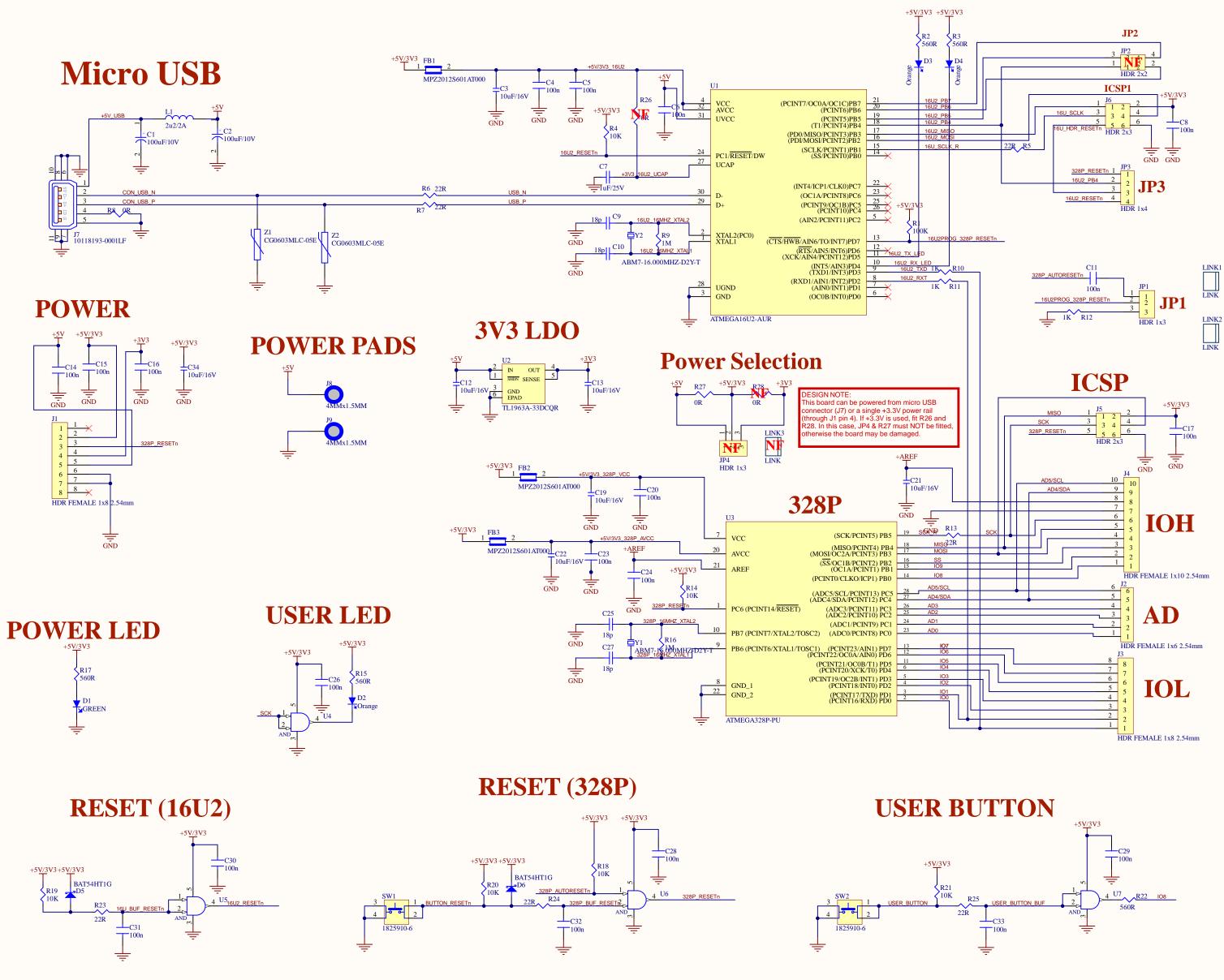
Embedsystem			http://www.fedevel.com/academy				
Title: EmbedUno			Variant: [No Variations]				
Page Contents: [01] - COVER PAGE.SchDoc			Checked by				
Size:	DWGNO			Revision: V1I1			
Date: 08/01/2021	Embedsystem		Sheet 1 of	5			

DESIGN NOTE: Example text for debug notes.

4



EMBEDUNO - SCHEMATIC



ESIGN NOTE:

This board supports 5V or 3V3 voltage levelo on the IO pins:

2) 3.3V IO - Remove R27, Fit R28, *Replace Y1 (change from 16MHz to 10MHz), *Replace Y2 (change from 16MHz to 8MHz).

*Note: The 16MHz crystals are not recommended for 3.3V operation. We need to adjust their values, thats why

3) Both 5V and 3V3, selected through JP4 - Remove R27, Remove R28, Fit JP4, *Replace Y1 (change from 16MHz to 10MHz), *Replace Y2 (change from 16MHz to 8MHz).

IMPORTANT: Once you change the crystal value, you may need to re-compile your source code.

DESIGN NOTE

1) DebugWire support - Short 1&2. This was added to support possible debugWire debugging (programming?) of 328P through 16U2. In this case, the 16U2 needs to have a correct firmware and has to behave as a debugWire tool

2) ISP programmer mode - Short 2&3. In this case, take a cable and connect J5 & J6 together. Upload AVRISP MKII firmware into 16U2 and you can program 328P. Example of AVRISP MKII firmware can be found at LUFA projects: http://www.fourwalledcubicle.com/LUFA.php (Tip: remap LEDs of the default AVRISP MKII LUFA project to the RX and TX LEDs on the 28Pin board)

3) ISP header - Short 3 & 4. In this mode, the ICSP1 header is used as a standard ISP header to program 16U2 through ISP interaface by an ISP programmer.

DESIGN NOT

About JP1:

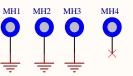
1) Autoreset Enabled - Short 1&2. In this case, 16U2 is used to reset 328P when firmware inside 328P is updated from Archiving IDE.

2) 16U2 DFU mode Enabled - Short 2&3. 16U2 HWB pin is sampled by 16U2 during RESET. If pulled low, then after Reset the 16U2 will go into DFU mode (it's the mode when you can flash 16U2 firmware through USB and Atmel Flip software: http://www.atmel.com/tools/flip.aspx).

DIP SOCKET



MOUNTING HOLES



Mounting holes 7.4mm pad 3.2mm drill

BOARD MOUNTING HOLES ONE IN EACH CORNER

FUDICIALS



FIDICIALS 4x TOP

AYOUT NOTE:

LAYOUT NOTE:

1) Route all the POWER tracks with minimum track width 0.4mm.

2) Route all the other tracks by 0.4mm and change them by the end of the design

2) Route all the other tracks by 0.4mm and change them by the end of the design to 0.2mm. To change all of them at once, use this filter "(not InNet('+*') and not InNet('GND')) and IsTrack and (OnLayer('L1') or OnLayer ('L2'))" and then set 0.2mm width in PCB Inspector panel.

Embedsystem

Title: EmbedUno [No Variant: [No Variations]

Page Contents: [03] - EmbedUno_Schematic.SchDoc

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