

|             |                        |
|-------------|------------------------|
| <b>IO</b>   | 4 × Input / Output     |
| <b>CLB</b>  | 8 × LUT4 + FF          |
| <b>REG</b>  | 4 × 32 bit, 2r1w       |
| <b>MAC</b>  | 8 bit · 8 bit + 20 bit |
| <b>BRAM</b> | 32 × 256 bit, 1rw1r    |
| <b>ADC</b>  | 12 bit SAR-ADC         |
| <b>DAC</b>  | 8 bit R-DAC            |
| <b>TERM</b> | Termination            |
| <b>NULL</b> | No tile                |

| NULL | TERM | TERM | TERM | TERM | TERM | TERM | TERM | TERM | TERM | TERM | TERM | TERM | TERM |
|------|------|------|------|------|------|------|------|------|------|------|------|------|------|
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| IO   | CLB  | CLB  | CLB  | REG  | CLB  | CLB  | CLB  | CLB  | MAC  | CLB  | CLB  | CLB  | SRAM |
| NULL | TERM | TERM | TERM | TERM | ADC  | ADC  | DAC  | DAC  | TERM | TERM | TERM | TERM | TERM |