A Data-Driven Sensor Placement Approach for Detecting Voltage Violations in Distribution Systems

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Abstract—Stochastic fluctuations in power injections from distributed energy resources (DERs) combined with load variability can cause constraint violations (e.g., exceeded voltage limits) in electric distribution systems. To monitor grid operations, sensors are placed to measure important quantities such as the voltage magnitudes. In this paper, we consider a sensor placement problem which seeks to identify locations for installing sensors that can capture all possible violations of voltage magnitude limits. We formulate a bilevel optimization problem that minimizes the number of sensors and avoids false sensor alarms in the upper level while ensuring detection of any voltage violations in the lower level. This problem is challenging due to the nonlinearity of the power flow equations and the presence of binary variables. Accordingly, we employ recently developed conservative linear approximations of the power flow equations that overestimate or underestimate the voltage magnitudes. By replacing the nonlinear power flow equations with conservative linear approximations, we can ensure that the resulting sensor locations and thresholds are sufficient to identify any constraint violations. Additionally, we apply various problem reformulations to significantly improve computational tractability while simultaneously ensuring an appropriate placement of sensors. Lastly, we improve the quality of the results via an approximate gradient descent method that adjusts the sensor thresholds. We demonstrate the effectiveness of our proposed method for several test cases, including a system with multiple switching configurations.

Index Terms—Sensor placement, voltage violations, bilevel optimization, approximate gradient descent.

I. INTRODUCTION

DISTRIBUTED energy resources (DERs) are being rapidly deployed in distribution systems. Fluctuations in DER power outputs and varying load demands can potentially cause violations of voltage limits, i.e., voltages outside the bounds imposed in the ANSI C84.1 standard. These violations can cause equipment malfunctions, failures of electrical components, and, in severe situations, power outages.

To mitigate the impacts of violations, distribution system operators (DSOs) must identify when power injection fluctuations lead to voltages exceeding their limits. To do so, sensors are placed within the distribution system to measure and communicate the voltage magnitudes at their locations. Due to the cost of sensor hardware and communication infrastructure and the structure of distribution systems, sensors are not placed at all buses. The question arises whether a voltage violation at a location without a sensor can nevertheless be detected.

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Various studies have proposed sensor siting methods to capture constraint violations and outages in power systems. For instance, [1] and [2] focus on a cost minimization problem that aims to capture all node (e.g., voltage magnitude) and line (e.g., power flow) outages. However, these references assume that a power source/generation is only located at the root node, which is not always the case, especially in the distribution systems where DERs can be located further down a feeder. Additional research efforts such as [3]-[6] seek to locate the minimum number of sensors to achieve full observability for the system. Alternatively, instead of considering full observability for the entire system, [7] considers satisfying observability requirements given a probability of observability at each bus. Other research efforts, such as [8] and [9], focus on voltage control schemes that prevent voltage violations. These efforts take a control perspective rather than extensively considering how to best place sensors. There is also research on siting phasor measurement units, which are utilized as sensors in power grids [10]. Existing smart meters installed at customer locations measure power consumption over a long period of time (e.g., a day to a month). Reference [11] discusses the potential benefits of using smart meters to report voltages and currents at higher resolutions. However, handling large amounts of data remains challenging, necessitating methods to selectively site high-resolution sensors.

In this paper, we consider a *sensor placement* problem which seeks to locate the minimum number of sensors and determine corresponding sensor alarm thresholds in order to reliably identify all possible violations of voltage magnitude limits in a distribution system. We formulate this sensor placement problem as a bilevel optimization with an upper level that minimizes the number of sensors and chooses sensor alarm thresholds and a lower level that computes the most extreme voltage magnitudes within given ranges of power injection variability. This problem additionally aims to reduce the number of false positive alarms, i.e., violations of the sensors' alarm thresholds that do not correspond to an actual voltage limit violation.

In contrast to previous work, this problem *does not* attempt to ensure full observability of the distribution system. Rather, we seek to locate (a potentially smaller number of) sensors that can nevertheless identify all voltage limit violations for any power injections within a specified range of power injection variability. With a small number of sensors, the proposed formulation also provides a simple means to design corrective actions if voltage violations are encountered in real-time operations. By restoring voltages at these few critical locations to within their alarm thresholds, the system operator can

guarantee feasibility of the voltage limits for the full system. This guarantee is obtained by our sensor placement method purely by analyzing the geometric properties of the feasible set. We do not consider the design details of the feedback control protocol and thus dynamic properties of the sensors such as latency are not relevant in our approach.

Due to the nonlinear nature of the AC power flow equations, computing a globally optimal solution is challenging. We utilize *conservative linear approximations* of the power flow equations to convert the lower-level problem to a linear program [12]. This bilevel problem can be reformulated to a single-level problem using the Karush-Kuhn-Tucker (KKT) conditions with binary variables via a big-M formulation [13]-[15]. In this paper, we consider a duality-based approach, which has substantial computational advantages over traditional KKT-based approaches to solving the bilevel problem. The conservative linear approximations can incorporate the behavior of more complex components such as tap-changing transformers, smart inverters, etc., as long has we have access to a power flow solver for the system. By using these linear approximations as the first step, we are able to treat the power flow solver as a black-box. Consequently, all complexities of component behavior and power flow physics are absorbed by the power flow solver and the complexity of the resulting sensor placement formulation remains unaffected.

Note that conservativeness from the conservative linear approximations may increase the number of false positive alarms. We therefore propose an approximate gradient descent method as a post-processing step to further improve the quality of the results. This method iteratively adjusts the sensor thresholds while ensuring that all violations are still detected.

In summary, our main contributions are:

- (i) A bilevel optimization formulation for a sensor placement problem that minimizes the number of sensors needed to capture all possible violations of voltage limits while minimizing the number of false positive alarms.
- (ii) Reformulations that substantially improve the computational tractability of this bilevel problem.
- (iii) An approximate gradient descent method to improve solution quality.
- (*iv*) Numerical demonstration of our proposed problem formulations for a variety of test cases, including networks with multiple switching configurations.

This paper is organized as follows. Section II formulates the sensor placement problem using bilevel optimization. Section III proposes different techniques to reformulate the optimization problem. Section IV provides our numerical tests. Section V concludes the paper and discusses future work.

II. SENSOR PLACEMENT PROBLEM

This section describes the sensor placement problem by introducing notation, presenting the bilevel programming formulation that is the focus of this paper, and detailing the objective function that simultaneously minimizes the number of sensors and reduces the number of false positive alarms.

A. Notation

Consider an n-bus power system. The sets of buses and lines are denoted as $\mathcal{N} = \{1, \dots, n\}$ and \mathcal{L} , respectively. One bus

in the system is specified as the slack bus where the voltage is $1 \angle 0^\circ$ per unit. For the sake of simplicity, the remaining buses are modeled as PQ buses with given values for their active (P) and reactive (Q) power injections. Extensions to consider PV buses, which have given values for the active power (P) and the voltage magnitude (V), are straightforward. (The controlled voltage magnitudes at PV buses imply that voltage violations cannot occur at these buses so long as the voltage magnitude setpoints are within the voltage limits.) The set of all nonslack buses is denoted as \mathcal{N}_{PQ} . The set of neighboring buses to bus i is defined as $\mathcal{N}_i := \{k \mid (i,k) \in \mathcal{L}\}$. Subscript $(\cdot)_i$ denotes a quantity at bus i, and subscript $(\cdot)_{ik}$ denotes a quantity associated with the line from bus i to bus k, unless otherwise stated. Conductance (susceptance) is denoted as G(B) as the real (imaginary) part of the admittance.

To illustrate the main concepts in this paper, we consider a balanced single-phase equivalent network representation rather than introducing the additional notation and complexity needed to model an unbalanced three-phase network. Our work does not require assumptions regarding a radial network structure, and we are able to handle multiple network configurations (i.e., a network with a set of topologies), as discussed later in Section III-H. Extensions to consider other limits such as restrictions on line flows, alternative characterizations of power injection ranges such as budget uncertainty sets, and unbalanced three-phase network models impose limited additional complexity.

B. Bilevel optimization formulation

The main goal of this problem is to find sensor location(s) such that sensor(s) can capture all possible voltage violations. This paper assumes that the voltages read by the sensors are accurate and noise-free measurements. We formulate this problem as a bilevel optimization with the following upper-level and lower-level problems.

- **Upper level:** Determine sensor locations and alarm thresholds such that whenever the voltages at the sensors are within the chosen thresholds, the voltages at all other buses are within pre-specified safety limits.
- Lower level: Find the extreme achievable voltages at all buses given the sensor locations, sensor alarm thresholds, and the specified range of power injection variability.

The sensor locations and alarm thresholds output from the upper-level problem are input to the lower-level problem, and the extreme achievable voltage magnitudes output from the lower-level problem are used to evaluate the bounds in the upper-level problem. We first introduce notation for various quantities associated with the voltage at bus i:

 $V_i(\widetilde{V_i})$: Lower (Upper) sensor alarm threshold.

 \underline{V}_i (\overline{V}_i) : Lowest (Highest) achievable voltage obtained from the lower-level problem.

 $U_i(\widetilde{U}_i)$: Translation of lower (upper) sensor threshold via a big-M formulation; see (1c).

 $V_i^{\min}(V_i^{\max})$: Specified lower (upper) voltage limit.

We formulate the following bilevel optimization formulation:

$$\min_{\mathbf{s}, \widetilde{\mathbf{V}}, \mathbf{V}} c(\mathbf{s}, \widetilde{\mathbf{V}}, \mathbf{V})$$
 (1a)

$$\begin{aligned} \text{s.t.} & & (\forall i \in \mathcal{N}_{PQ}) \\ & & \underline{V}_i \geq V_i^{\min}, \ \overline{V}_i \leq V_i^{\max}, \end{aligned} \tag{1b}$$

$$U_i = V_i s_i, \ \widetilde{U}_i = \widetilde{V}_i s_i + M(1 - s_i),$$
 (1c)

$$\underline{V}_i = \mathbb{L}_i(\mathbf{s}, \widetilde{\mathbf{U}}, \underline{\mathbf{U}}), \ \overline{V}_i = \mathbb{U}_i(\mathbf{s}, \widetilde{\mathbf{U}}, \underline{\mathbf{U}}),$$
 (1d)

where c is the cost function associated with the placement of sensors including costs for the hardware, installation, communication network, etc. and s is a vector of sensor locations modeled as binary variables (1 if a sensor is placed, 0 otherwise). All bold quantities are vectors. The quantities $\mathbb{L}_i(\mathbf{s}, \mathbf{U}, \mathbf{U})$ and $\mathbb{U}_i(\mathbf{s}, \mathbf{U}, \mathbf{U})$ are the solutions to the lowerlevel problems which, for each $i \in \mathcal{N}_{PQ}$, are given by

$$\mathbb{L}_{i}(\mathbf{s}, \widetilde{\mathbf{U}}, \underline{\mathbf{U}}) = \min_{V_{i}} V_{i} \quad (\mathbb{U}_{i}(\mathbf{s}, \widetilde{\mathbf{U}}, \underline{\mathbf{U}}) = \max_{V_{i}} V_{i}) \quad (2a)$$
s.t. $(\forall j \in \mathcal{N}_{PQ})$

$$P_j = V_j \sum_{k \in \mathcal{N}_j} V_k (G_{jk} \cos(\theta_{jk}) + B_{jk} \sin(\theta_{jk})), \qquad (2b)$$

$$Q_j = V_j \sum_{k \in \mathcal{N}_j} V_k(G_{jk} \sin(\theta_{jk}) - B_{jk} \cos(\theta_{jk})), \qquad (2c)$$

$$\theta_1 = 0, \tag{2d}$$

$$U_j \le V_j \le \widetilde{U}_j,$$
 (2e)

$$P_i^{\min} \le P_i \le P_i^{\max},\tag{2f}$$

$$P_j^{\min} \le P_j \le P_j^{\max},$$

$$Q_j^{\min} \le Q_j \le Q_j^{\max},$$

$$(2f)$$

where P_j and Q_j denote the active and reactive power injections at bus j within a particular lower-level problem, $\theta_{ik} := \theta_i - \theta_k$ denotes the voltage angle difference between buses j and k, and superscripts max and min denote upper and lower limits, respectively, on the corresponding quantity. The quantities \mathbb{L}_i and \mathbb{U}_i are functions of s, U, and U as shown in (1d), but these dependencies are omitted later in this paper for the sake of notational brevity. For the upperlevel problem, the objective function in (1a) minimizes a cost function $c(\mathbf{s}, \mathbf{V}, \mathbf{V})$ associated with the sensor locations s and alarm thresholds V, V while ensuring that the extreme achievable voltage magnitudes calculated in the lower-level problem, $\underline{V}_i, \overline{V}_i$ are within safety limits as shown in (1b). The cost function $c(\mathbf{s}, \mathbf{V}, \mathbf{V})$ will be detailed in the following subsection. In the lower-level problem, the objective function (2a) computes the maximum or minimum voltage magnitude for each PQ bus $i \in \mathcal{N}_{PQ}$. For each lower-level problem, constraints (2b)-(2c) are the power flow equations at each bus j, constraint (2e) forces the voltage magnitudes to be within voltage alarm thresholds if a sensor is placed at the corresponding bus, and constraints (2f)-(2g) model the range of variability in the net power injections. Constraint (2d) sets the angle reference for the power flow equations.

C. Cost function

Overly restrictive sensor thresholds can potentially trigger an alarm even when there are no voltage violations actually occurring in the system, thus resulting in a false positive. To reduce both the number of sensors and the number of

false positive alarms due to unnecessarily restrictive alarm thresholds, our cost function, c(s, V, V), is:

$$c(\mathbf{s}, \mathbf{V}, \widetilde{\mathbf{V}}) = \sum_{i \in \mathcal{N}} c_i(s_i, \mathbf{V}_i, \widetilde{V}_i)$$
 (3)

$$c_i(s_i, V_i, \widetilde{V}_i) = \begin{cases} (V_i - V_i^{\min}) + (V_i^{\max} - \widetilde{V}_i) + \delta; & s_i = 1, \\ 0; & s_i = 0, \end{cases}$$

$$(4)$$

where δ is a specified cost of placing a sensor. When $s_i = 1$, the objective $c(\mathbf{s}, \mathbf{V}, \mathbf{V})$ in (4) seeks to reduce the restrictiveness of the sensor alarm thresholds to have fewer false positives. Changing the value of δ in (4) models the tradeoff between placing an additional sensor and making the sensor range more restrictive. This is a crucial part of our formulation since our main goal is to identify a small number of critical locations that carry sufficient information about the feasibility of the entire network. Beyond the clear financial benefit of having to place fewer sensors, this also provides a simple and practical mechanism for deploying corrective actions in realtime. Indeed, when the system operator encounters a voltage violation, a reactive power compensation protocol that brings the voltages at these few critical locations to within the alarm thresholds will guarantee feasibility of the voltage limits for the entire network.

III. REFORMULATING THE SENSOR PLACEMENT PROBLEM

The bilevel problem (1) is computationally challenging due to the non-convexity in the lower-level problem induced by the AC power flow equations in (2b)-(2c) and the presence of two levels. Moreover, as we will show numerically in Section IV, traditional methods for reformulating the bilevel problem into a single-level problem suitable for standard solvers using the Karush-Kuhn-Tucker (KKT) conditions turn out to yield computationally burdensome problems. In this section, we provide methods for obtaining a tractable version of the bilevel sensor placement problem. We first use recently proposed conservative linear approximations of the power flow equations to convert the lower-level problem to a more tractable linear programming formulation that nevertheless retains characteristics from the nonlinear AC power flow equations. We then use various additional reformulation techniques that yield significantly more tractable problems than standard KKT-based reformulations. These reformulations first yield a (single-level) mixed-integer bilinear programming formulation that can be solved using commercial mixed-integer programming solvers like Gurobi. Via further reformulations that discretize the sensor alarm thresholds, we transform the bilinear terms to obtain a mixed-integer linear program (MILP) that has further computational advantages and, more importantly, allows us to select a broader range of solvers, such as Cplex and Mosek.

A. Conservative linear power flow approximations

To address challenges associated with power flow nonlinearities and to take account of the complexity of the systems, we employ a linear approximation of the power flow equations that is adaptive (i.e., tailored to a specific system and a range of load variability) and conservative (i.e., intend to overor under-estimate a quantity of interest to avoid constraint violations). These linear approximations are called *conservative linear approximations* (CLAs) and were first proposed in [12]. As a sample-based approach, the CLAs are computed using the solution to a constrained regression problem. They linearly relate the voltage magnitudes at a particular bus to the power injections at all PQ buses. These linear approximations can also effectively incorporate the characteristics of more complex components (e.g., tap-changing transformers, smart inverters, etc.), only requiring the ability to apply a power flow solver to the system. An example of an *overestimating* CLA of the voltage magnitude at bus i is the linear expression

$$a_{i,0} + \boldsymbol{a}_{i,1}^T \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}$$

such that the following relationship is satisfied for some specified range of power injections **P** and **Q**:

$$V_i - \left(a_{i,0} + \boldsymbol{a}_{i,1}^T \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}\right) \le 0, \tag{5}$$

where and superscript T denotes the transpose. We replace the AC power flow equations in (2b)–(2d) with a CLA as in (5) for all $i \in \mathcal{N}_{PQ}$. The bilevel problem in (1) is modified by replacing the lower-level problem in (2) by

$$\mathbb{L}_{i} = \min_{\mathbf{P}, \mathbf{Q}} \ \underline{a}_{i,0} + \underline{\mathbf{a}}_{i,1}^{T} \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^{i} \ \left(\mathbb{U}_{i} = \max_{\mathbf{P}, \mathbf{Q}} \ \overline{a}_{i,0} + \overline{\mathbf{a}}_{i,1}^{T} \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^{i} \right)$$
(6a)

s.t. $(\forall j \in \mathcal{N}_{PQ} \setminus \{i\})$

$$\overline{a}_{j,0} + \overline{a}_{j,1}^T \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^i \ge \mathcal{U}_j,$$
 (6b)

$$\underline{a}_{j,0} + \underline{a}_{j,1}^T \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^i \le \widetilde{U}_j, \tag{6c}$$

$$\begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^{\min} \le \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^{i} \le \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^{\max}.$$
 (6d)

In (6), superscripts i denote quantities associated with the ith lower-level problem. Replacing the AC power flow equations with CLAs yields a linear programming formulation for the lower-level problem rather than the non-convex lower-level problem in (1). Comparing (6b)–(6c) and (2b)–(2e), we see that satisfaction of (6b)–(6c) is sufficient to ensure satisfaction of (1b), assuming that the conservative linear approximations do indeed reliably overestimate and underestimate the voltage magnitudes. Thus, the resulting optimization problems are sufficient to ensure that the sensor locations and alarm thresholds will identify all potential voltage limit violations. As a result, solving the reformulation (6) will compute sensor locations and thresholds that avoid false negatives, i.e., alarms will always be raised if there are indeed violations of the voltage limits. The computational advantages provided by linearity of the reformulated lower-level problem come with the potential tradeoff of additional false positives, i.e., spurious alarms. Various methods proposed in [12] for improving the accuracy of the CLAs naturally yield variants of (6) that reduce the number of false positives. Additionally, in Section III-G, we describe a method for post-processing the sensor alarm thresholds to further reduce the occurrences of false positives.

B. Reformulation using KKT constraints

With a linear lower-level problem, we can apply standard techniques for reformulating the bilevel problem (6) as a (single-level) mixed-integer linear program (MILP). These techniques replace the lower-level problem (6a)-(6d) with its KKT conditions that are both necessary and sufficient for optimality of this problem [14] and also apply McCormick envelopes [16] to convert the bilinear product of the continuous and discrete variables in (1c) to an equivalent linear form. The resulting single-level problem still involves bilinear constraints associated with the complementarity conditions. These bilinear constraints are traditionally addressed using binary variables in a "Big-M" formulation. Commercial MILP solvers are applicable to this traditional reformulation, which we denote throughout the paper as the "KKT formulation". This formulation is obtained by defining the lower-level coupling quantities \mathbb{L}_i and \mathbb{U}_i using the KKT conditions given below:

$$\mathbb{L}_{i} = \underline{a}_{i,0} + \underline{\mathbf{a}}_{i,1}^{T} \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^{i} \quad \left(\mathbb{U}_{i} = \overline{a}_{i,0} + \overline{\mathbf{a}}_{i,1}^{T} \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^{i} \right),$$

$$(\forall j \in N_{PQ} \setminus \{i\}) \qquad (7a)$$

$$\underline{a}_{i,1} - \sum_{k \in \mathcal{N}_{PQ}} \lambda_{k}^{i} \overline{\mathbf{a}}_{k,1} + \sum_{k \in \mathcal{N}_{PQ}} \mu_{k}^{i} \underline{\mathbf{a}}_{k,1}$$

$$- \sum_{k \in \mathcal{N}_{PQ}} \gamma_{k}^{i} e_{k} + \sum_{k \in \mathcal{N}_{PQ}} \pi_{k}^{i} e_{k} = 0$$

$$(\text{for } \mathbb{U}_{i} : \overline{a}_{i,1} - \sum_{k \in \mathcal{N}_{PQ}} \lambda_{k}^{i} \overline{\mathbf{a}}_{k,1} + \sum_{k \in \mathcal{N}_{PQ}} \mu_{k}^{i} \underline{\mathbf{a}}_{k,1}$$

$$- \sum_{k \in \mathcal{N}_{PQ}} \gamma_{k}^{i} e_{k} + \sum_{k \in \mathcal{N}_{PQ}} \pi_{k}^{i} e_{k} = 0), \qquad (7b)$$

$$\overline{a}_{j,0} + \overline{a}_{j,1}^T \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^i \ge \underline{U}_j,$$
 (7c)

$$\underline{a}_{j,0} + \underline{a}_{j,1}^T \left(\frac{\mathbf{P}}{\mathbf{Q}} \right)^i \le \widetilde{U}_j, \tag{7d}$$

$$\begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^{\min} \le \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^i \le \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^{\max},$$
 (7e)

$$\lambda_j^i \cdot \left(\underline{U}_j - \overline{a}_{j,0} - \overline{a}_{j,1}^T \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^i \right) = 0, \tag{7f}$$

$$\mu_j^i \cdot \left(-\widetilde{U}_j + \underline{a}_{j,0} + \underline{a}_{j,1}^T \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^i \right) = 0,$$
 (7g)

$$\gamma^{i} \odot \left(\begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^{\min} - \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^{i} \right) = \mathbf{0},$$
(7h)

$$\boldsymbol{\pi}^{i} \odot \left(\begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^{i} - \begin{pmatrix} \mathbf{P} \\ \mathbf{Q} \end{pmatrix}^{\max} \right) = \mathbf{0},$$
(7i)

$$\boldsymbol{\gamma}^i, \boldsymbol{\pi}^i \geq \mathbf{0}; \lambda^i_j, \mu^i_j \geq 0, \ \forall j \in N_{PQ} \setminus \{i\}, \tag{7j}$$

where the operator \odot is the element-wise multiplication; e_i is the i^{th} column of the identity matrix; λ , μ , γ := $(\gamma_1, \gamma_2, \ldots, \gamma_{2m})^T$, and π := $(\pi_1, \pi_2, \ldots, \pi_{2m})^T$ are dual variables associated with the voltage and power injection bounds in the constraints (6b)–(6d). Note that the solution to the set of equations in \mathbb{L}_i is completely decoupled from that in \mathbb{U}_i . Equations (7b)–(7i) are the KKT conditions of

the lower-level problem. Equation (7b) is the stationarity condition. The primal feasibility conditions in (7c)–(7e) are similar to the constraints (6b)-(6d) in the original problem with conservative linear approximations of the power flow equations. The complementary slackness conditions are (7f)-(7i) and the dual feasibility condition is (7j). Observe that the complementary slackness conditions give rise to nonlinear functions due to the multiplication of the dual variables λ , μ , γ , and π with the primal variables P and Q. To handle these nonlinearities, traditional methods for bilevel optimization replace these products using additional binary variables and a big-M reformulation. This requires bounds on the dual variables that are difficult to determine, and bad choices for these bounds can result in either infeasibility or poor computational performance [15].

C. Duality of the lower-level problem

As an alternative to the KKT formulation, one can reformulate a bilevel problem into a single-level problem by dualizing the lower-level problem. This technique can only be usefully applied to problems with specific structure where the optimal objective value of the lower-level problem is constrained in the upper-level problem in the appropriate sense (max $< \cdot$ or $\min \geq \cdot$). In this special case, we can significantly improve tractability compared to the KKT formulation.

Let y^i be the vector of all dual variables associated with the lower-level problem \mathbb{L}_i and $\widetilde{\mathbf{y}}^i$ be the vector of all dual variables associated with lower-level problem \mathbb{U}_i . Let I be the identity matrix of appropriate dimension. By dualizing the lower-level problem (6), we obtain the following:

$$\begin{split} \mathbb{L}_i &= \max_{\mathbf{y}^i} \ \mathbf{b}^T \mathbf{y}^i + \underline{a}_{i,0} \ (8\mathbf{a}) \quad \mathbb{U}_i = \min_{\widetilde{\mathbf{y}}^i} \ \mathbf{b}^T \widetilde{\mathbf{y}}^i + \overline{a}_{i,0} \ (9\mathbf{a}) \\ \text{s.t.} \ A \widetilde{\mathbf{y}}^i &= \underline{a}_{i,1}, \ (8\mathbf{b}) \\ \widetilde{\mathbf{y}}^i &\geq \mathbf{0}, \quad (8\mathbf{c}) \quad \qquad \widetilde{\mathbf{y}}^i \leq \mathbf{0}, \quad (9\mathbf{c}) \end{split}$$

where

$$\begin{split} A &= \begin{bmatrix} -I, I, \overline{\boldsymbol{a}}_{1,1}, \dots, \overline{\boldsymbol{a}}_{n,1}, -\underline{\boldsymbol{a}}_{1,1}, \dots, -\underline{\boldsymbol{a}}_{n,1} \end{bmatrix}, \\ \mathbf{b} &= \begin{bmatrix} (-\mathbf{P}^{\max})^T, (-\mathbf{Q}^{\max})^T, (\mathbf{P}^{\min})^T, (\mathbf{Q}^{\min})^T, \widetilde{\boldsymbol{U}}_1 - \overline{\boldsymbol{a}}_{1,0}, \dots, \\ \widetilde{\boldsymbol{U}}_n &- \overline{\boldsymbol{a}}_{n,0}, -\widetilde{\boldsymbol{U}}_1 + \underline{\boldsymbol{a}}_{1,0}, \dots, -\widetilde{\boldsymbol{U}}_n + \underline{\boldsymbol{a}}_{n,0} \end{bmatrix}^T. \end{split}$$

Due to strong duality of the linear lower-level problem, the dual (8a) (and (9a)) has the same objective value as (6a) and does not directly provide any advantages. However, the problem has a specific structure where objectives from each lower-level problem (8a) and (9a) only appear in a single inequality constraint (1b). Hence, we only need to show that there exists some choice of duals \mathbf{y}^i and $\widetilde{\mathbf{y}}^i$ for which (1b) is feasible. This allows us to obtain a single-level formulation by defining the lower-level coupling quantities via the following set of constraints:

$$\mathbb{L}_{i} = \mathbf{b}^{T} \mathbf{y}^{i} + \underline{a}_{i,0}, \quad (10a) \qquad \mathbb{U}_{i} = \mathbf{b}^{T} \widetilde{\mathbf{y}}^{i} + \overline{a}_{i,0}, \quad (11a)$$

$$A \mathbf{y}^{i} = \underline{a}_{i,1}, \quad (10b) \qquad A \widetilde{\mathbf{y}}^{i} = \overline{a}_{i,1}, \quad (11b)$$

$$\mathbf{y}^{i} \geq \mathbf{0}. \quad (10c) \qquad \widetilde{\mathbf{y}}^{i} \leq \mathbf{0}. \quad (11c)$$

$$\mathbf{y}^i \ge \mathbf{0}.$$
 (10c) $\widetilde{\mathbf{y}}^i \le \mathbf{0}.$ (11c)

We refer to the formulation using (10) and (11) as the "bilinear formulation" due to the bilinear product of the sensor threshold variables ($\mathbf{\underline{U}}$ and \mathbf{U}) and the dual variables \mathbf{y}^i and $\widetilde{\mathbf{y}}^i$ in (10a) and (11a). Similar to the KKT formulation, using (10) and (11) leads to a single-level optimization problem. However, the latter has the major advantage that no additional binary variables are required (beyond those associated with the sensor locations in the upper-level problem) since there are no analogous equations to the complementarity condition. Our numerical results in Section IV show that modern mixedinteger programming packages like Gurobi can solve the bilinear formulation for much larger systems than are possible with the standard KKT formulation. However, if required, the bilinear constraints (10a) and (11a) can be further simplified with one additional reformulation that is discussed next.

D. Bilinear to mixed-integer linear programming

The bilinear formulation can be further converted into an MILP by discretizing the continuous-valued sensor thresholds. This formulation has the advantage of being within the scope of a larger range of mixed-integer programming solvers since not all of them can handle billinear forms. We partition the sensor threshold ranges into d discrete steps with size ϵ and define the vectors of threshold variables, U and U, as

$$\mathbf{U} = \eta^T \underline{v}_t \quad (\widetilde{\mathbf{U}} = \eta^T \widetilde{v}_t), \tag{12}$$

where

$$\eta = \begin{bmatrix} \eta_{0,1} & \eta_{0,2} & \cdots & \eta_{0,n} \\ \eta_{1,1} & \eta_{1,2} & \cdots & \eta_{1,n} \\ \vdots & \vdots & \ddots & \vdots \\ \eta_{d,1} & \eta_{d,2} & \cdots & \eta_{d,n} \end{bmatrix},$$

$$\psi_{t} = \begin{bmatrix} 0, V^{\min}, V^{\min} + \epsilon, \dots, V^{\min} + (d-1)\epsilon \end{bmatrix}^{T}$$
(13a)

$$\underbrace{v_t} = \left[0, V^{\min}, V^{\min} + \epsilon, \dots, V^{\min} + (d-1)\epsilon\right]^T
\left(\widetilde{v}_t = \left[0, V^{\max} - \epsilon, V^{\max} - 2\epsilon, \dots, V^{\max}\right]^T\right),$$
(13b)

$$\sum_{i=1}^{d} \eta_{i,k} = 1, \quad \forall k \in \mathcal{N}_{PQ}. \tag{13c}$$

Note that this discretization exploits the fact that any sensor threshold will necessarily be above the lower voltage limit V_i^{\min} and below the upper voltage limit V_i^{\max} . Equations (13a)– (13c) imply that when $\eta_{0,i}=0$, no sensor is placed (i.e., $s_i = 0$). Using this discretization, the constraints (10a) and (11a) now contain bilinear products of binary variables. These products can be equivalently transformed into a mixed-integer linear (as opposed to bilinear) programming formulation using McCormick Envelopes [16]. With McCormick Envelopes and discrete sensor thresholds, the problems (10) and (11) become a MILP that can be computed using any MILP solver. We refer to the reformulation of the lower-level problems (10) and (11) using the discretization (12) as the "MILP formulation".

To further improve tractability, we can remove unnecessary binary variables by inspecting data from the samples of power injections used to compute the conservative linear approximations of the power flow equations. Let b be a bus where the voltage magnitude never reaches the highest discretized sensor threshold value (i.e., $V^{\min} + (d-1)\epsilon$) in any of the sampled power injections. Given a sufficiently comprehensive sampling of the range of possible power injections, we can then simplify the discretized representation of the sensor alarm threshold as:

$$\underbrace{U}_{b} = 0 \cdot \eta_{0,b} + V^{\min} \cdot \eta_{1,b}, \tag{14a}$$

$$\eta_{0,b} + \eta_{1,b} = 1. \tag{14b}$$

A similar simplification can be used for the upper sensor thresholds. This pre-screening thus eliminates binary variables associated with sensor thresholds at buses that will never violate their voltage limits. We henceforth call this data-driven simplification technique "binary variable removal" (BVR).

E. A sensor placement heuristic for benchmarking

For comparison with our proposed sensor placement approach, we describe a heuristic alternative that exploits the traditional behavior of distribution systems where voltage magnitudes typically drop as one moves down a feeder away from the substation. This behavior suggests that the ends of each feeder may be good locations for locating sensors since the voltage magnitude limits are likely to be violated here first.

This heuristic technique avoids the computational burden from computing the conservative linear power flow approximations and solving some reformulation of the bilevel optimization problem. However, it can fail to eliminate false negatives and may produce sub-optimal results in terms of the number of required sensors. This is especially true in systems with (i) DERs towards the ends of some branches, (ii) multiple operating topologies, and (iii) a large number of branches. Illustrative examples of systems with such characteristics are provided in Section IV-D.

F. Comparisons of each formulation

The previous subsections present several problem reformulations that convert the bilevel sensor placement problem (1) into various single-level problems that can be solved with mixed-integer solvers like Gurobi. Each reformulation has different computational characteristics and yields solutions with differing accuracy. We next compare the KKT formulation (7) described in Section III-B with the duality-based bilinear formulation (10) and (11) described in Section III-C according to the numbers of decision variables and constraints.

Both formulations involve bilinear terms but the bilinear formulation is more compact. Consider a system with b PQ buses, of which there are r buses where the voltage magnitudes may violate their limits after the pre-screening described in Section III-D. The total number of decision variables in the KKT formulation is $5 \cdot b \cdot r + 2 \cdot b$ ($2 \cdot b \cdot r$ from power injections, $3 \cdot b \cdot r$ from dual variables, b from the voltage thresholds, and b from the sensor locations). Our proposed duality-based bilinear formulation involves only $3 \cdot b \cdot r + 2 \cdot b$ decision variables. The reduction happens because the variables corresponding to power injections are entirely removed by duality.

Regarding the number of constraints, the duality-based bilinear formulation does not have the stationarity conditions, primal feasibility, or power injections directly involved, resulting in a reduction of $2 \cdot b \cdot r + b \cdot r + 2 \cdot b \cdot r = 5 \cdot b \cdot r$ constraints. The implications of these differences on tractability is assessed via the solution times presented in Section IV.

G. Approximate gradient descent

Solving any of the reformulated bilevel optimization problems may lead to *false positives*, i.e., alarms that occur in the absence of a voltage limit violation. This is both due to the limited number of sensors and the conservative nature of the linear power flow approximations used in the lowerlevel problem. These false positives are undesirable because they may lead system operators to take unnecessary corrective actions that could reduce the efficiency of system operations or put avoidable wear on system components.

To reduce the number of false positives, this section proposes a post-processing step that iteratively adjusts the sensor thresholds that result from the reformulated bilevel optimization problems. We refer to this post-processing step as the Approximate Gradient Descent (AGD) method. This method uses the voltage magnitudes associated with a large number of sampled power injections within the considered range of power injection variability. These samples can be the same as those used to compute the conservative linear approximations of the power flow equations.

In this section, let superscript k denote the k^{th} iteration of the AGD method. Let ϵ_{AGD} be a step size for adjusting the sensor thresholds and \mathbf{f}^k be the vector of the number of false positives from the sampled power injections using the sensor thresholds in the k^{th} iteration. Using the sampled power injections, this method computes an "approximate gradient" indicating how small changes to the sensor alarm thresholds affect the number of false positives. The approximate gradient at iteration k is denoted as \mathbf{g}^k . We denote the set of buses with sensors as \mathcal{N}_s . Subscripts denote the bus number and superscripts denote the iteration number.

Let Δf_i^k represent the change in the number of false positives among the sampled power injections using the sensor thresholds in the k^{th} iteration when the sensor alarm threshold i is changed by ϵ_{AGD} (leaving all other sensor thresholds unchanged). We then compute an approximate gradient \mathbf{g}^k by comparing the values of Δf_i^k across different buses i:

$$\mathbf{g}^k = \frac{\Delta \mathbf{f}^k}{\sqrt{\sum_{i \in \mathcal{N}_s} (\Delta f_i^k)^2}}.$$
 (15)

The approximate gradient \mathbf{g}^k thus points in the direction of the steepest reduction in the (empirically determined) number of false positives. Each iteration of the AGD method updates the sensor thresholds according to the update rule:

$$\mathbf{V}^{k+1} = \mathbf{V}^k + \epsilon_{AGD} \cdot \mathbf{g}^k \quad (\widetilde{\mathbf{V}}^{k+1} = \widetilde{\mathbf{V}}^k + \epsilon_{AGD} \cdot \mathbf{g}^k).$$
(16)

The AGD method stops when taking an additional step would result in the appearance of false *negatives*, i.e., undetected violations of voltage limits. The AGD method for the upper voltage limits is calculated separately from the lower voltage limits. Fig. 1 shows the overall process for computing the sensor locations and thresholds starting from computing the conservative linear approximations of the power flow equations (a pre-processing step), solving a reformulation of the bilevel problem, and adjusting thresholds via the approximate gradient descent method (a post-processing step).

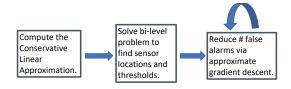


Fig. 1. A diagram showing the process of computing sensor locations and alarm thresholds. The final step is performed iteratively until encountering false negatives.

H. A system with multiple possible network configurations

Sensors are placed once and operate for extended periods of time during which network conditions may change. In addition to the power injection variability considered in the bilevel problem (2f)–(2g), the network may be reconfigured using switches, resulting in multiple topologies. Network reconfiguration can significantly affect which locations in the network are best suited for sensor placement.

To address this, our sensor placement formulation can be extended to consider a set of possible network topologies. In this context, the goal of the sensor placement problem is to determine locations for sensors and sensor alarm thresholds that will reliably identify voltage limit violations for any topology within the considered set. The sensor locations must be selected once and remain consistent across all topologies, but the sensor alarm thresholds may vary between different topologies. With m different topologies for an n-bus system, the computational complexity is similar to solving a sensor placement problem for an $(n \times m)$ -bus system with one configuration. For each topology, we compute different conservative linear approximations of the power flow equations and introduce additional lower-level problems (i.e., for a specific bus, each configuration introduces different constraints (10a)– (10c) and (11a)-(11c)). The sensor locations have one set of binary variables since they remain the same across topologies.

IV. NUMERICAL TESTS

In this section, we perform numerical experiments on a number of test cases to analyze the sensor locations and thresholds, demonstrate the advantages of our problem reformulations and the post-processing step, and compare results and computational efficiency from different problem formulations.

The test cases we use in these experiments are the 10-bus system case10ba, the 33-bus system case33bw, and the 141bus system case141 from MATPOWER [17]. For the CLAs, we minimize the ℓ_1 error with 1000 samples in the first iteration and 4000 additional samples in a sample selection step, and we choose a quadratic output function of voltage magnitude. (See [12] for a discussion on computationally efficient iterative methods for computing CLAs and variants of CLAs that approximate different quantities in order to improve their accuracy.) All power injections vary within 50% to 150% of the load demand values given in the MATPOWER files except for case33bw where we consider a variant with solar panels at buses 18 and 33. The active power demands at these two buses vary within -200% to 150% of the nominal values. Note that a manufacturer can provide actual data regarding the range of power injections from DERs like solar PV.

We implement the single-level reformulations of the sensor placement problem in MATLAB using YALMIP [18] and use Gurobi as a solver with a MIP gap tolerance of 0.5%, unless otherwise stated. The AGD step size is $\epsilon_{AGD}=2\times10^{-4}$ per unit. The value of δ in the objective (4) is 0.02. In case10ba, case33bw, and case141, the lower voltage limits are 0.90, 0.91, and 0.92, respectively, and the computation times for the CLAs are 58, 198, and 1415 seconds, respectively.

A. Sensor locations

We compare the quality of results and the computation time from the following reformulations: (i) the KKT formulation (7), (ii) the duality-based bilinear formulation (10) and (11), (iii) the MILP formulation without applying the binary variable removal (BVR) pre-processing, and (iv) the MILP formulation with the BVR pre-processing.

The first test case is the 10-bus system *case10ba*, a simple single-branch network. We consider a variant where the nominal loads are 60% of the values in the MATPOWER file. The results from each formulation place a sensor at the end of the branch (furthest bus from the substation) with an alarm threshold of 0.9 per unit (at the voltage limit). Fig. 2a compares computation times from the four formulations. The the KKT formulation takes 26.7 seconds while the bilinear, the MILP without BVR, and the MILP formulations take 1.96, 1.95, and 1.54 seconds, respectively. Since the sensor threshold for the KKT and both MILP formulations is at the voltage limit, AGD is not needed. Conversely, the bilinear formulation gives a higher alarm threshold. As a result, the AGD method is applied as a post-processing step to achieve the lowest possible threshold without introducing false alarms. The number of false positives reduces from 5.48% to 0%. Executing the AGD method takes 0.11 seconds.

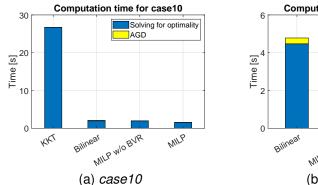
The second test case is the 33-bus system *case33bw*, which has multiple branches. In this example, we demonstrate the efficacy of our approach in handling a system with complex components through the implementation of volt-VAR control, which represents smarter inverter behavior (whose characteristics are described in [19]). To incorporate the behavior of volt-VAR control, we enhance the power flow solver used to compute the CLAs by integrating an additional fixed-point iterative method. Table I shows the computation times for the bilinear and the two MILP formulations. We exclude the computation time for the KKT formulation since the solver fails to find even a feasible (but potentially suboptimal) point within 55000 seconds (15 hours). Our final test case is the 141bus system case141. Similar to the 33-bus system, the solver could not find the optimal solution for the KKT formulation within a time limit of 15 hours. It is evident the KKT formulation is intractable. Table I again shows the results for this test case, and Figs. 2b and 2c compare the computation times for the bilinear and MILP formulations.

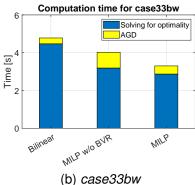
Table I shows both the computation times and the results of randomly drawing sampled power injections within the specified range of variability, computing the associated voltages by solving the power flow equations, and finding the number of false positive alarms (i.e., the voltage at a bus with a sensor is outside the sensor's threshold but there are no voltage

TABLE I
RESULTS SHOWING SENSOR ALARM THRESHOLDS AND NUMBER OF FALSE POSITIVES FROM KKT, BILINEAR, AND MILP FORMULATIONS.

	KKT§	Bilinear		MILP w/o BVR		MILP				
	case10ba	case10ba	case33bw	case141	case10ba	case33bw	case141	case10ba	case33bw	case141
Computation Optimality	26.7	1.96	4.47	46.52	1.95	3.19	69.6	1.54	2.87	22.95
time [s] AGD	_	0.11	0.31	18.3	_	0.83	16.5	_	0.43	13.8
Sensor location(s)	10	10	14, 15, 17, 31	79, 80, 82, 85	10	14, 31	76, 80, 86	10	14, 30	80, 86
Sensor threshold(s)	0.9	0.9017	0.91, 0.91,	0.92, 0.9213,	0.9	0.9195	0.92, 0.9295,	0.9	0.9195	0.929
Sensor uneshold(s)	0.9	0.9017	0.9118, 0.9126	0.93, 0.93		0.9195	0.9295		0.9185	0.9295
with AGD			0.91, 0.91,	0.92, 0.9212,		0.9158	0.92, 0.9211,		0.9163	0.9213
with AGD —		0.9	0.9107, 0.9112	0.9218, 0.9201		0.9125	0.9201		0.9161	0.9201
# feasible points	7317	7317	9753	9955	7317	9753	9955	7317	9753	9955
% false positive(s)	0%	4.01%	1.91%	72.07%	0%	18.97%	66.64%	0%	7.64%	66.64%
with AGD		- 0% -	$ \overline{0.24\%}$	0.03%		1.55%	$-\frac{1}{0.01}$ $-\frac{1}{0.01}$		1.34%	$\overline{0.01}$ %
% false negatives	0%	0%	0%	0%	0%	0%	0%	0%	0%	0%

[§]The solver does not find a solution to case33bw and case141 within 55000 seconds.





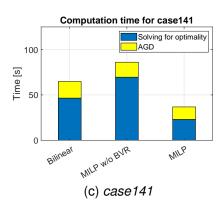


Fig. 2. Bar graphs showing the computation times for solving various problem formulations and executing the approximate gradient descent method.

violations in the system). The results for the 33-bus and 141bus test cases given in Table I illustrate the performance of the proposed reformulations. Whereas the KKT formulation is computationally intractable, our proposed reformulations find solutions within approximately one minute, where the MILP formulation with the BVR method typically exhibits the fastest performance. The solutions to the reformulated problems place a small number of sensors (two to four sensors in systems with an order of magnitude or more buses). No solutions suffer from false negatives since all samples where there is a voltage violation trigger an alarm. There are a number of false alarms prior to applying the AGD that after its application decrease dramatically to a small fraction of the total number of samples (1.34% and 0.01% in the 33-bus and the 141bus systems, respectively). These observations suggest that our sensor placement formulations provide a computationally efficient method for identifying a small number of sensor locations and associated alarm thresholds that reliably identify voltage constraint violations with no false negatives (missed alarms) and few false positives (spurious alarms).

B. MIP gap tolerance, solver times, and solution quality

Each of the problem formulations involves a mix of continuous and binary variables, thus requiring solution from mixed-integer programming solvers like Gurobi. These solvers are based on branch-and-bound algorithms that iteratively update upper and lower bounds on the optimal solution, terminating when the difference between these bounds (i.e., the "MIP Gap") converges to a value less than a specified tolerance.

RESULTS FROM DIFFERENT MIP GAP TOLERANCES FOR case33bw

	Bili	near	MILP		
MIP Gap 7	0.3%	0.1%	0.3%	0.1%	
Computation	Optimality	4.81	5.51	3.01	5.35
time [s]	AGD	0.37	0.22	0.75	0.13
Sensor loc	cation(s)	15, 31	17, 32	14, 32	17, 33
Sensor three	ashald(s)	0.9166	0.9129	0.9195	0.9120
Sensor und	esiloid(s)	0.9182	0.911	0.918	0.9115
with AGD		0.9144	0.9106	0.9167	0.9105
		0.9162	0.9102	0.9132	0.9101
# feasible points		9753	9753	9753	9753
% false positive(s)		4.34%	2.20%	6.91%	1.96%
with AGD		0.99%	0.08%	0.71%	0.09%
% false negatives		0%	0%	0%	0%

The choice of MIP gap tolerance can directly affect the computation time and the quality of the solution (e.g., sensor thresholds and number of sensors).

We investigate the effect of two different choices for the MIP gap tolerance, 0.3% and 0.1%, on the bilinear and MILP formulations for the test cases *case33bw* and *case141*. The results are shown in Table II and Table III, respectively. As expected, these results suggest that decreasing the MIP gap tolerance can increase the computational times in some cases, with approximately twice as much time required for the MILP formulation with *case33bw* and the bilinear formulation with *case141*. Nevertheless, the computation times with the smaller MIP gaps are still reasonable in all cases (less than two minutes), and the MIP gap tolerance has little impact on the computation times for other cases. Regarding solution quality, tighter MIP gap tolerances can lead to fewer sensors

TABLE III
RESULTS FROM DIFFERENT MIP GAP TOLERANCES FOR case 141

		Bilin	MILP		
MIP Gap Tolerance		0.3%	0.1%	0.3%	0.1%
Computation	Optimality	49.9	94.3	27.6	27.7
time [s]	AGD	18.3	12.6	10.5	10.5
Sensor loc	ention(s)	79, 80,	79, 80,	80,	80,
Schsol loc	Zation(s)	82, 85	85	86	86
Sensor three	achold(c)	0.92, 0.9213	0.9297,	0.929,	0.929,
Sensor tim	esiloid(s)	0.93, 0.93	0.92, 0.93	0.9295	0.9295
with AGD		0.92, 0.9212	- 0.922,	0.9213,	0.9213
		0.9218, 0.9201	0.92, 0.9202	0.9201	0.9201
# feasible points		9955	9955	9955	9955
% false positives		72.07%	71.74%	66.64%	66.64%
with AGD		0.03%		0.01%	$\overline{0}.\overline{0}1\overline{\%}$
% false negatives		0%	0%	0%	0%

(as in the bilinear formulation with *case141*) and fewer false positives (as in *case33bw*). However, the results are not always necessarily improved by tightening the MIP gap tolerance, as the 0.1% tolerance leads to the same number of sensors and *more* false positives than the 0.3% tolerance after applying the AGD method. This suggests a potential benefit of assessing the performance of multiple "nearly optimal" solutions obtained with different MIP gap tolerances.

C. Multiple configurations

The previous results described the sensor placements for the *case10ba*, *case33bw*, and *case141* systems in their nominal network topologies. We next demonstrate the effectiveness of our problem reformulations for variants of these systems with multiple network configurations. We consider a variant of the *case33bw* system with three different network configurations and two solar PV generators installed at buses 18 and 33. The first configuration is the nominal topology given in the MATPOWER version of the test case. In the second configuration, the line connecting buses 6 and 7 is removed and a new line connecting buses 4 and 18 is added. The third configuration removes the line connecting buses 6 and 26 and adds a new line connecting buses 25 and 33. All network configurations are displayed in Fig. 3.

Table IV shows the results from using the bilinear and MILP formulations to solve the multiple-configuration problem for this case. The results generally mirror those from the single-network-configuration test cases shown earlier in that compu-

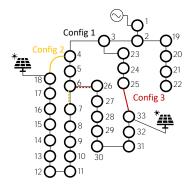


Fig. 3. The *case33bw* system with three different network configurations. A dash line means that line is removed when a solid line with the same color is added such that the system has a radial topology in each configuration.

tation times are still reasonable (approximately a factor of four larger) and there are no false negatives and a small number of false positives after applying the AGD method.

We note that some configurations may not need to utilize all available sensors. To show this, we describe an experiment that considers each configuration separately. In this experiment, we compare the number of sensors and the locations of the sensors with those in the previous experiment. As Table V shows, configurations 1 and 2 require only two sensors while configuration 3 requires only one sensor as opposed to three-sensor solution obtained from the multiple-configuration problem. This demonstrates the need to jointly consider network topologies in one problem for such situations.

D. Comparison to a heuristic sensor placement technique

We next demonstrate the performance of the heuristic sensor placement technique described in Section III-E in the context of multiple network configurations for the three-configuration variant of the test case case33bw. We study the effects of two versions of this heuristic: (i) place sensors at the end of all branches based on configuration 1 and (ii) place sensors at the ends of branches considering all configurations. The results are shown in Table VI. For the first version of this heuristic, we place four sensors. This technique works well in configuration 1; however, it introduces false negatives (failures to alarm in cases with voltage limit violations) in approximately 10% to 20% of the power injection samples for configurations 2 and 3 since the sensors are instead located in the middle of some branches and thus do not capture all possible violations. To reduce the number of false negatives, we next consider sensor locations based on all configurations, i.e., the second version of the heuristic. This results in six sensors being placed and only one occurrence of a false negative. Comparing to our approach (refer to Table IV), we only need three sensors (as opposed to six sensors) to detect all violations. This shows the necessity of using an optimization formulation to obtain sparse sensor placement solutions, since a naive approach where sensors are placed at all buses will always avoid false positives.

V. CONCLUSION

This paper has formulated a bilevel optimization problem that seeks to minimize the number of sensors needed to detect violations of voltage magnitude limits in an electric distribution system. We first addressed the power flow nonlinearities in the lower-level problem via previously developed conservative linear approximations of the power flow equations. Due to computational challenge from the bilevel nature, we therefore exploited structure specific to this problem to obtain single-level mixed-integer programming formulations that avoid introducing unnecessary additional discrete variables. We also demonstrated how to obtain a mixedinteger-linear programming formulation by discretizing the sensor thresholds. Furthermore, we developed extensions to these reformulations that consider the possibility of multiple network topologies. Our proposed sensor placement reformulations require substantially less computation time than standard reformulation techniques. We also developed a postprocessing technique that reduces the number of false alarms

TABLE IV RESULTS FOR THE case33bw system with three network configurations.

			Bilinear			MILP		
		Config 1	Config 2	Config 3	Config 1	Config 2	Config 3	
Computation	Optimality		20.1		 7.93 			
time [s]	AGD	0.53	0.55	0.10	0.86	0.93	0.40	
Sensor loca	ation(s)	8, 14, 26, 33			9, 14, 30			
Sancor thre	acholde	0.91, 0.919,	0.9126, 0.91,	0.91, 0.91,	0.91, 0.919,	0.9185, 0.91,	0.91, 0.91,	
Sensor und	Sensor thresholds		0.91, 0.9111	0.9106, 0.9137	0.919	0.919	0.9165	
with AGD		0.91, 0.9167,	0.9117, 0.91,	0.91, 0.91,	0.91, 0.9167,	0.9166, 0.91,	0.91, 0.91,	
		0.91, 0.9104	0.91, 0.9109	0.9101, 0.9136	0.9168	0.9189	0.9151	
# feasible points		9560	8292	9112	9560	8292	9112	
% false positives		6.19%	4.86%	1.28%	9.19%	12.38%	6.55%	
with AGD		1.86%	1.21%	0.05%	$ \overline{2.87\%}$ $ -$	3.38%	$-\frac{1}{2.38\%}$	
% false negatives		0%	0%	0%	0%	0%	0%	

TABLE V
RESULTS FOR case33bw WITH THREE NETWORK CONFIGURATIONS WHERE SENSOR LOCATIONS ARE NOT NECESSARY THE SAME.

	Config 1	Config 2	Config 3
Sensor location(s)	14, 30	9, 31	30
Sensor threshold(s)	0.9195 0.9190	0.9185 0.9185	0.9185
with AGD	0.9167 0.9168	0.9164 0.9186	0.9151
# feasible points	9560	8292	9112
% false positives	10.48%	12.45%	14.53%
with AGD	2.89%	3.06%	2.38%
% false negatives	0%	0%	0%

TABLE VI RESULTS FOR *case33bw* with three network configurations using two variants of the heuristic technique

Case		Config 1	Config 2	Config 3
	Sensor location(s)		18, 22, 25, 3	3
g	Sensor thresholds	0.91	, 0.91, 0.91,	0.91
sio	# feasible points	9560	8292	9112
version	% false positives	0%	0%	0%
	% false negatives	0%	16.26%	8.85%
	Sensor location(s)	 7, 1	8, 22, 25, 26	5, 33
	Sensor thresholds	0.91, 0.91	, 0.91, 0.91,	0.91, 0.91
version	# feasible points	9560	8292	9112
Je.	% false positives	0%	0%	0%
	% false negatives	0.01%	0%	0%

via an approximate gradient descent method. The combination of the bilevel problem reformulation and this post-processing technique allows us to compute sensor locations and alarm thresholds that result in few false alarms and no missed alarms, as validated numerically using out-of-sample testing.

In our future work, we seek to identify where the violations occur using the information obtained from CLAs and solutions from the sensor placement problem. Furthermore, we intend to use the sensor locations and thresholds resulting from the proposed formulations to design corrective control actions which ensure that all voltages remain within safety limits.

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