

Internal Connections (Bottom View)



REMARK: Tolerance of outline dimensions: ±0.1(.004).

UNIT: mm (inch)



Pin Assignment				
1	2	3		
E	С	В		

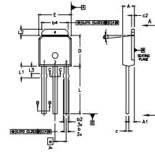
TO-92



表 8				
端子番号	端子記号	端子説明		
1	VSS	GND 端子		
2	VIN	電圧入力端子		
3	VOUT	電圧出力端子		



1 電源



E → 64 → (€)0,000 (0,003)(€)(A)(6)	1 2	HEXFET
	ELANGO PLANE	1,- GATE 2,- DRAM 3,- SOURCE 4,- DRAM
5 F F F F F F F F F	# A1	4,- DRAM