# FORMAL VERIFICATION OF FINITE STATE MACHINES

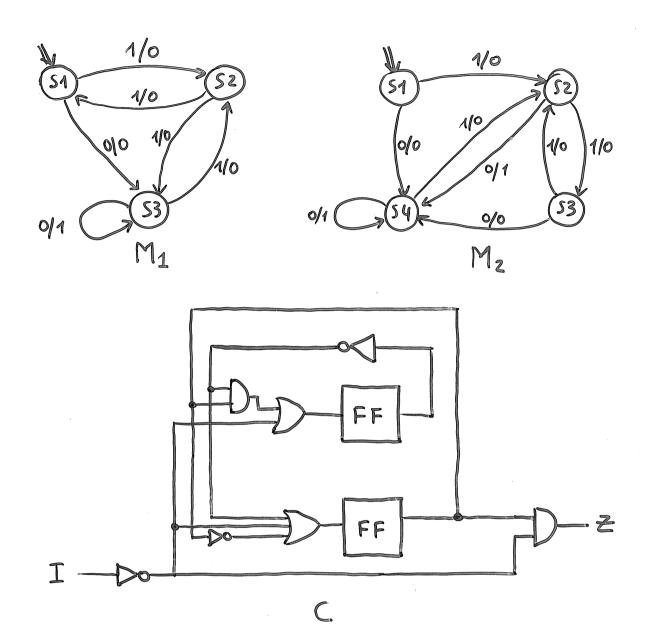
# Why?

- · Bugs in hardware are expensive
- · Simulation is inadequate to guarantee correctness
- · Synthesis tools may also have bugs
- · A specification may not match the specifier's intent

# Approaches for Formal Verification

- · Process algebras and trace theory (CCS, CSP)
- · Automatic theorem provers (Boyer - Moore, HOL)
- · Temporal logic (CTL)
- · Finite state models

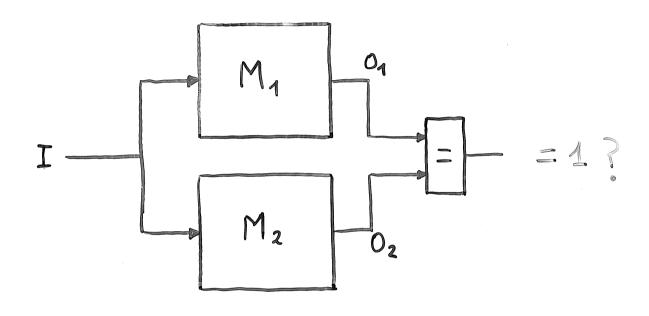
## Formal verification of FSMs



- · Does My have property X?
- · Are M1 and M2 equivalent?
- · Are My and C. equivalent?

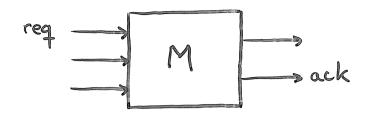
## Equivalence checking

- 1. Calculate the Product Machine
- 2.- Verify "observational equivalence" from the reset state



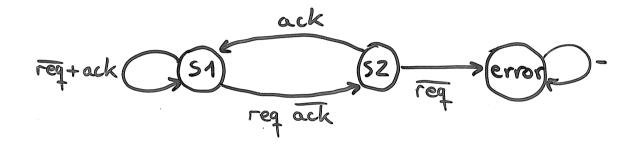
- · State: S= (S1, S2)
- Transition function:  $S(S, I) = (\delta_1(S_1, I), \delta_2(S_2, I))$
- Output: 0 = 0, ⊕ 02

# Verification of properties

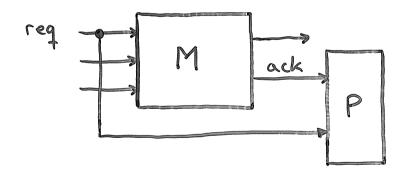


After req rising, ack will always fall before req falling

. FSM for the property (possibly non-deterministic)

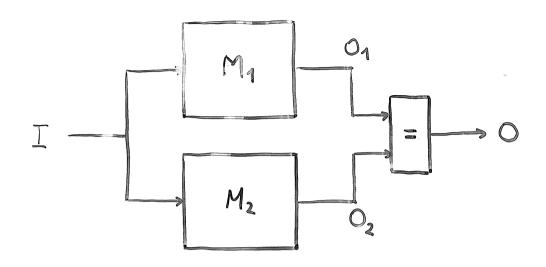


#### · Verification



correct if the product machine never reaches a state (\*, error)

# Building a product machine



for equivalence checking:

$$M_1 = (I, S_1, O_1, \delta_1, \lambda_1, S_1^{\circ})$$

$$M_2 = (I, S_2, O_2, \delta_2, \lambda_2, S_2^{\circ})$$

I: input alphabet, O: output alphabet

S: set of states, So: set of initial states

S: transition function A: output function

$$M = M_4 \times M_2 = (I, S, O, \delta, \lambda, S^{\circ})$$

$$S \subseteq S_1 \times S_2 \iff \text{must be}$$
  
 $O = \{0, 1\} \qquad \text{calculated}$ 

$$\delta = (\delta_1, \delta_2)$$

$$\lambda = \overline{\lambda_1 \oplus \lambda_2}$$

# Reachability Analysis (FSM traversal)

· Algorithm for explicit enumeration

RS: set of reachable states

PS: set of states to be processed

$$RS = \{S_0\}$$
  
 $PS = \{S_0\}$ 

while PS = Ø do.

ns = first\_element (PS); PS = PS - {ns} for each state ss successor of ns do

if ss 
$$\notin$$
 RS then

RS = RS U {ss}

PS = PS U {ss}

end if

end for

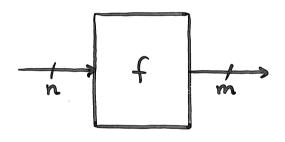
end while

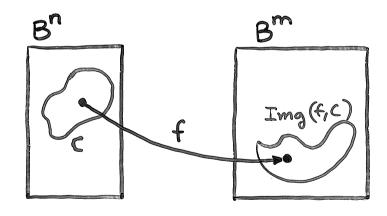
- · RS contains all reachable states
- · Data structure for PS
  - LIFO -> Depth First Traversal
  - FIFO -> Breadth-First Traversal
- · Problem: the number of states can be very large (exponential on the number of state signals)

# Symbolic traversal

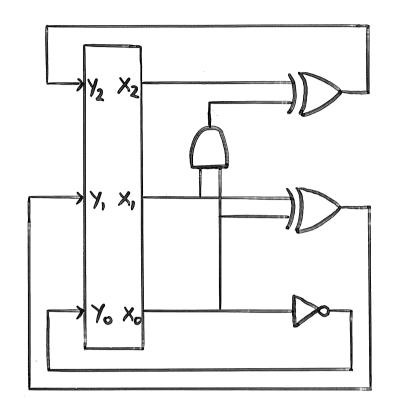
- · Several states processed in parallel
- · Usually breadth-first traversal
- · Symbolic representation of
  - sets of states
  - transition function

## Image computation





# Example: Modulo 8 counter



$$X = (X_2, X_1, X_0)$$

Transition function:  $Y = \delta(X)$ 

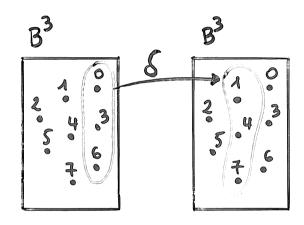
$$(\gamma_2, \gamma_1, \gamma_0) = (\delta_2(x), \delta_1(x), \delta_0(x))$$

$$y_0 = \delta_0(X) = \overline{X}_0$$

$$y_1 = \delta_1(x) = x_0 \oplus x_1$$

$$y_2 = \delta_2(x) = (x_0 x_1) \oplus x_2$$

# Image computation (example)



· Sets of states can be represented by characteristic functions

$$C(X) = \overline{X_0} \cdot \overline{X_1 \oplus X_2} + \overline{X_2} \overline{X_1} \overline{X_0}$$

$$= \overline{X_2} \overline{X_1} \overline{X_0} + \overline{X_2} \overline{X_1} \overline{X_0} + \overline{X_2} \overline{X_1} \overline{X_0}$$

#### Transition relations

· The transition function defines a relation between pairs of states

$$x R y \iff y = \delta(x)$$

· Relations can be represented by boolean formulae

$$x Ry \Leftrightarrow T_R(x,y) = 1$$

• Let M be an FSM and  $\delta = (\delta_1, ..., \delta_m)$  its transition function. The <u>transition</u> relation of M is

$$T_{R}(x_{1},...,x_{n},y_{1},...,y_{m}) = \bigwedge_{i=1}^{m} (y_{i} \equiv \delta_{i}(x_{1},...,x_{n}))$$

## Transition relation (example)

$$y_0 = \delta_0(x) = \overline{X}_0$$
 $y_1 = \delta_1(x) = X_0 \oplus X_1$ 
 $y_2 = \delta_2(x) = (x_0 X_1) \oplus X_2$ 

$$T_{R}(X,Y) = \left[ Y_{0} = \overline{X}_{0} \right] \cdot \left[ Y_{1} = (X_{0} \oplus X_{1}) \right] \cdot \left[ Y_{2} = \left( (X_{0} X_{1}) \oplus X_{2} \right) \right]$$

$$T_{R}(X,Y) = X_{2}X_{1}X_{0} Y_{2}Y_{1}Y_{0} + X_{2}X_{1}X_{0} Y_{2}Y_{1}Y_{0}$$

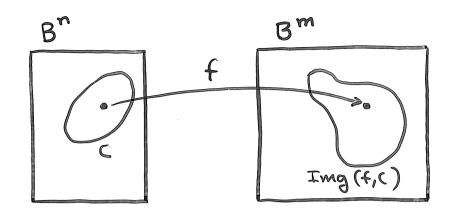
$$(2,4) \in \mathbb{R} ?$$

# Manipulation with characteristic functions

odd 
$$(x) = X_0$$
  
 $m_3(x) = \overline{X_0} \cdot \overline{X_1 \oplus X_2} + \overline{X_2} X_1 X_0$ 

$$(0,1) \qquad (6,7)$$

## Image computation with transition relations

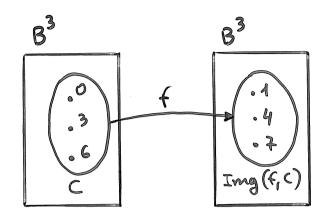


$$Y = Img(f, C) = \exists_X [T_R(x, y) \cdot C(x)]$$

#### Two steps:

1. 
$$P = T_R(x, y) \cdot C(x)$$
  
finds all pairs  $(x, y)$  such  $C(x)$ 

# Image computation (example)



$$((x) = \overline{X}_0 \ \overline{X_1 \oplus X_2} + \overline{X_2} X_1 X_0$$

1) 
$$P = T_R(X, Y) \cdot C(X) =$$

$$= \overline{X_2} \overline{X_1} \overline{X_0} \overline{Y_2} \overline{Y_1} y_0 + \overline{X_2} X_1 X_0 y_2 \overline{Y_1} \overline{Y_0} +$$

$$X_2 X_1 \overline{X_0} y_2 y_1 y_0$$

# Symbolic Breadth-First Traversal

symbolic-traversal (S, So)

Reached = From = So;

repeat

To = Img (S, From);

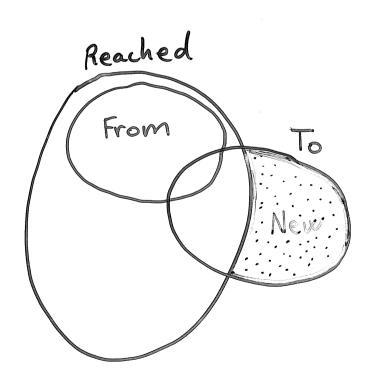
New = To - Reached;

From = New;

Reached = Reached U New;

until New = Ø

return Reached



# Symbolic Breadth-First Traversal

- · Number of iterations: sequential depth of the FSM i.e. the longest of the shortest path connecting each state to an initial state
- · Implementation

$$\begin{array}{ccccc}
A & U & B & \longrightarrow & X_A + X_B \\
A & \cap & B & \longrightarrow & X_A \cdot X_B \\
A & - & B & \longrightarrow & X_A \cdot \overline{X}_B
\end{array}$$

XA is the characteristic function of A

· Efficient manipulation of characteristic functions by representing them with BDDs

# Backward Traversal

backward\_traversal (S, S°, P)

/\* P is the property to be verified \*/

Reached = From = P;

repeat

To = Img (5<sup>-1</sup>, From)

New = To - Reached

if S° ∈ New return FALSE

From = New

Reached = Reached U New

until New = Ø

return TRUE

Transition Relation for  $S^{-1}$  (Preimage)  $TR^{-1}(x,y) = TR(y,x)$ 

## Results

circuit	# latches	# size	# states	depth	CPU (sec)
sand	6	1310	32	4	16.9
scf	8	2208	115	16	18.7
s344	15	269	2625	7	34.6
s444	21	352	8865	151	186.7
s526	21	445	8868	151	126.1
s713	19	591	1544	7	63.7
s953	29	766	504	11	70.2
s1238	18	1042	2616	3	43.6
cbp.32.4	32	480	4.29e+09	2	14.1
minmax32	96	1874	1.32e+28	4	444.4
sbc	28	1670	154593	10	2903.7
key	228	3865	1.35e+68	17	5706.2

(from Touati et al., ICCAD-90)

## Conclusions

- · Formal verification is crucial for the design of complex systems
- · Many formal verification techniques suffer the state explosion problem
- · Symbolic traversal enables to perform reachability analysis with succint representations of sets of states
- Much effort has been focused to control circuits (FSM models)
- · On-going research for data-path circuits