

Circuits (ENGG1203)

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1 Operational Amplifier

An operational amplifier (op-amp) is a voltage controlled voltage source with high gain. The equivalent circuit model of an op-amp is shown on Figure 1. The voltage V_i is the differential input voltage $V_i = V_p - V_n$. R_i is the input resistance of the device and R_o is the output resistance. The gain parameter A is called the open loop gain.

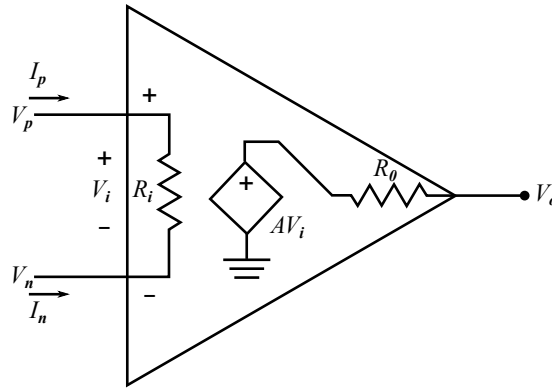


FIGURE 1 – Equivalent circuit model of op-amp device

If there is no load at the output, the output voltage is

$$V_o = AV_i = A(V_p - V_n) \quad (1.1)$$

From the equation above, output voltage V_o is a function of the difference between the input voltage V_p and V_n . For this reason, the op-amps are also called **difference amplifiers**.

The figure that reflects relation between the output voltage and the input voltage is called the voltage transfer curve and is fundamental in designing and understanding amplifier circuits. The voltage transfer curve of the op-amp is shown on Figure 2.

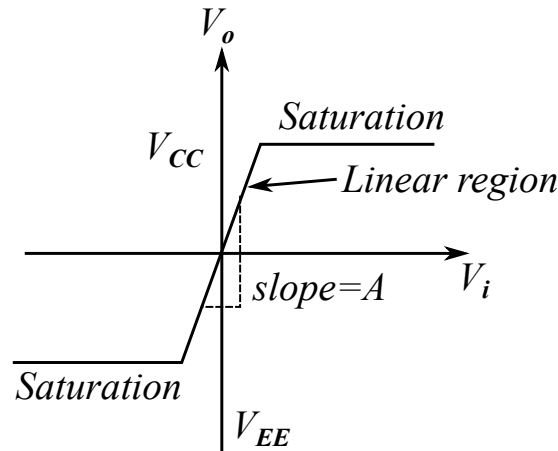


FIGURE 2 – Op-amp voltage transfer characteristics

1.1 The ideal op-amp model

Practically speaking, an ideal op-amp is a device which acts as an ideal voltage controlled voltage source. Referring to Figure 3, this implies that the device will have the following characteristics :

- * No current flows into the input terminals of the device. This is equivalent to having an infinite input resistance $R_i = \infty$. In practical terms this implies that the amplifier device will make no power demands on the input signal source.
- * Have a zero output resistance ($R_o = 0$). This implies that the output voltage is independent of the load connected to the output.

In addition the ideal op-amp model will have infinite open loop gain ($A \rightarrow \infty$). The ideal op-amp model can be shown schematically on Figure 3.

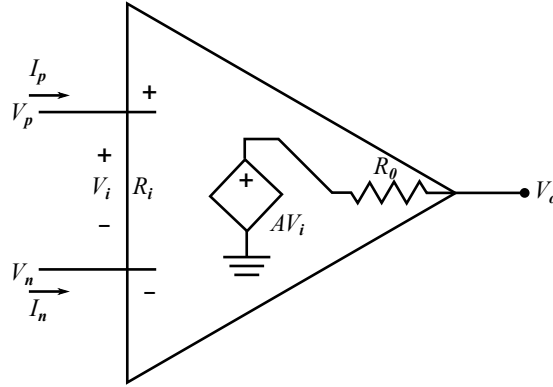


FIGURE 3 – Op-amp voltage transfer characteristics

In summary, the ideal op-amp conditions are :

- $I_p = I_n = 0$ **No current into the input terminals**
- $I_p = I_n = 0$ **No current into the input terminals**
- $R_i = \infty$ **Infinite input resistance**
- $R_o = 0$ **Zero output resistance**

Even though real op-amps deviate from these ideal conditions, the ideal op-amp rules are very useful and are used extensively in circuit design and analysis.

1.2 Ideal op-amp in a negative feedback configuration

When an op-amp is arranged with a negative feedback the ideal rules are :

- $I_p = I_n = 0$ **input current constraint**
- $V_n = V_p$ **input voltage constraint**

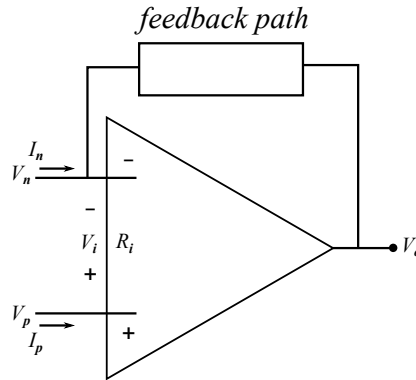


FIGURE 4 – Op-amp voltage transfer characteristics

1.3 Computational Devices

So far we have explored the use of op amps to multiply a signal by a constant. For the inverting amplifier the multiplication constant is the gain $-\frac{R_2}{R_1}$ and for the non inverting amplifier the multiplication constant is the gain $1 + \frac{R_2}{R_1}$. Op amps may also perform other mathematical operations ranging from addition and subtraction to integration, differentiation and exponentiation. We will next explore these fundamental “operational” circuits.

1.3.1 Summing Amplifier

A basic summing amplifier circuit with three input signals is shown in Figure 5.

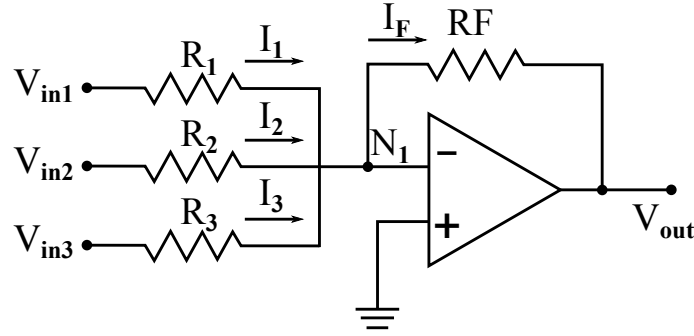


FIGURE 5 – Summing Amplifier

Apply KCL at Node N1 :

$$I_1 + I_2 + I_3 = I_F \quad (1.2)$$

By relating the currents I_1 , I_2 and I_3 to their corresponding voltage and resistance by Ohm's law :

$$\frac{V_{in1}}{R_1} + \frac{V_{in2}}{R_2} + \frac{V_{in3}}{R_3} = -\frac{V_{out}}{RF} \quad (1.3)$$

So V_{out} is :

$$V_{out} = -\left(\frac{RF}{R_1}V_{in1} + \frac{RF}{R_2}V_{in2} + \frac{RF}{R_3}V_{in3}\right) \quad (1.4)$$

From the expression (1.4), V_{out} is a sum of the input voltages with weighting factors given by the values of the resistors. If $R_1 = R_2 = R_3 = R$, then we will get :

$$V_{out} = -\frac{RF}{R}(V_{in1} + V_{in2} + V_{in3}) \quad (1.5)$$

The output voltage is the sum of the input voltages with a multiplication constant $\frac{RF}{R}$. For a special case when $RF = R$, the output voltage is the sum of the inputs :

$$V_{out} = -(V_{in1} + V_{in2} + V_{in3}) \quad (1.6)$$

The input resistance seen by each source connected to the summing amplifier is the corresponding series resistance connected to the source. Therefore, the sources do not interact with each other.

1.3.2 Difference Amplifier

This fundamental op amp circuit, shown on Figure 2, amplifies the difference between the input signals. The subtracting feature is evident from the circuit configuration which shows that one input signal is applied to the inverting terminal and the other to the non-inverting terminal.

$$V_- = \frac{V_{out} - V_{in1}}{R_1 + R_2}R_1 + V_{in1} = \frac{R_1}{R_1 + R_2}V_{out} + \frac{R_2}{R_1 + R_2}V_{in1} \quad (1.7)$$

$$V_+ = \frac{R_4}{R_3 + R_4}V_{in2} \quad (1.8)$$

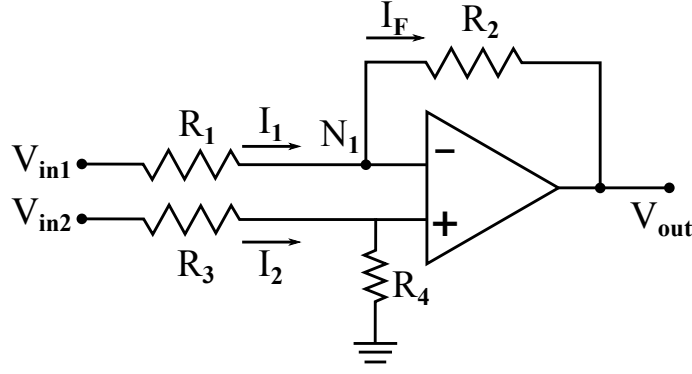


FIGURE 6 – Difference Amplifier

From the input voltage constraint ($V_+ = V_-$), the output voltage is :

$$V_{out} = V_{in2} \left(\frac{R_4}{R_3 + R_4} \right) \left(1 + \frac{R_2}{R_1} \right) - V_{in1} \frac{R_2}{R_1} \quad (1.9)$$

Note that in order to have a subtracting circuit which gives $V_{out} = 0$ for equal inputs, the weight of each signal must be the same. Therefore

$$\left(\frac{R_4}{R_3 + R_4} \right) \left(1 + \frac{R_2}{R_1} \right) = \frac{R_2}{R_1} \quad (1.10)$$

which holds only if

$$\frac{R_4}{R_3} = \frac{R_2}{R_1} \quad (1.11)$$

The output voltage is now

$$V_{out} = \frac{R_2}{R_1} (V_{in2} - V_{in1}) \quad (1.12)$$

which is a difference amplifier with a differential gain of R_2/R_1 and with zero gain for the common mode signal. It is often practical to select resistors such as $R_4 = R_2$ and $R_3 = R_1$.

1.3.3 Buffer Amplifier

A **buffer amplifier** (sometimes simply called a buffer) is one that provides electrical impedance transformation from one circuit to another. A buffer is something that isolates or separates one circuit from another.

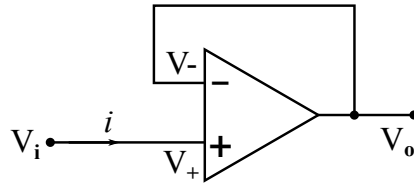


FIGURE 7 – Buffer Amplifier

$$i = 0 \quad (1.13)$$

$$v_o = v_i (= v_- = v_+) \quad (1.14)$$

A **buffer** is a **unity-gain** amplifier that has an extremely high input resistance and an extremely low output resistance. This means that the buffer can be modeled as a voltage controlled voltage source that has a gain of one.

1.3.4 Non-inverting&inverting Amplifier

Amplify difference with reference voltage

Amplify and invert difference with reference voltage

$$v_o - v_r = \left(1 + \frac{R_2}{R_1} \right) (v_i - v_r) \quad (1.15)$$

$$v_o - v_r = -\frac{R_2}{R_1} (v_i - v_r) \quad (1.15)$$

