**Lab Report : 02**

# Title: Reliulization of NAND gate using µ-wind

# 

# Course title:VLSI Circuits Design Lab.

Course code: CSE-412

4th Year1st Semester

**Date of Submission : 24-11-1015**

**Submitted to-**

**Abu Sayed Md. Mostafizur Rahaman**

**Associate Professor**

**Department of Computer Science and Engineering**

**Jahangirnagar University**

**Amina Khatun**

**Lecturer**

**Department of Computer Science and Engineering**

**Jahangirnagar University**

**Submitted by-**

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|  | *Class Roll* | *Exam Roll* | *Name* | *Session* | *Signature* |
|  | 407 | 120103 | Aditi Sarker | 2011-12 |  |