Name - Mohd Monish Roll No - 2021(A 063 Date 68 Nov 2021 Assignment - 🖛 04 aux 1 study of intend 8085 (Microprocussor). 1. Word bength - the total required to feed the instruction in memory is called instruction word size. the memory locution of 2005 micro processor

can accommodate a bits of data to

store 16-bits data they take are stored

in two consecutive memory location (1.6.2 byte) According to the instruction word size in 8005 microprocussor there are three type of intruction -. I- Bite instruction 2 - Byte instruction 3 - Byte instruction 1- Byte instruction !- They include opeocle

and operands in the same byte

operands are internal negister and code into

the instruction.

Instruction require one memory location to

Storie the single by in the memory Instruction having the only register or instruction in the Instruction. Operand circ also 1 Byte Instruction. Example - 1 Mov B, A (copy the condent of accomplator in rugista B. opeode - Mov operands - BIA. to the contents of xogister B. · opcoche - ADD operand - B. 2- Byte instruction - 1st Byte specific operated instruction require two memory location 1. Loud the Hexadecimal data 3214 in the accumulator operode MVI operand A, 32-11.

3-Byte instruction! - Three - byte instruction is type of instruction in which first orbits indication the opioche and the next byte specify the 16bit address the low-order and the high -orcher address is represented in the theird Byte-Example load Contents of memory 2000 H in the accumulator. 10A 2050H operade - LDA operard - 2050 H Aslo word lungth its chapends upon the widh of internal data bus, rigister ALU etc. An O-bit microprocessor can process of-bit data at a time the word length runger from 4 to 64 bits chepiraling upon the microcomputor 2. Memory Addressing capacity i- 2005 has a bit deldress bus thus it capable of 69kB of memory.