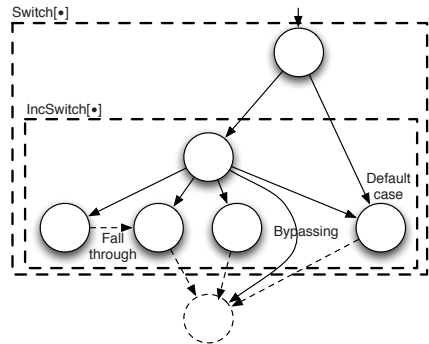


	Seq $[n_1, \dots, n_k]$: A block of sequential regions that have a single predecessor and a single successor.
	IfThenElse $[c, n, n_t, n_f]$: If-then-else region.
	IfThen $[c, n, n_t]$: If-then region.
	IncSwitch $[n, (c_1, n_1), \dots, (c_k, n_k)]$: Incomplete switch region. The outgoing conditions are pairwise disjoint and satisfy $\bigvee_{i \in [1, k]} c_i \neq \text{true}$.
	Switch $[n, (c_1, n_1), \dots, (c_k, n_k)]$: Complete switch region. The outgoing conditions are pairwise disjoint and satisfy $\bigvee_{i \in [1, k]} c_i = \text{true}$.

(a)Acyclic regions.



(c)Complete and incomplete switches.

	While $[c, h, s, b]$: A while loop.
	DoWhile $[c, h, b]$: A do-while loop.
	NatLoop $[h, b, e_1 \dots e_k]$: A natural loop. Note that there are no edges leaving the loop; outgoing edges must be virtualized during refinement to match this schema.

(b)Cyclic regions.