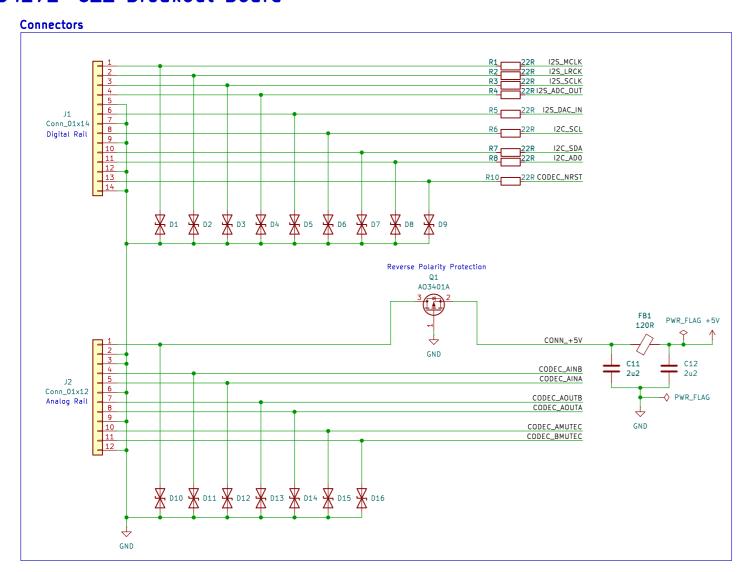
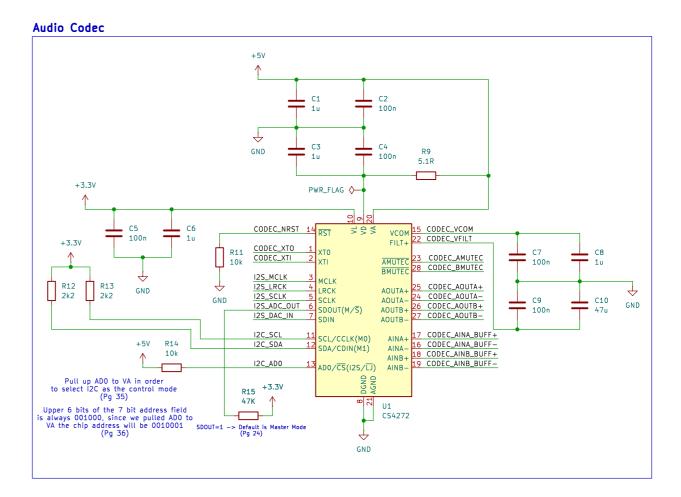
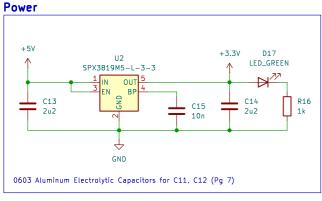
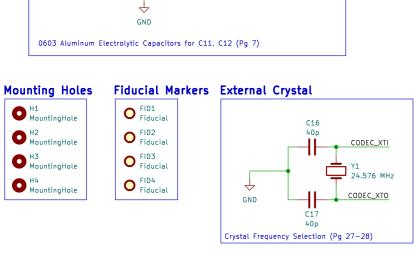
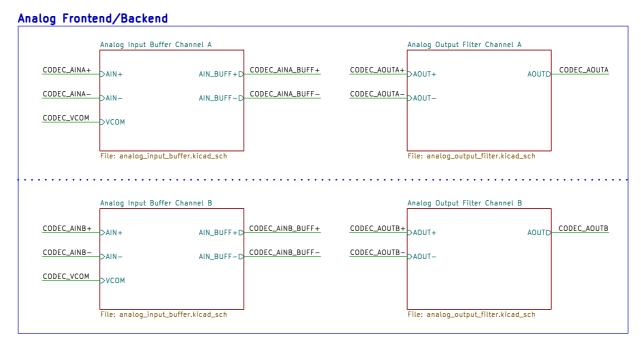
CS4272-CZZ Breakout Board

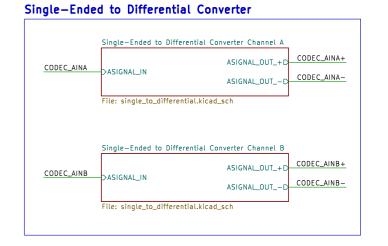












Analog Input Buffer Refer to Pg 32 for Circuit R17 634R C18 470p C0G Ouput signals are centered around 2.5V NE5532 C19 10u R18 U3A 91R ELEC C20 100n X7R -DAIN_BUFF+ +5٧ AIN+D R19 100k R20 10k ⁺ U3C GND NE5532 R21 634R VCOMD-C21 2.7n C0G C22 10n X7R C23 470p C0G GND GND \uparrow GND NE5532 C24 R24 R22 100k R23 U3B 10u 91R 10k ELEC -DAIN_BUFF-AIN-D Sheet: /Analog Input Buffer Channel A/ File: analog_input_buffer.kicad_sch Title: **Rev:** Id: 2/7 Size: A4 Date: KiCad E.D.A. 8.0.4

Analog Output Filter Refer to Pg 33 for Circuit C25 R25 4.99k 2.2n C26 COG +5٧ 100n X7R C27 470p C0G Ouput signals are centered around 2.5V GND R27 C28 22u ELEC R26 + > U4C R28 4.42k 2.32k NE5532 1.3K AOUT-D C29 1.5n C0G AOUT+D-NE5532 U4A R29 R30 R31 47k 1.33k 715R GND U4B R32 NE5532 C31 C30 GND 1.5k GND 6.8n 22u GND ELEC COG 2-Pole Butterworth Low Pass Filter Second Op Amp not used Sheet: /Analog Output Filter Channel A/File: analog_output_filter.kicad_sch Title: Size: A4 Date: Rev: KiCad E.D.A. 8.0.4 ld: 3/7 **Analog Input Buffer** Refer to Pg 32 for Circuit R33 634R C32 470p C0G Ouput signals are centered around 2.5V NE5532 C33 R34 U5A 10u 91R ELEC C34 100n X7R -DAIN_BUFF+ +5٧ AIN+D R35 100k R36 10k ⁺ U5C GND NE5532 R37 634R VCOMD-C35 2.7n C0G C36 10n X7R C37 470p C0G GND GND \uparrow GND NE5532 C38 R40 R38 100k R39 U5B 10u 91R 10k ELEC -DAIN_BUFF-AIN-D Sheet: /Analog Input Buffer Channel B/ File: analog_input_buffer.kicad_sch Title: Size: A4 Date: Rev: KiCad E.D.A. 8.0.4 ld: 4/7 Analog Output Filter Refer to Pg 33 for Circuit C39 R41 2.2n C40 4.99k COG +5٧ 100n X7R C41 470p C0G Ouput signals are centered around 2.5V GND R43 C42 22u ELEC R42 ÷ U6C 2.32k R44 4.42k NE5532 1.3K AOUT-D C43 1.5n C0G AOUT+D-NE5532 U6A R45 R46 R47 47k 1.33k 715R GND U6B NE5532 R48 C45 C44 GND 1.5k GND 6.8n 22u GND ELEC COG 2-Pole Butterworth Low Pass Filter Second Op Amp not used Sheet: /Analog Output Filter Channel B/ File: analog_output_filter.kicad_sch Title: Size: A4 Date: Rev: KiCad E.D.A. 8.0.4 ld: 5/7 Single-Ended to Differential Converter High Impedance Input and Low Impedance Output (Impedance Bridging) +5٧ C46 +5٧ Line input impedance: ~10k Line output imdpedance: 100 to 600 R 100n R49 1 k R50 1 k +2V5_VOLT_DIV GND ± U7C I NE5532 GND R51 C47 U7A R52 1 M NE5532 68n 1 k GND ASIGNAL_IND--DASIGNAL_OUT_+ R53 1k C48 R54 220p 1M R55 22k GND GND GND Differential output signals centered around 2.5V with ~2.8V peak to peak GND +2V5_VOLT_DIV Assumes input signal is zero centered with ~3V peak voltage R57 U7B R56 1 k 1k NE5532 -DASIGNAL_OUT_-R59 R58 1k 22k GND C49 100p Sheet: /Single-Ended to Differential Converter Channel A/File: single_to_differential.kicad_sch Title: Size: A4 Date: Rev: KiCad E.D.A. 8.0.4 Id: 6/7 Single-Ended to Differential Converter High Impedance Input and Low Impedance Output (Impedance Bridging) +5٧ C50 +5٧ Line input impedance: ~10k Line output imdpedance: 100 to 600 R 100n R60 1 k R61 1 k +2V5_VOLT_DIV GND ± 08C I NE5532 GND R62 C51 R63 1 M NE5532 68n 1 k GND ASIGNAL_IND--DASIGNAL_OUT_+ R64 C52 R65 1k 220p 1M R66 22k GND GND GND Differential output signals centered around 2.5V with ~2.8V peak to peak GND +2V5_VOLT_DIV Assumes input signal is zero centered with ~3V peak voltage R67 R68 U8B 1k 1k NE5532 -DASIGNAL_OUT_-R70 R69 1k 22k GND C53 100p Sheet: /Single-Ended to Differential Converter Channel B/File: single_to_differential.kicad_sch Title: Size: A4 Date: Rev: KiCad E.D.A. 8.0.4 Id: 7/7