

# Object Oriented Analysis and Design Lab

## Experiment 8

### State Diagram

#### Submitted By:

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AIML B3

#### Submitted To:

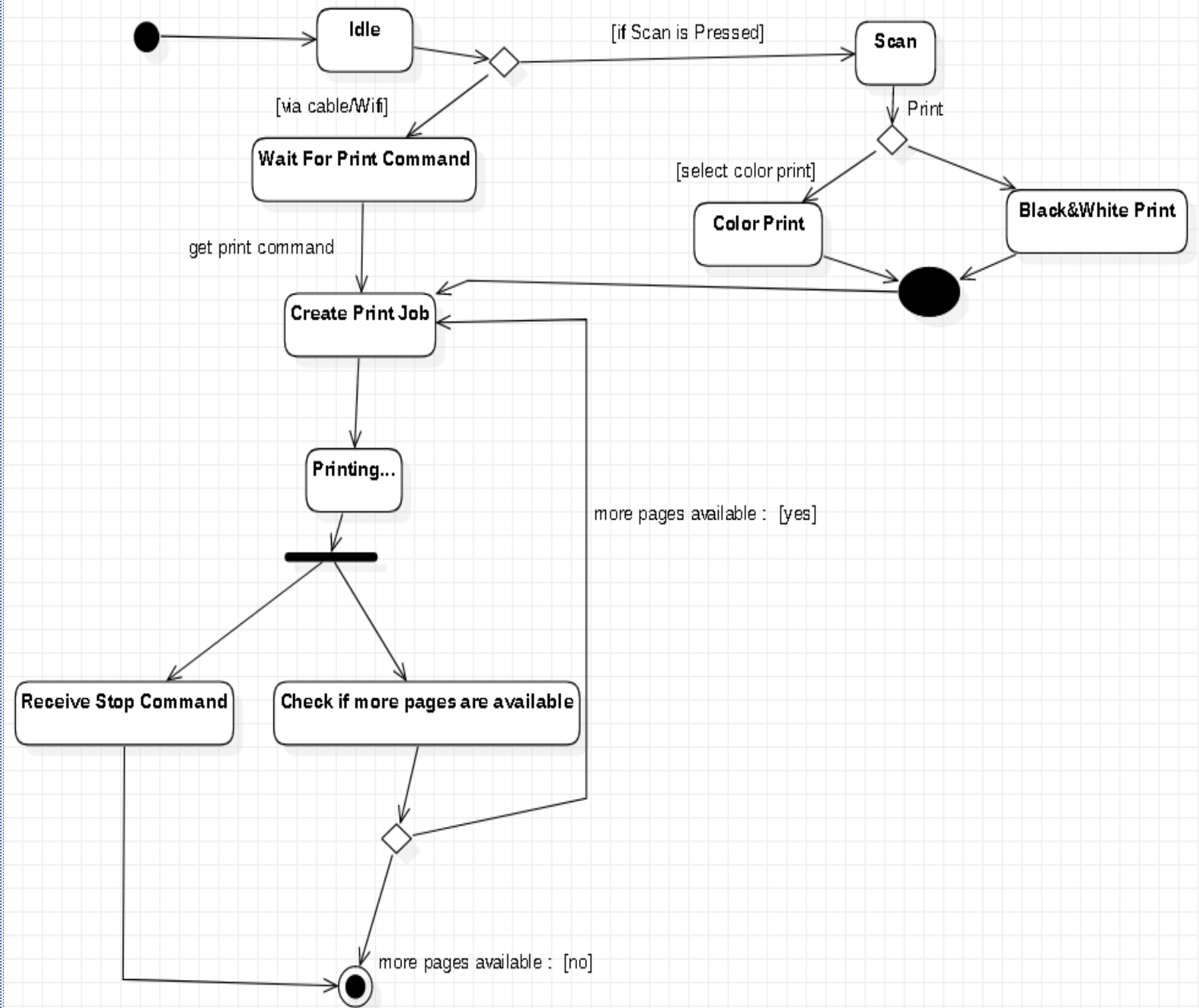
Dr. Sujoy Chatterjee

Asst. Professor

SOCS

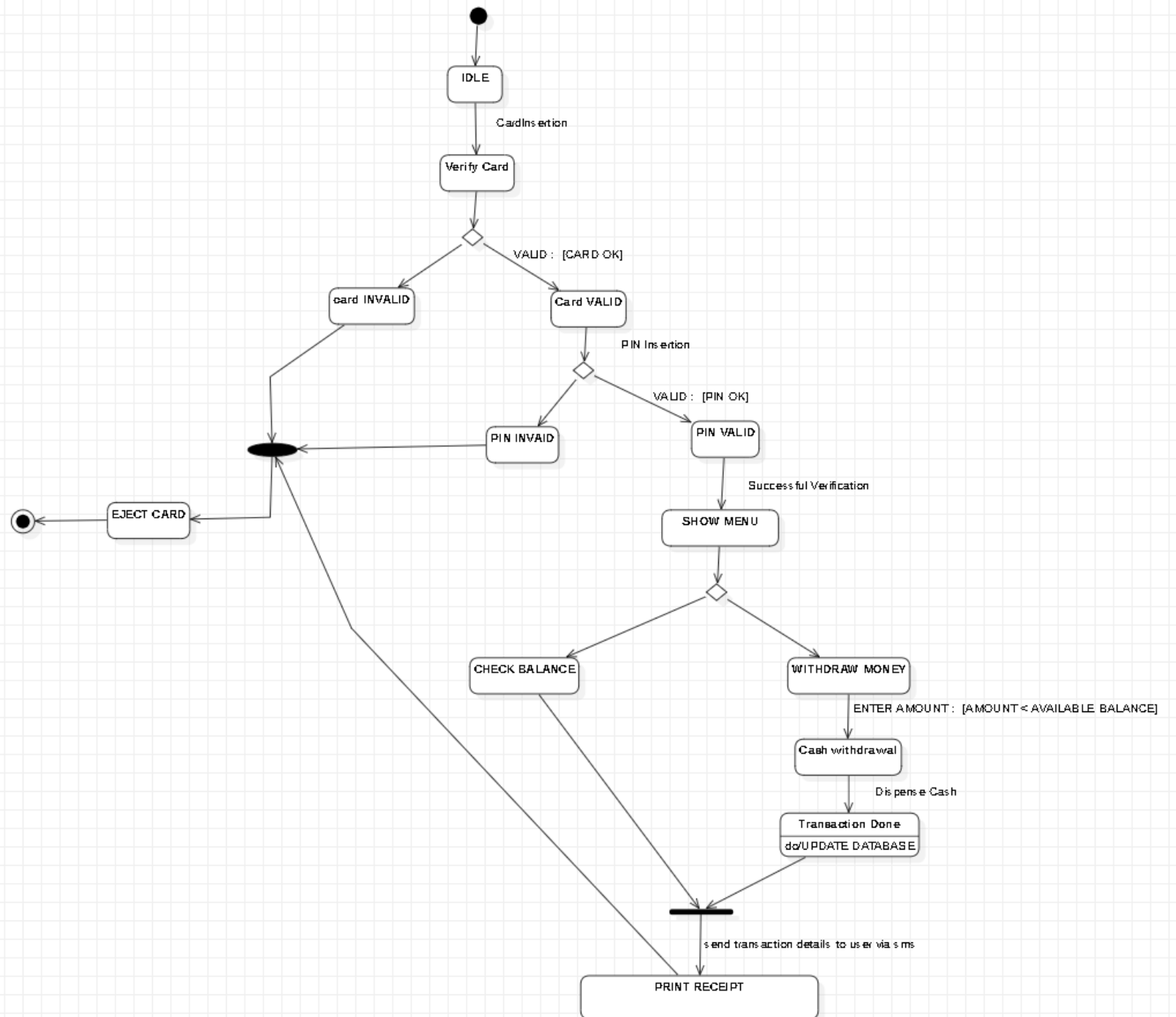
UPES

Assume suitable states, events, etc. to design State diagram for:  
“Printer”



Assume suitable states, events, etc. to design State diagram for:

“ATM Machine”



Assume suitable states, events, etc. to design State diagram for:

“Telephone”

