Name: Manthan M. Sonawane Panel: 17 E POIL NO: 17 Assignment -7 # Problem Statement: write X86 64 ALP to add an array of N hexadecimal. # Objectives: 1. Understand the concept of unpacked and packed HEX number and the need of packing the accepted number from user. 2. Repetitive addition. # Theory: · Explain unpacked and packed number with example Packed Numbers: Packed Numbers are stored in two digits to a byte, in 4 bit groups referred to as nibbles. ALU is capable of performing only binary addition and substraction.

Unpacked Numbers: In unpacked numbers, there is

only one digit per byte and because of this, unpacked

mutiplication and division can be done.

	why packing is required. 1) while accepting an array of numbers from user, all numbers are stored in unpacked form and need to be packed for further arithmetic operations.
0	2) Packing is required for displaying a number taken from user or displaying addition result of the initialized 2-digit hex numbers.
099()4	i) Macro instruction - a request to assemble program to process a predefined sequence of
HQ900	instructions called a macro defination. 'macro rw,4 mov rax, '/.1
	mov rdi, 1.2 mov rsi, 1.3 mov rdx, 1.4
nû	syscall -1. end macro
L.	ii) Array times 10 dbo. iii) mov top, array iv) Mov tsi, num
#	Algorithm Implementation: Attached print outs.

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-		
-	#	Platform
	Has	· Editor - gedit, a GNU editor.
	200	Assembles - NASM (Netwide Accombine)
		· Linker - LD, a GNU Linker
	3#	Inquit.
		Array elements
0	#	Output:
		fum of array elements
	11	201 1 1 1 1 1 1 1 2 - (45) bag near
ons	#	The concept of warding
	200	packed HEX numbers and ALP to add an array
		of N Hexadecimal numbers.
	#	FACY'S
- 13	n	ab cathor stag (ac) - 15 act then string do
	1.	Explain flag register of 8086 with neat diagram.
6	- 1	Jo J. Jean Will Dear Wagawn.
	35	15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
		UUU OF DF IF TF SF ZF U AF U PF U CF
		Overflow flag (Carry flag (
	7,000	Direction Hay Hoa
		Interrupt enable Hag - Auxillary Carry
1116		Trap flag
	200	Sign Flag
	TA P	of that art on archana took at it art bounds
3	4.4	Hi was Africa wines was something the delication
		as well-come took and all adjaces as the

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The flag register in 8086 microprocessor is a 16-bit register that stores various flags. We can divide flag bils into a sections:

1. Status Flags:

- · Carry flag (CF)-set if there is carry out of MSB of result
- · Parity Flag (PF)-set if no of set bits is even
- · Auxillary carry flag (AF) set if there is carry out of bit3.
- · zero Hag (ZF) set if result is zero
- · sign flag (SF) set is MSB of result is set.
- · Overflow flag (OF) set if result of signed operation is too large to fit in destination register

2. Control Flags:

- · Direction flag (DF) IF set, then string data is nemory location, if reset (o), then string data is accessed from lower memory location to higher memory location
- State the difference been ROL and SHL instructions. ROL (rotate left) and SHL (shift left) are both instruction that perform bit shirting on a binary number, but they do so in different ways. ROL shifts all the bits in a number to the left and wraps around the bits that overflow on the left to the right side, whereas OHI simply shirts all the bits to the left, discarding, the bit that overflow on the left.

3. Why 304 1374 is subtracted from the number? Explain with example of each. The subtraction of 30H or 37H from a number is typically done in order to convert the number from its nexa decimal representation to its ASCII representation. For example, if the hexadecimal number is 30H. represents the decimal number 48. The ASCII code for the digit "O" is 48. Therefore, by subtracting 304 from the hexadecimal number, we can convert if to the ASCII representation of the digit "O".

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(Sundaram)

section .data array db 11H, 12H, 13h, 14h, 15h msg db "Sum of array:", 10 msgLen equ \$-msg

section .bss sum resw 1 temp resw 2 temp1 resb 1

%macro rw 4 mov rax, %1 mov rdi, %2 mov rsi, %3 mov rdx, %4 syscall %endmacro

section .text global _start _start: mov rsi, array mov ax, 0h mov bx, 0h mov cx, 5

up2: mov bl, byte[rsi] add ax, bx jnc skip inc ah

skip: inc rsi dec cx jnz up2

mov word[sum], ax rw 1, 1, msg, msgLen;

call disp

rw 60,0,0,0;

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disp:
mov bp, 4
mov ax, word[sum]
```

up1: rol ax, 4 mov [temp], ax and ax, 0fh cmp al, 09 jbe down1 add al, 07

down1: add al, 30h mov [temp1], al rw 1, 1, temp1, 1;

mov ax, word[temp] dec bp jnz up1 ret

OUTPUT

