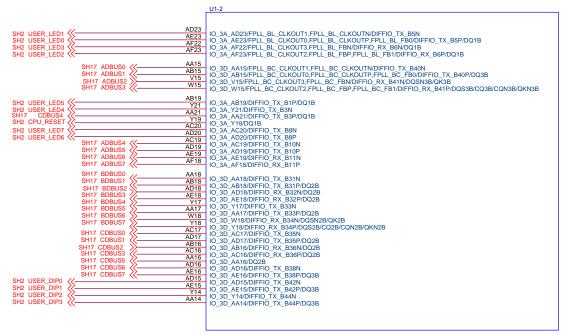
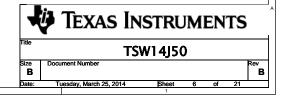


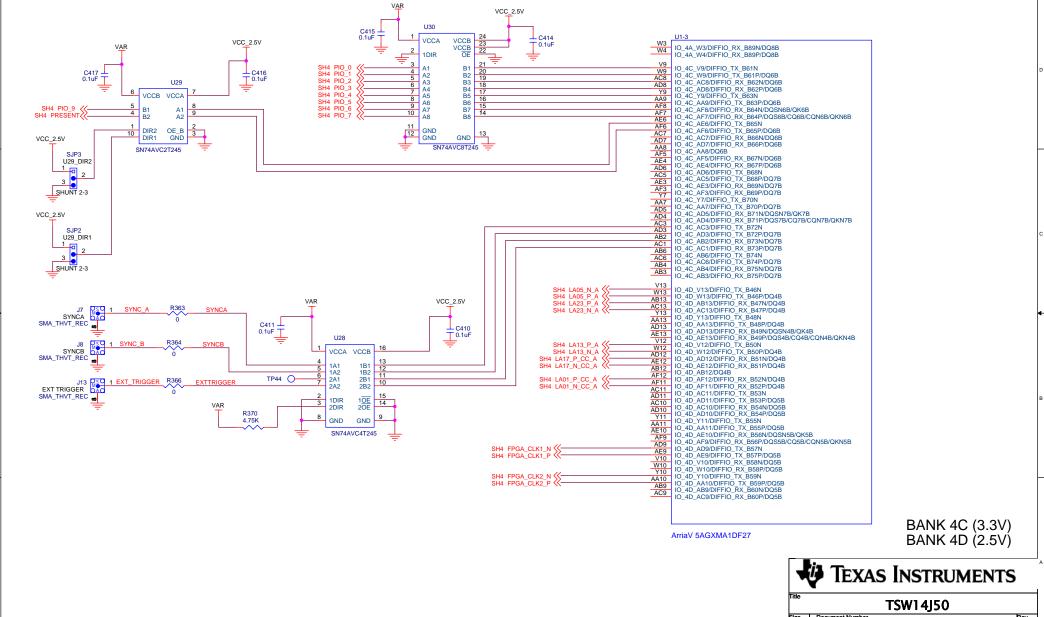
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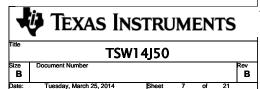


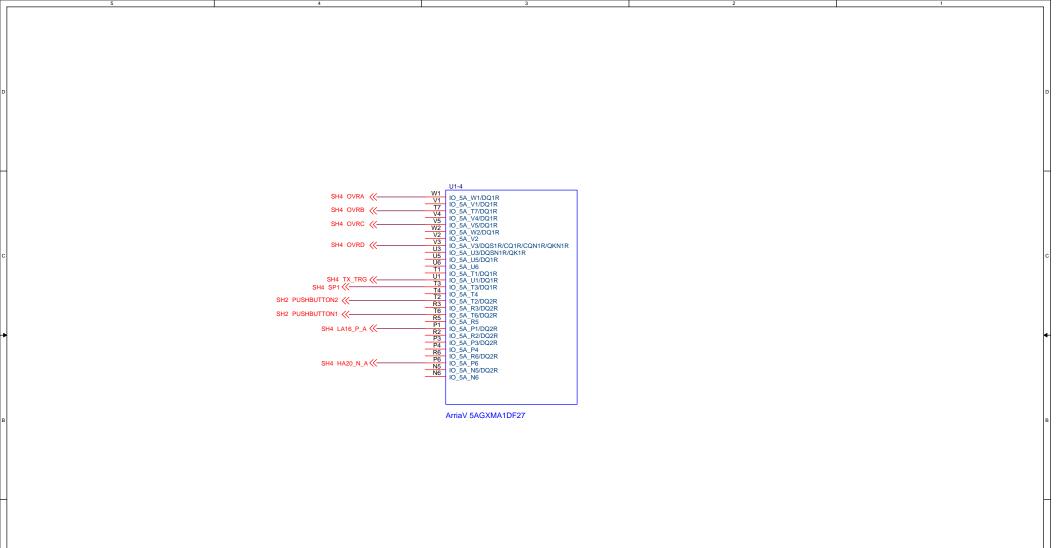
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BANK 3

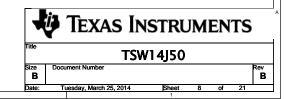


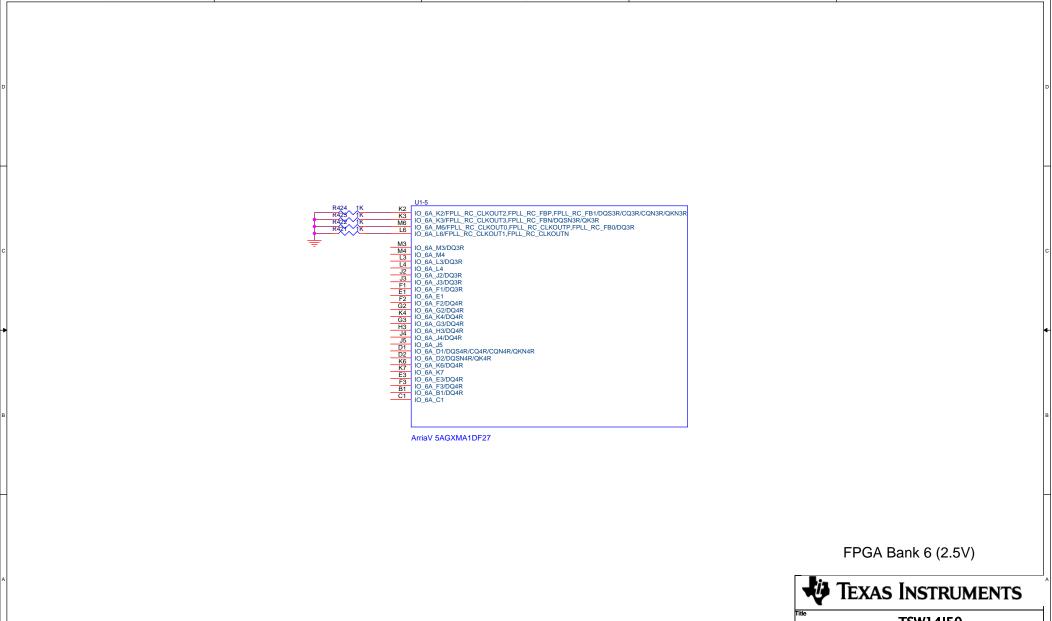


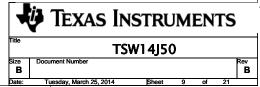




FPGA Bank 5A (VAR)





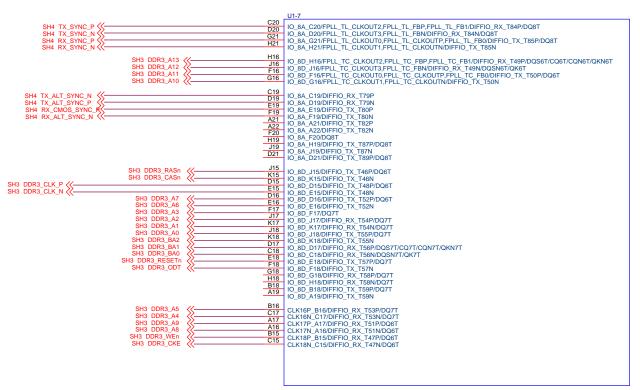


IO 7A F4/DIFFIO_RX_T1P/DQ1T G4 J6 IO_7A_G4/DIFFIO_RX_T1N/DQ1T IO_7A_J6/DQ1T SH3 DDR3 DQ31 <<-SH3 DDR3_DQ30 X SH3 DDR3_DQ29 X SH3 DDR3 DQ28 </ SH3 DDR3_DQ27 | IO_7C_F7/DIFFIO_RX_11/NVQQ21 | IO_7C_C5/DIFFIO_TX_T18P/DQ2T | IO_7C_D5/DIFFIO_TX_T18P/DQ2T SH3 DDR3 DQ26 SH3 DDR3_DQS_P3 \\-SH3 DDR3_DQS_N3 \\-SH3 DDR3_DM3 <<-SH3 DDR3_DQ25(SH3 DDR3_DQ24(SH3 DDR3_DQ23(SH3 DDR3_DQ22(SH3 DDR3_DQ21(SH3 DDR3_DQ20(SH3 DDR3_DQ18 SH3 DDR3_DQS_P2\(\right\)-SH3 DDR3_DQS_N2\(\right\)-SH3 DDR3_DM2((-SH3 DDR3_DQ17(\squares SH3 DDR3_DQ16(\squares C9 IO_7C_C9/DIFFIO_TX_T29N | B10 | O_TC_C9DIFFIO_TX_T28N |
B10	O_TC_C9DIFFIO_TX_T28N	
B10	O_TC_C9DIFFIO_TX_T39N	DO4T
C10	O_TD_C10DIFFIO_RX_T30N	DO4T
F11	O_TD_C10DIFFIO_RX_T30N	DO4T
G11	O_TD_C10DIFFIO_RX_T31PIDQ4T	
G11	O_TD_G110DIFFIO_RX_T32PIDQ4T	
C11	O_TD_G110DIFFIO_RX_T32PIDQ4T	
C11	O_TD_G110DIFFIO_RX_T32PIDQ4T	
C11	O_TD_G110DIFFIO_RX_T33PIDQ4T	
C12	O_TD_G110DIFFIO_TX_T38PIDQ4T	
C13	O_TD_G110DIFFIO_TX_T38PIDQ4T	
C14	O_TD_G110DIFFIO_TX_T38PIDQ5T	
C15	O_TD_G110DIFFIO_TX_T38PIDQ5T	
C16	O_TD_G110DIFFIO_TX_T38PIDQ5T	
C17	O_TD_G110DIFFIO_TX_T38PIDQ5T	
C18	O_TD_G110DIFFIO_TX_T38PIDQ5T	
C14	O_TD_G110DIFFIO_TX_T38PIDQ5T	
C14	O_TD_G110DIFFIO_TX_T38PIDQ5T	
C15	O_TD_G110DIFFIO_TX_T38PIDQ5T	
C14	O_TD_G110DIFFIO_TX_T40PIDQ5T	
C15	O_TD_G110DIFFIO_TX_T40PIDQ5T	
C16	O_TD_G110DIFFIO_TX_T41PIDQSNST/QKST/QKST	
C17	O_TD_G110DIFFIO_TX_T41PIDQSNST/QKST	
C18	O_TD_G110DIFFIO_TX_T41PIDQSNST/QKST	
C18	O_TD_G110DIFFIO_TX_T41PIDQST	
C18	O_TD_G110DIFFIO_TX_T41PIDQST	
C18	O_TD_G110DIFFIO_TX_T44PIDQST	
O_TD_G110DIF SH3 DDR3_DQ8(SH3 DDR3 DQ10 SH3 DDR3_DQ11X/-SH3 DDR3_DQ12X-SH3 DDR3_DQ13 SH3 DDR3_DQS_P1		
SH3 DDR3_DQS_N1 SH3 DDR3_DM1(< SH3 DDR3_DQ14X SH3 DDR3_DQ15X SH3 DDR3_DQ7(SH3 DDR3_DQ6 SH3 DDR3_DQ4 SH3 DDR3_DQS_P0X— SH3 DDR3_DQS_N0X— SH3 DDR3 DQQ J14 IO_7D_J13/DIFFIO_TX_T44P/DQ5T IO_7D_J14/DIFFIO_TX_T44N SH3 DDR3_CSr(<-

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DDRIII - FPGA Bank 7C & 7D (1.5V)

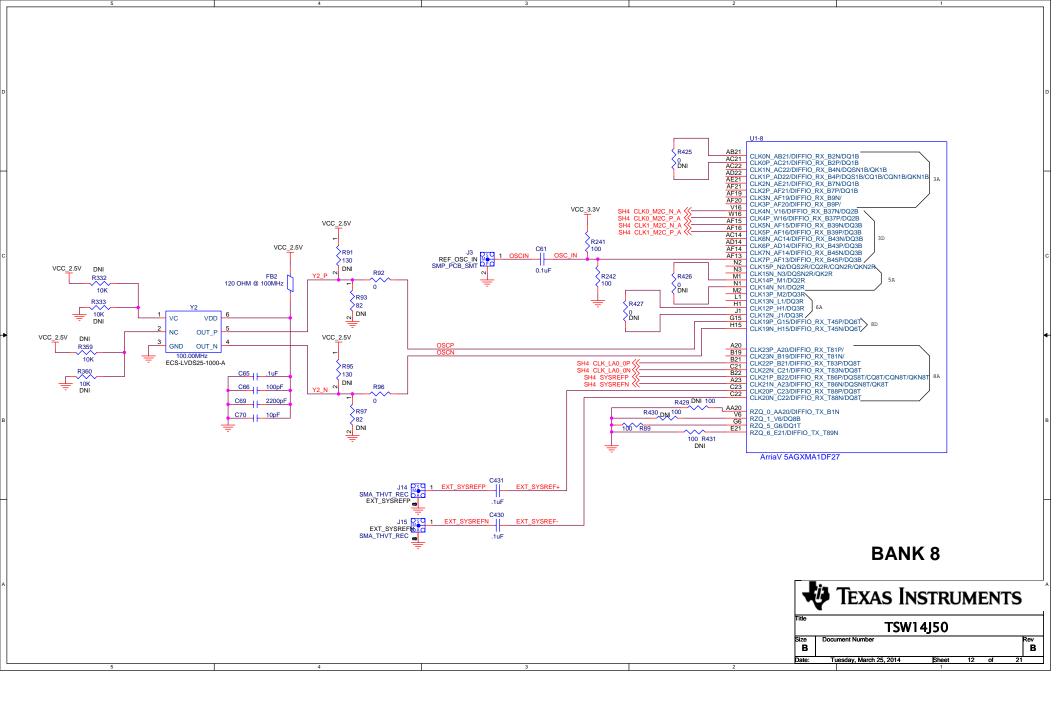
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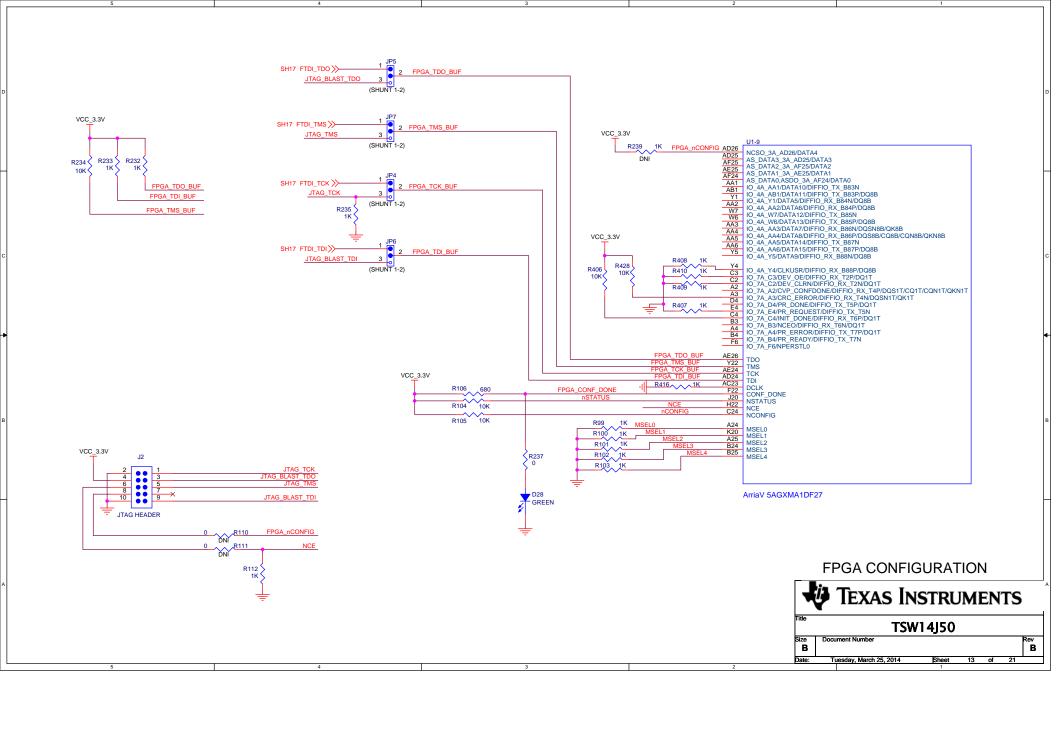


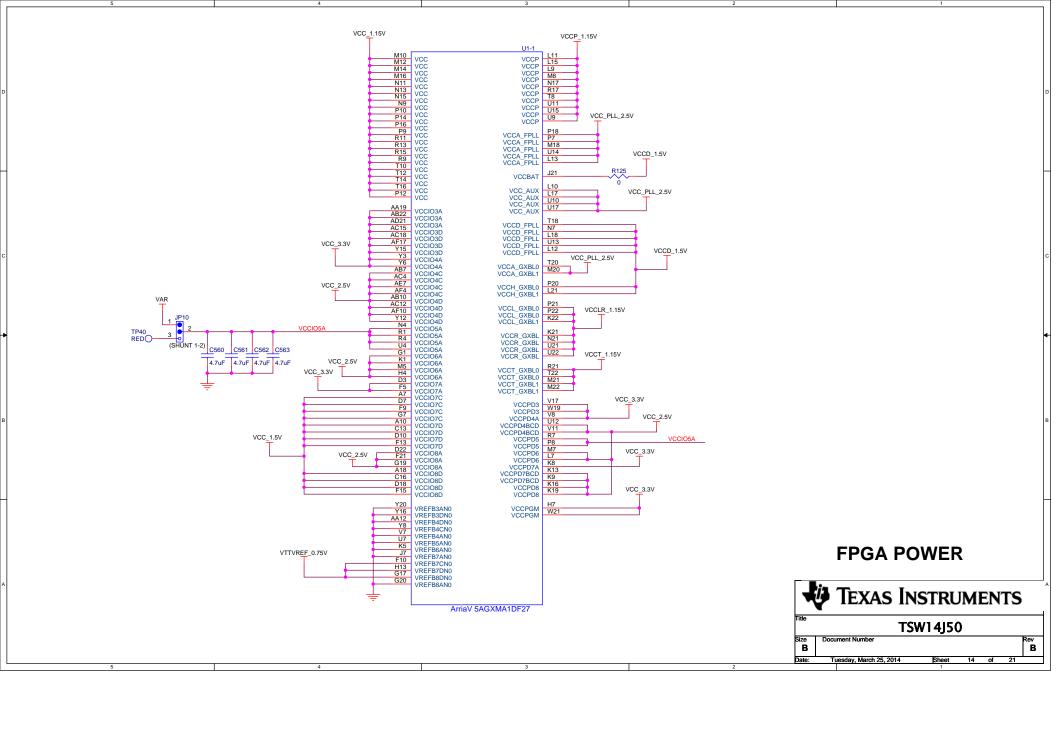
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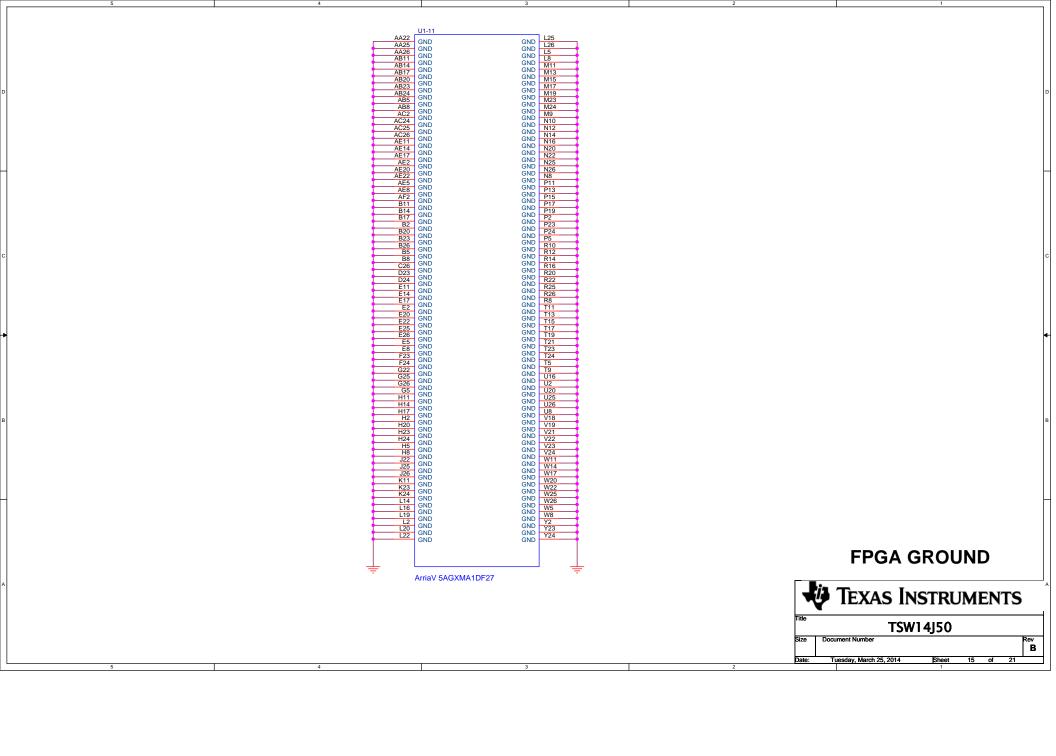
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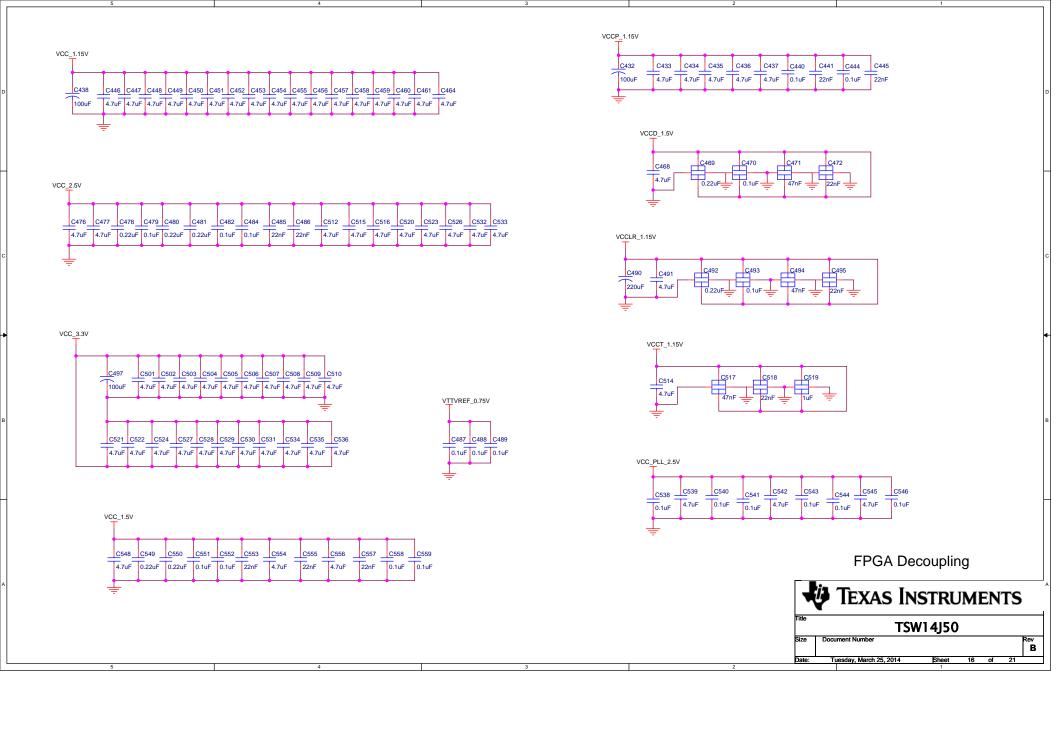
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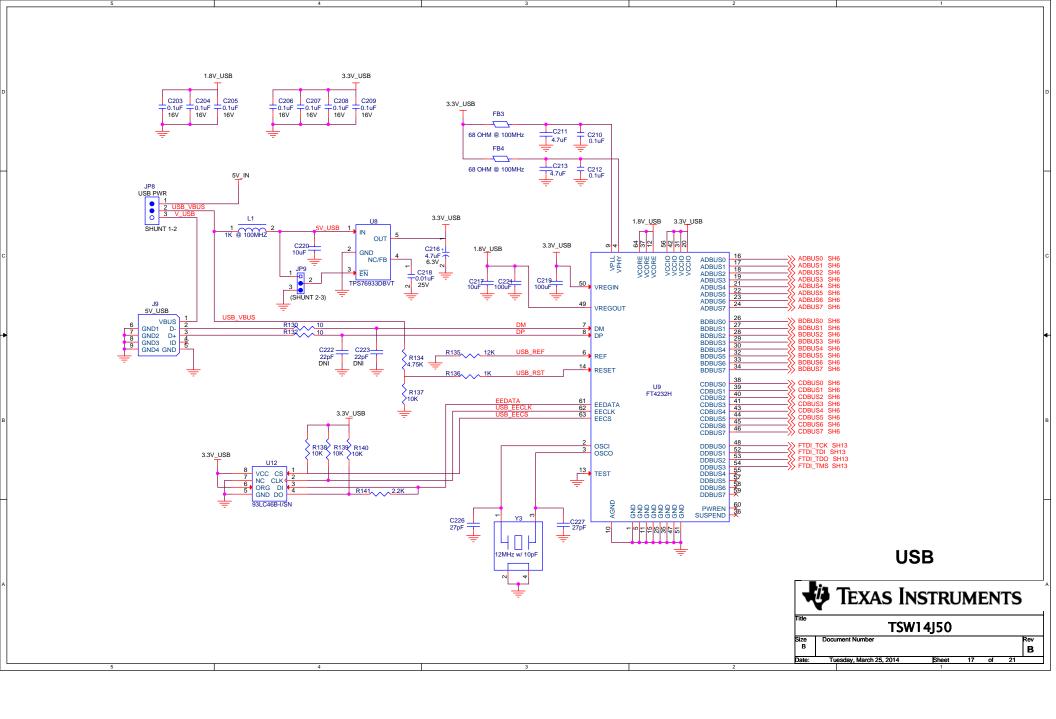


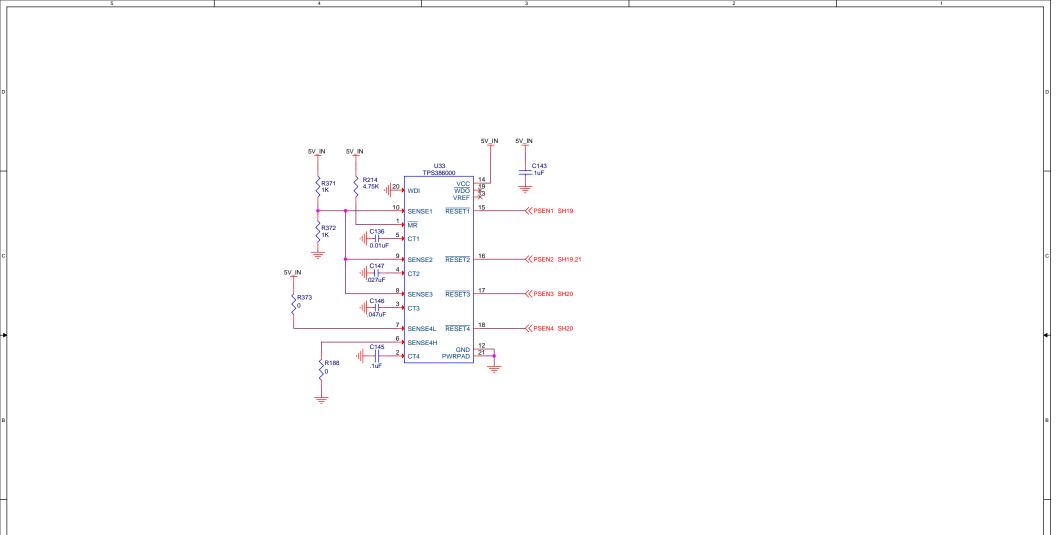












SEQUENCER

