

mysub - MPLAB IDE v8.92

File Edit View Project Debugger Programmer Tools Configure Window Help

Debug

Checksum: 0xc8c6

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mysub.mcw

mysub.mcp

Source Files

mysub.s

Header Files

Object Files

Library Files

Linker Script

Other Files

Files

Symbols

File Registers

Address	00	02	04	06	08	0A	0C	0E	ASCII
07B0	----	----	----	----	----	----	----	----	-----
07C0	----	----	----	----	----	----	----	----	-----
07D0	----	----	----	----	----	----	----	----	-----
07E0	----	----	----	----	----	----	----	----	-----
07F0	----	----	----	----	----	----	----	----	-----
0800	1FBC	BEEF	0034	0038	0006	003A	00FE	0000	....4.8. ...
0810	5652	0000	0000	0000	0000	0000	0000	0000	RV.....
0820	68D2	0000	0000	0000	0000	0000	0000	0000	.h.....
0830	0064	0000	0000	0000	0016	0000	0000	0000	d.....
0840	0000	0000	0000	0000	0000	0000	0000	0000	.....
0850	0000	0000	0000	0000	0000	0000	0000	0000	.....
0860	0000	0000	0000	0000	0000	0000	0000	0000	.....
0870	0000	0000	0000	0000	0000	0000	0000	0000	.....
0880	0000	0000	0000	0000	0000	0000	0000	0000	.....
0890	0000	0000	0000	0000	0000	0000	0000	0000	.....
08A0	0000	0000	0000	0000	0000	0000	0000	0000	.....
08B0	0000	0000	0000	0000	0000	0000	0000	0000	.....

Hex Symbolic Dual Port

Program Memory

	Line	Address	Opcode	Disassembly
	268	00216	884022	mov.w 0x0004,0x0804
	269	00218	200383	mov.w #0x38,0x0006
	270	0021A	FB8183	ze 0x0006,0x0006
	271	0021C	884033	mov.w 0x0006,0x0806
	272	0021E	200064	mov.w #0x6,0x0008
	273	00220	FB8204	ze 0x0008,0x0008
	274	00222	884044	mov.w 0x0008,0x0808
	275	00224	414104	add.b 0x0004,0x0008,0x0004
	276	00226	FB8102	ze 0x0004,0x0004
	277	00228	884052	mov.w 0x0004,0x080a
	278	0022A	51C182	sub.b 0x0006,0x0004,0x0006
	279	0022C	FB8183	ze 0x0006,0x0006
	280	0022E	884063	mov.w 0x0006,0x080c
	281	00230	50C083	sub.b 0x0002,0x0006,0x0002
	282	00232	500001	sub.w 0x0000,0x0002,0x0000
	283	00234	884000	mov.w 0x0000,0x0800
	284	00236	040236	goto 0x000236
	285	00238	000000	nop

Output

Build Version Control Find in Files MPLAB SIM

Total program memory used (bytes): 0x63 (99) <1%

Data Memory [Origin = 0x800, Length = 0x2000]

section	address	alignment	gaps	total length (dec)
.bss	0x800		0	0xe (14)

Total data memory used (bytes): 0xe (14) <1%

Dynamic Memory Usage

region	address	maximum length (dec)
heap	0	0 (0)
stack	0x80e	0x1ff2 (8178)

Maximum dynamic memory (bytes): 0x1ff2 (8178)

Executing: "C:\Program Files (x86)\Microchip\MPLAB ASM30 Suite\bin\pic30-bin2hex.exe" "C:\temp\michael\chapters\mysub.cof"

Loaded C:\temp\michael\chapters\mysub.cof.

Debug build of project 'C:\temp\michael\chapters\mysub.mcp' succeeded.

Language tool versions: pic30-as.exe v3.31, pic30-ld.exe v3.31, pic30-ar.exe v3.31

Preprocessor symbol '\_\_DEBUG' is defined.

Sun Sep 25 12:20:12 2022

BUILD SUCCEEDED

C:\temp\michael\chapters\mysub.s

add.b w2, w4, w2 ; w2 = w2 (i) + w4 (k)  
ze w2, w2  
mov w2, 1 ; 1 = w2  
  
; m = j - 1;  
sub.b w3, w2, w3 ; w3 = w3 (j) - w2 (1)  
ze w3, w3  
mov w3, m ; m = w3  
  
; xx = xx - yy - m  
; yy = m  
sub.b w1, w3, w1  
; xx = yy - m  
sub w0, w1, w0  
mov w0, xx

Add SFR AD1CHS0 Add Symbol \_\_SP

Update	Address	Symbol Name	Value
	0000	WREG0	0x1FBC
	0002	WREG1	0xBEF1
	0004	WREG2	0x003A
	0006	WREG3	0x00FE
	0008	WREG4	0x0006
	0804	i	0x0034
	0806	j	0x0038
	0808	k	0x0006
	080A	l	0x003A
	080C	m	0x00FE
	0800	xx	0x1FBC
	0802	yy	0xBEEF

Watch 1 Watch 2 Watch 3 Watch 4

Special Function Registers

Address	SFR Name	Hex
0018	WREG12	x0000
001A	WREG13	x0000
001C	WREG14	x0000
001E	WREG15	x080E
0020	SPLIM	x27F0
002E	PC	00236
002E	PCL	x0236
0030	PCH	x0000
0032	TBLPAG	x0000
0034	PSVPAG	x0000
0036	RCOUNT	x0000
0042	SR	x0101
0044	CORCON	x0000
0052	DISICNT	x0000
0060	CNEN1	x0000
0062	CNEN2	x0000
0068	CNPU1	x0000
006A	CNPU2	x0000
0080	INTCON1	x0000
0082	INTCON2	x0000
0084	IFS0	x0000
0086	IFS1	x0000
0088	IFS2	x0000
008A	IFS3	x0000
008C	IFS4	x0000
0094	IEC0	x0000
0096	IEC1	x0000
0098	IEC2	x0000
009A	IEC3	x0000
009C	IEC4	x0000

name - Notepad

File Edit View

Michael Orscheln

CWID: 12065652

Ln 1, Col 1 100% Windows (CRLF)

MPLAB SIM PIC24HJ128GP502 pc:0x236 oab sab IPO DC n ov z C 120 MHz Ln 104, Col 1 INS WR

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