Logic Design Lab Report: Week 2

2019-12345 Gildong Hong

1. Introduction

``` In this part, you write the introduction about what you do in the lab simply.

e.g.) In this lab, we implement the AND, OR, and NOT logic circuit only using NAND, NOR chips to understand functional completeness. ```

2. Implementation

``` In this part, you write the basic information or idea. If you attach charts, tables, or pictures, TA's are very happy. :-) e.g.) a. Using NAND, NOT logic is made using 1 NAND chips since there are 4 NAND gates in the chips. ~~~```

3. Result

``` You write the result in this part. if necessary, Attach and explain the pictures.

DON'T MANIPULATE YOUR CODE! We will give you F if you do it. e.g.) The result is.... ```

4. Conclusion/Discussion

``` You write what you learn in this lab shortly, just 2~3 sentences. e.g.) We learn the functional completeness during this lab. We can design basic logic circuit only using NAND and NOR. ```

● You can use Korean or English.

● Writing the report does not mean that TAs like irritating you. It helps TAs understand your homework and lab, and get you the better score. :-)