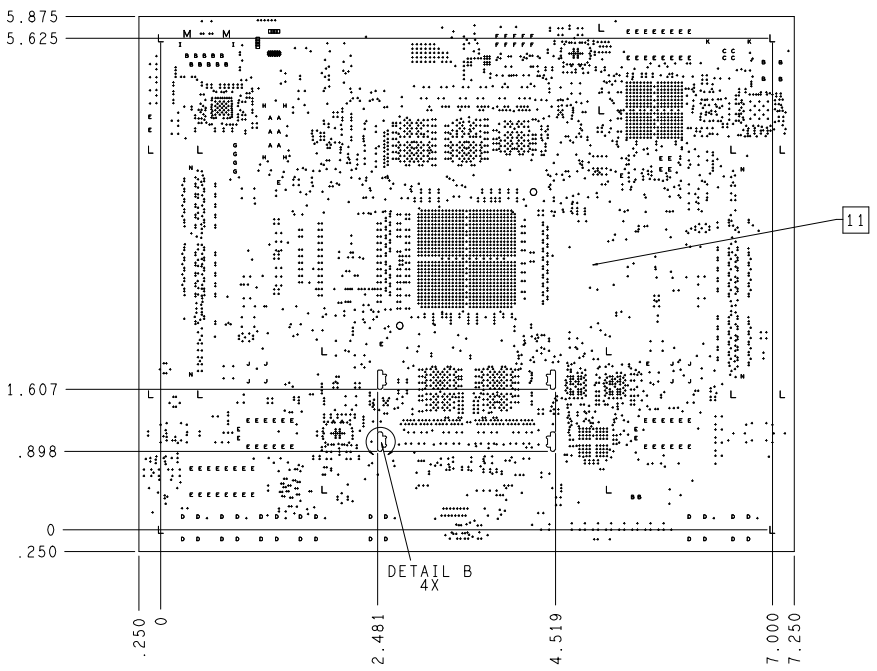
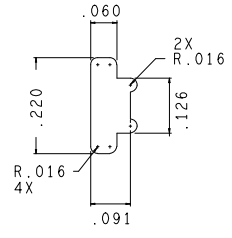
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

NOTES UNLESS OTHERWISE SPECIFIED:

1. **PCB VENDOR MUST NOTIFY RESPONSIBLE ALTERA PCB CONTACT OF ANY DISCREPANCIES FOUND BETWEEN FABRICATION DATA AND FABRICATION DRAWING NOTES.**
2. THIS DRAWING IS VIEWED FROM THE PRIMARY OR TOP SIDE OF THE PCB.
3. FABRICATION OF THIS PCB SHALL BE IN CONFORMANCE WITH THE FOLLOWING SPECIFICATIONS: IPC-6011 CLASS 2 (GENERIC).
4. ALL MATERIALS USED MUST BE RoHS COMPLIANT.
5. FABRICATION OF THIS PCB TO BE ACCEPTABLE TO IPC-A-600 CLASS 2 (LATEST REVISION).
6. ALL DIMENSIONAL LIMITS APPLY AFTER PLATING OR PROCESSING.
7. TOLERANCES OF DATUM HOLE TO: BOARD EDGE LOCATIONS +/- 0.010.
   DRILLED HOLE LOCATIONS +/- 0.003.
   V-SCORE LOCATIONS +/- 0.010
8. BASE MATERIAL: FR4 GLASS EPOXY (ISOLA 370HR), MIN. Tg OF 170 DEGREES C.
9. FLAME CLASS: UL 94V-0 & MUST MEET REQUIREMENTS OF UL796.
10. MANUFACTURER MUST BE UL RECOGNIZED TO PRODUCE THIS PRODUCT SUCH THAT IT MEETS 130 DEGREES CELSIUS MAXIMUM OPERATING TEMPERATURE (MOT).
11. THE FOLLOWING MUST BE MARKED OR ETCHED ON SECONDARY SIDE OF PCB IN AREA SHOWN:
    a) DATE CODE.
    b) UL RECOGNIZED VENDOR ID, UL TYPE DESIGNATION AND/OR MARKINGS WHICH REFLECT THE SPECIFIED FLAME CLASS AND MAXIMUM OPERATING TEMPERATURE RATINGS.
12. PCB VENDOR TO ENSURE ALL UNCONNECTED (NON-FUNCTIONAL) INTERNAL SIGNAL LAYER PADS AND VIAS ARE REMOVED.
13. HOLE/SLOT PLATING = 0.001 MIN. AVERAGE / 0.0008 ABSOLUTE MIN. PLATING. HOLE/SLOT DIAMETERS ARE SPECIFIED AFTER PLATING (SEE HOLE SCHEDULE).
14. A. SOLDERMASK VIA PLUG AND CAP/TENT FROM TOP SIDE.
    B. FINISHED SLOT TO BE .040 X .140.
    C. FINISHED SLOT TO BE .030 X .120 2X.
15. AFTER REVIEWING FABRICATION DATA, PCB VENDOR MUST DISCUSS WHETHER COPPER THIEVING IS NECESSARY WITH THE RESPONSIBLE ALTERA PCB CONTACT. WHEN DETERMINED NECESSARY. A SPACING OF 100 MILS FROM ANY OTHER COPPER FEATURE ON THE BOARD MUST BE MAINTAINED.
16. SMEAR REMOVAL SHALL NOT ETCHBACK GREATER THAN 0.001.
17. FINISHED CONDUCTOR WIDTH NOT TO BE REDUCED MORE THAN 20% OF MINIMUM WIDTH FROM ARTWORK SUPPLIED. FINISHED CONDUCTOR SPACING NOT TO BE REDUCED MORE THAN 20% OF MINIMUM SPACING FROM ARTWORK SUPPLIED.
18. INTERNAL ANNULAR RING 0.001 MINIMUM. EXTERNAL ANNULAR RING 0.002 MINIMUM. BOTH ARE MEASURED AT LINE TO PAD ENTRY. TEAR DROPPING OF TRACE TO PAD JUNCTION IS PERMITTED PROVIDED MINIMUM METAL-TO-METAL ARTWORK SPACINGS ARE NOT COMPROMISED.
19. SOLDER MASK IS LIQUID PHOTO IMAGEABLE AND IN ACCORDANCE WITH IPC-SM-840C CLASS T. FINISH MUST BE GREEN AND GLOSSY. REGISTRATION TO BE WITHIN +/- 0.003 OF ITS RESPECTIVE OUTER CIRCUIT LAYERS. VENDOR MAY ADJUST SOLDERMASK WHEREVER SOLDERMASK PADS ARE THE SAME SIZE (1:1) TO PROVIDE UP TO .003 MAXIMUM CLEARANCE FROM MASK TO PAD PROVIDED NO ADJACENT COPPER IS EXPOSED AND NO CONFLICT IS PRODUCED WITH ANY STATED "VIA TENTING/PLUGGING" REQUIREMENTS.
20. PCB VENDOR TO ENSURE ALL VIAS ARE COMPLETELY FREE OF SOLDERMASK ON BOTTOM SIDE OF PCB.
21. FINISH BOTH SIDES WITH 150 MICRO INCHES MINIMUM ELECTROLESS NICKEL (Ni) FOLLOWED BY 3-8 MICRO INCHES IMMERSION GOLD (Au).
22. REMOVE ALL SHARP EDGES AND BURRS 0.003" MAXIMUM.
23. SILKSCREEN USING WHITE NONCONDUCTIVE INK. NO INK TO APPEAR ON EXPOSED COPPER SUCH AS PLATED THROUGH HOLE PADS AND SURFACE MOUNT LANDS. INK ON SOLDER MASK COVERED PADS IS PERMISSIBLE. CLIPPING OF SILKSCREEN 0.008 MAX. FROM PADS IS PERMITTED.
24. BOW & TWIST SHALL BE DETERMINED BY PHYSICAL MEASUREMENT AND PERCENTAGE CALCULATION IN ACCORDANCE WITH IPC-TM-650, METHOD 2.4.22. BOW AND TWIST MAY NOT EXCEED 0.7%
25. 100% CONTINUITY AND ISOLATION ELECTRICAL TESTING PER CURRENT IPC TEST METHODS REQUIRED FOR EVERY PCB. FINAL PCB TEST DATA MUST BE CROSS-REFERENCED TO IPC-D-356 FILE, NEUTRAL FILE OR NETLIST PROVIDED.
26. CONTROLLED IMPEDANCE TRACES ARE AS FOLLOWS:
    a) TOLERANCE ON ALL LINES, UNLESS OTHERWISE SPECIFIED +/- 10%.
    b) SINGLE ENDED TRACES (.00425 & .0045) LYR 1 THRU 12 TO BE 50 OHMS.
    c) EDGE COUPLED TRACES (.004 -.019 CENTERS), LYR 1 & 12 TO BE 100 OHMS.
    d) EDGE COUPLED TRACES (.004/.0012 CENTERS), LYR 3 & 10 TO BE 100 OHMS.
    e) EDGE COUPLED TRACES (.004/.0014 CENTERS), LYR 6 & 7 TO BE 100 OHMS.
27. PCB VENDOR TO PROVIDE ONE TEST COUPON AND ONE CROSS SECTION PER LOT WITH SHIPMENT.
28. PCB VENDOR TO PROVIDE 2 SOLDER SAMPLES WITH FIRST SHIPMENT.
29. DETAILS NOT SPECIFIED ARE AT MANUFACTURER'S OPTION, HOWEVER FINAL APPROVAL MUST BE OBTAINED.

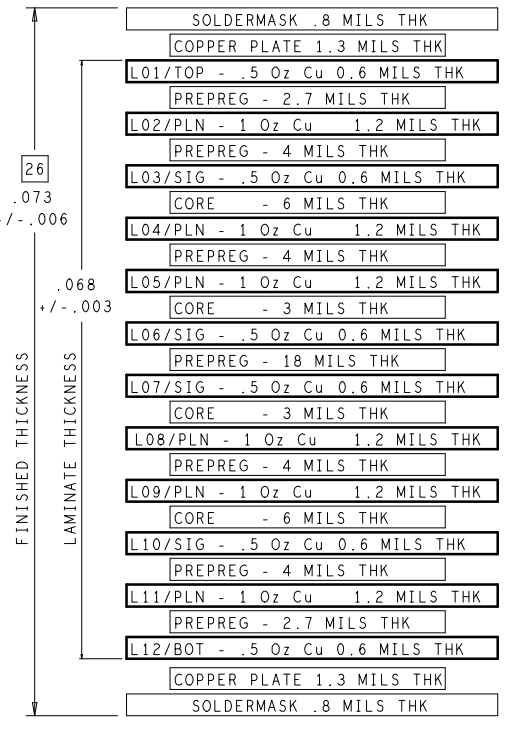


PRIMARY SIDE SHOWN



DETAIL B
SCALE: NONE

DRILL CHART: TOP to BOTTOM			
ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY
·	10.0	PLATED	4918
*	20.0	PLATED	14
A	28.0	PLATED	6
B	30.0	PLATED	8
#	35.0	PLATED	16
C	36.0	PLATED	4
D	39.0	PLATED	28
E	40.0	PLATED	70
F	43.0	PLATED	10
G	45.0	PLATED	4
H	60.0	PLATED	4
I	62.0	PLATED	2
J	72.0	PLATED	8
K	94.0	PLATED	2
L	125.0	PLATED	18
M	128.0	PLATED	2
N	40.0	NON-PLATED	4
O	138.0	NON-PLATED	2



DETAIL A
SCALE: NONE
(CROSS SECTION)

			MATL	8
			SPEC	3
.	XX-XXX-XXXX			
DASH	NEXT ASSY	USED ON	FNSH	19 21 23
APPLICATION				

THIRD ANGLE PROJECTION

UNLESS OTHERWISE SPECIFIED:
DIMENSIONS ARE IN INCHES IN ACCORDANCE WITH ANSI Y14.5.
DRILL TOLERANCES ARE PER 10387

TOLERANCES:
DEC .X ± ——
DEC .XX ± .01
DEC .XXX ± .005
FRACTIONS ± ——
ANGLES ± 1°

DWN P. Serino 25-Jul-07
CHKR
DSGN ENGR
RELEASE
CONTRACT NO. XXXXXXXX

TITLE
PCB FABRICATION
CYCLONE III
F780 HOST BOARD

SIZE	CAGE CODE	DWG NO.	REV
D	N/A	140-0310703-E1	E

SCALE: 1/1 SHEET 1 OF 1

REV E DWG NO. 140-0310703-E1