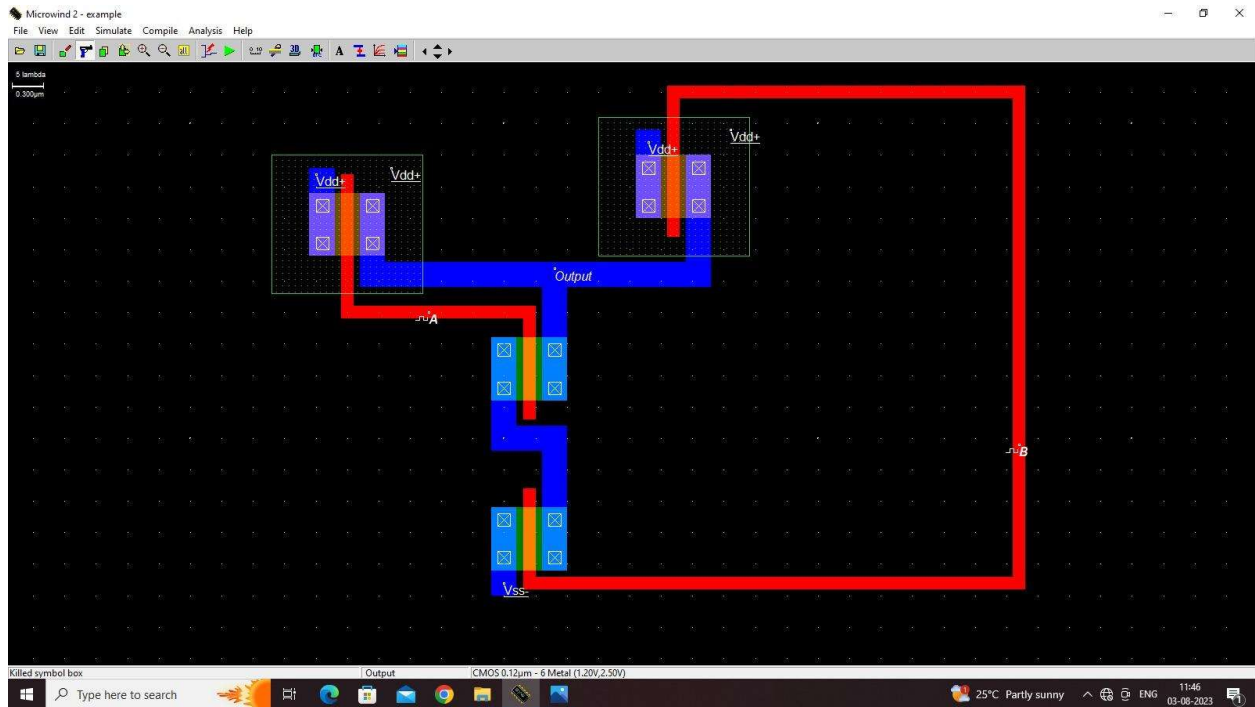


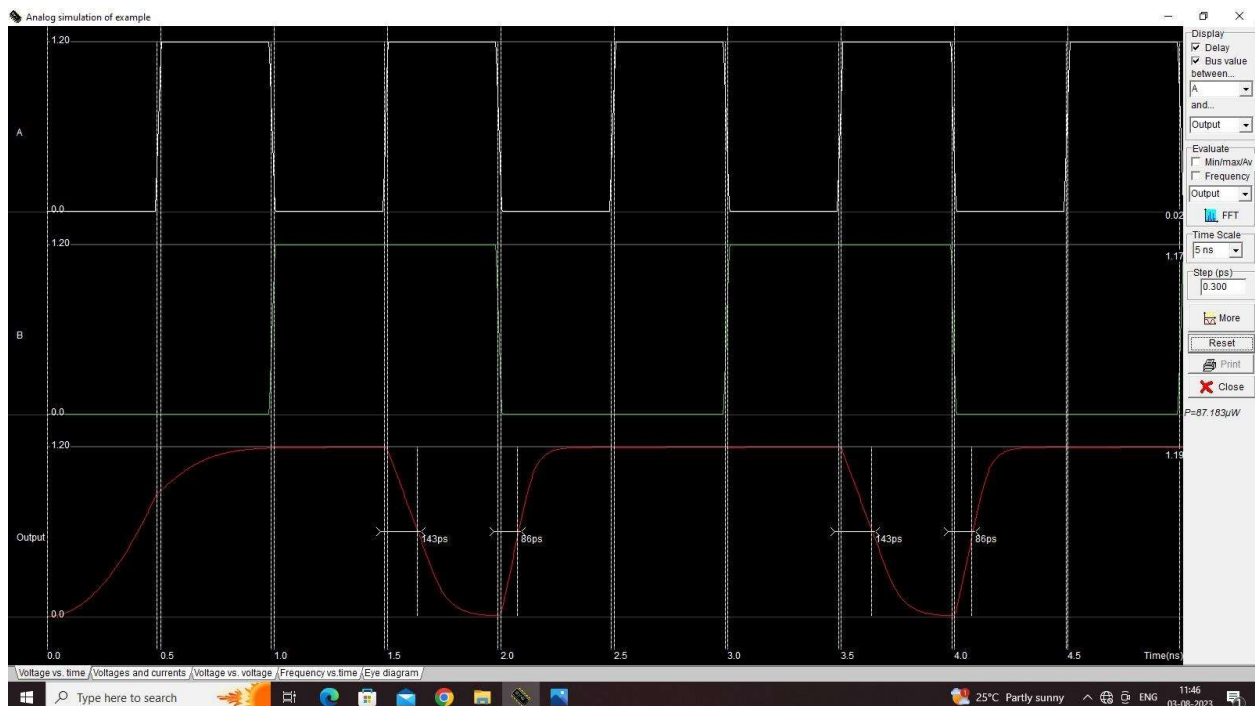
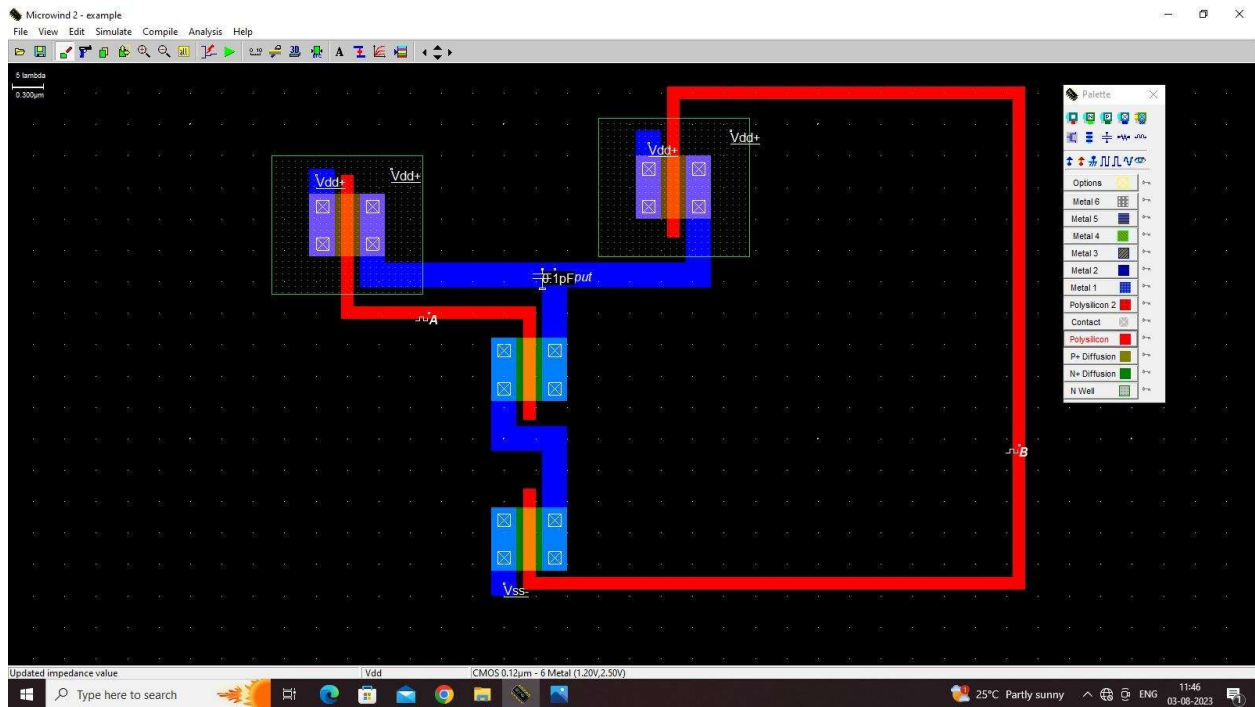
Experiment No :2b

CMOS NAND GATE WITHOUT USING CAPACITOR



NAND GATE

0.1 pF



NOR GATE

