6.823 Computer System Architecture
RISC ISA – 6.823 MIPS

Last Updated:
2/10/2006 4:44 PM

Please check out either Section 2.12 or [Appendix C (online)](#) of Hennessy and Patterson book. Throughout the course, all general-purpose registers (GPRs) are assumed to be 32 bits unless stated otherwise. (R0 is hardwired to zero.)