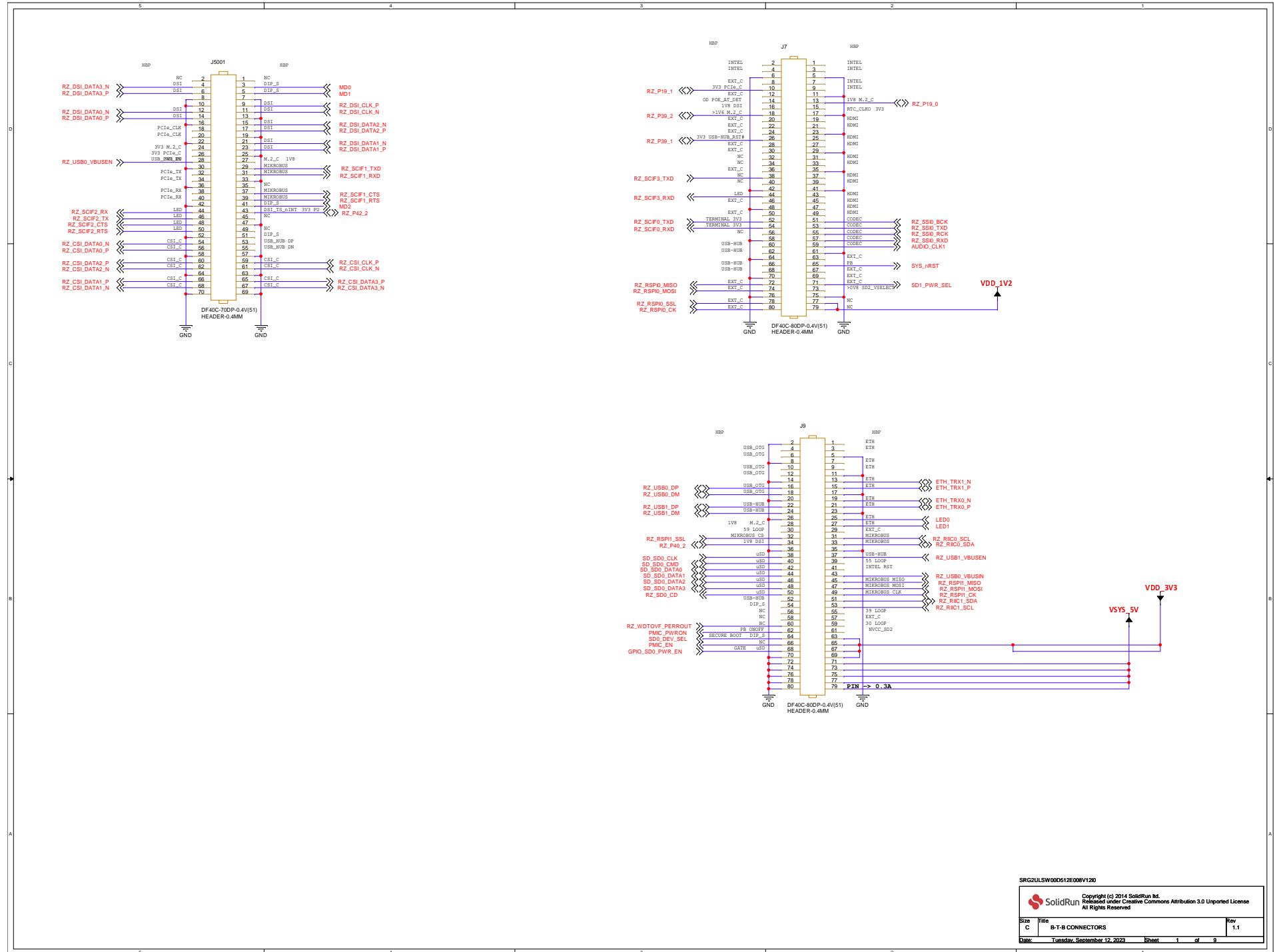


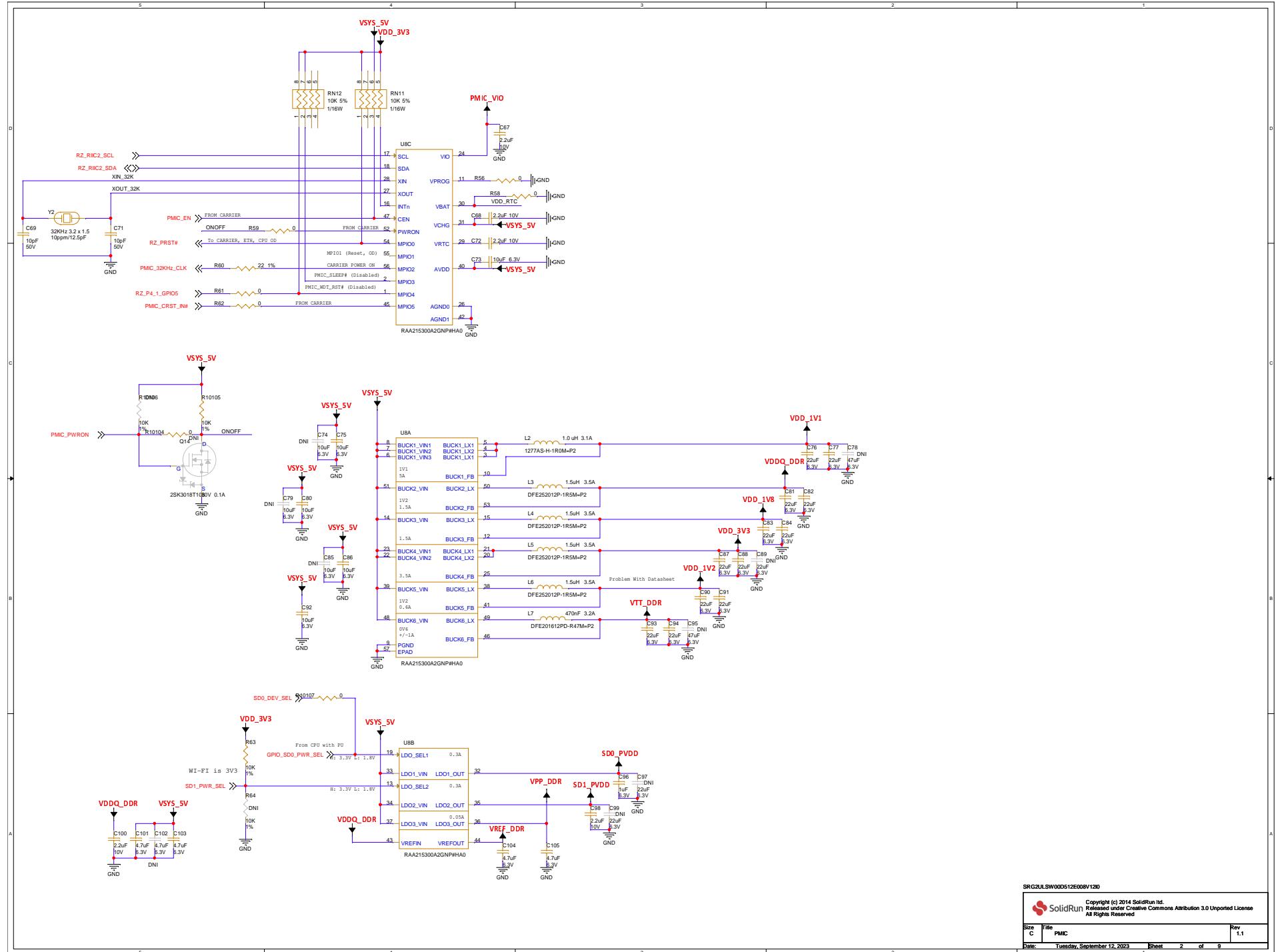
Rev. 1.0	Prototype Version
Rev. 1.1	<ol style="list-style-type: none">1. Change functionality of PWRON2. Removing Pull up from PMIC_32KHz_CLK
Rev. 1.1.1	<ol style="list-style-type: none">3. ON/OFF is active high
Rev. 1.2	<ol style="list-style-type: none">1. Moving RZ_P39_2 from J7-28 to J7-18 to support USB_PWR_EN on the HBP2. Connecting R24 to PU to support MODE43. Adding R10107 to support PMIC power control ove SD0 on BOOT



SRG2ULSW000512E008V120

Size	Title	Rev
C	B-T CONNECTORS	1.1

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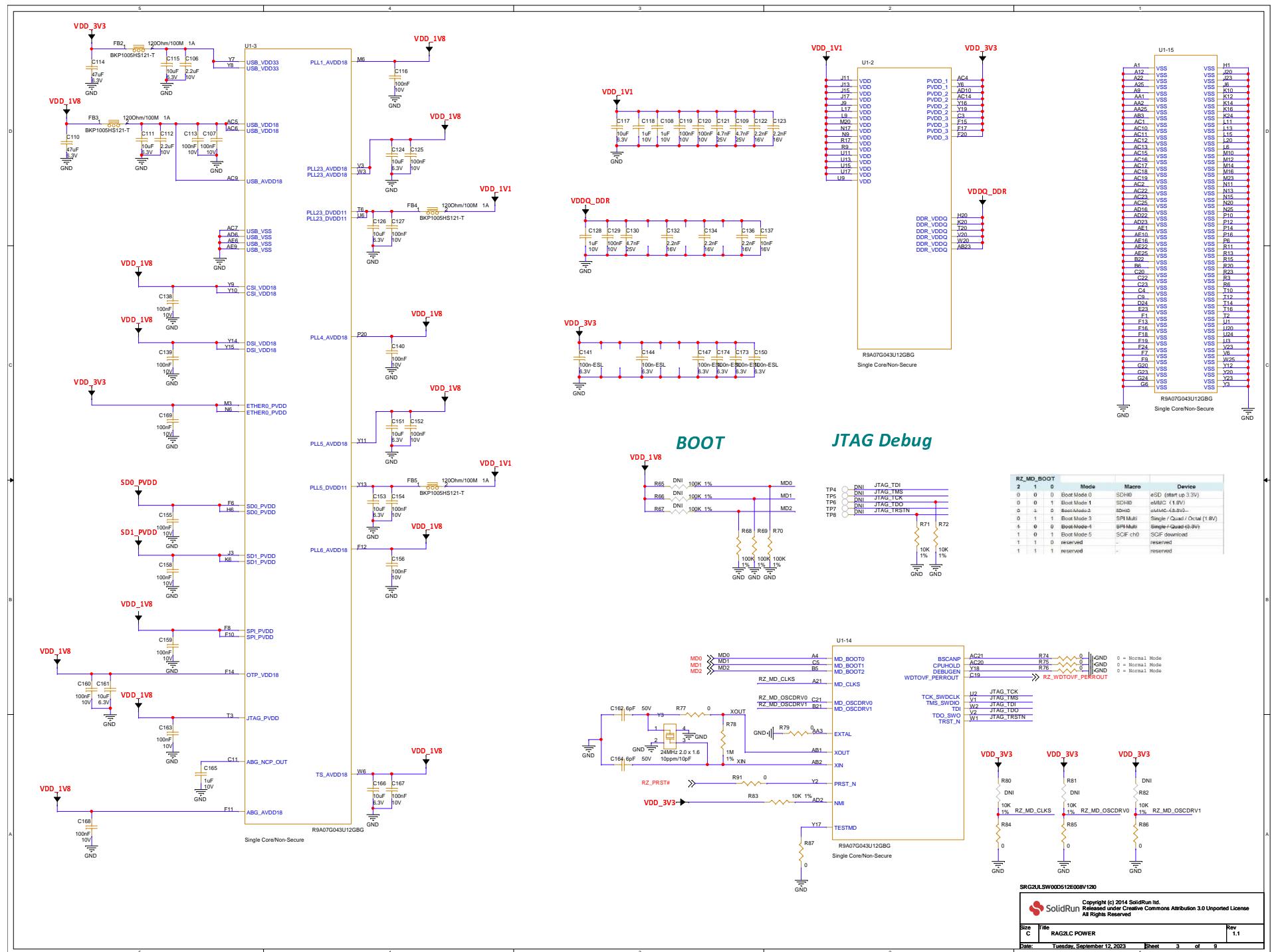


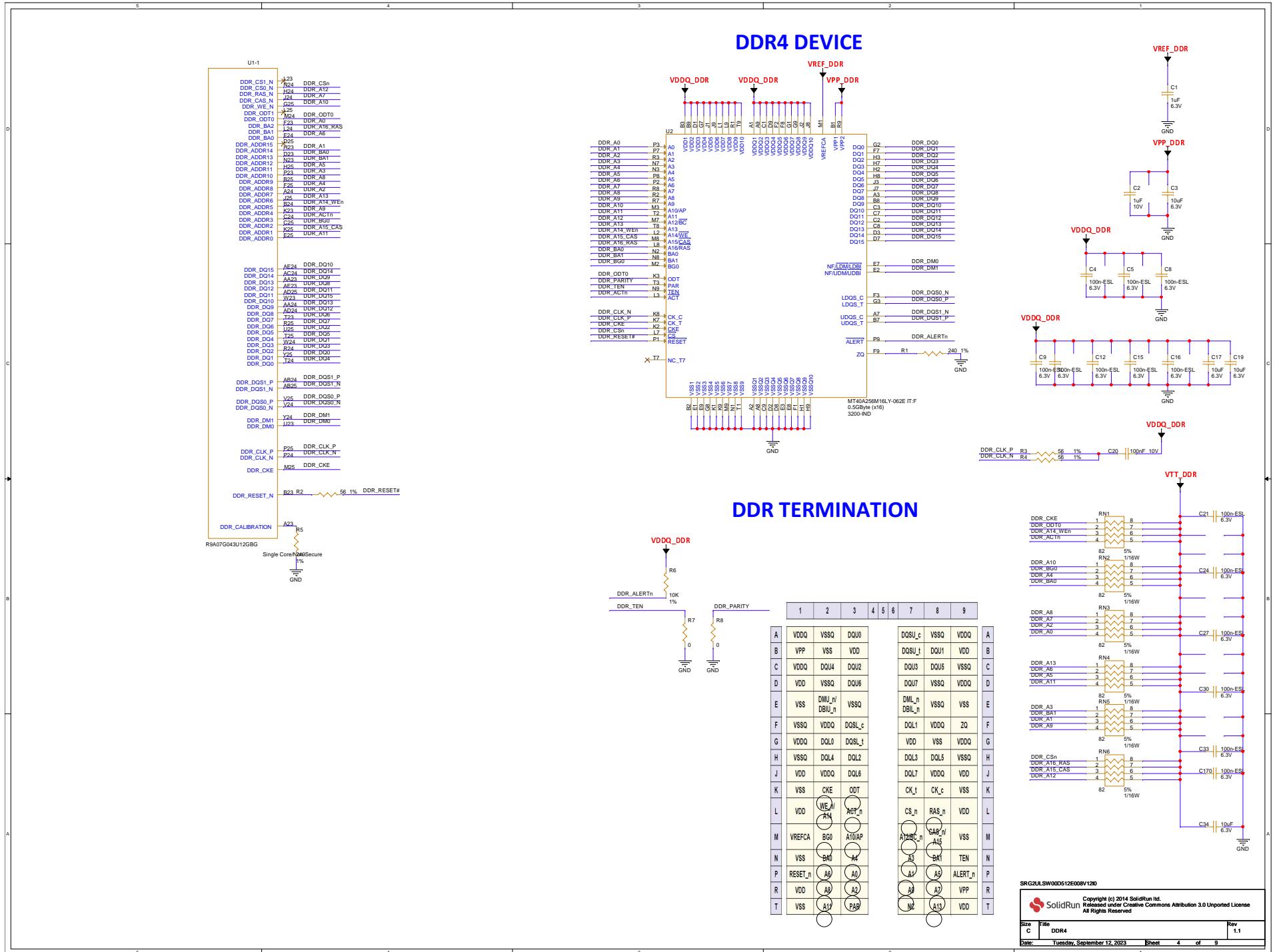
SRG2ULSW00D512E008V120

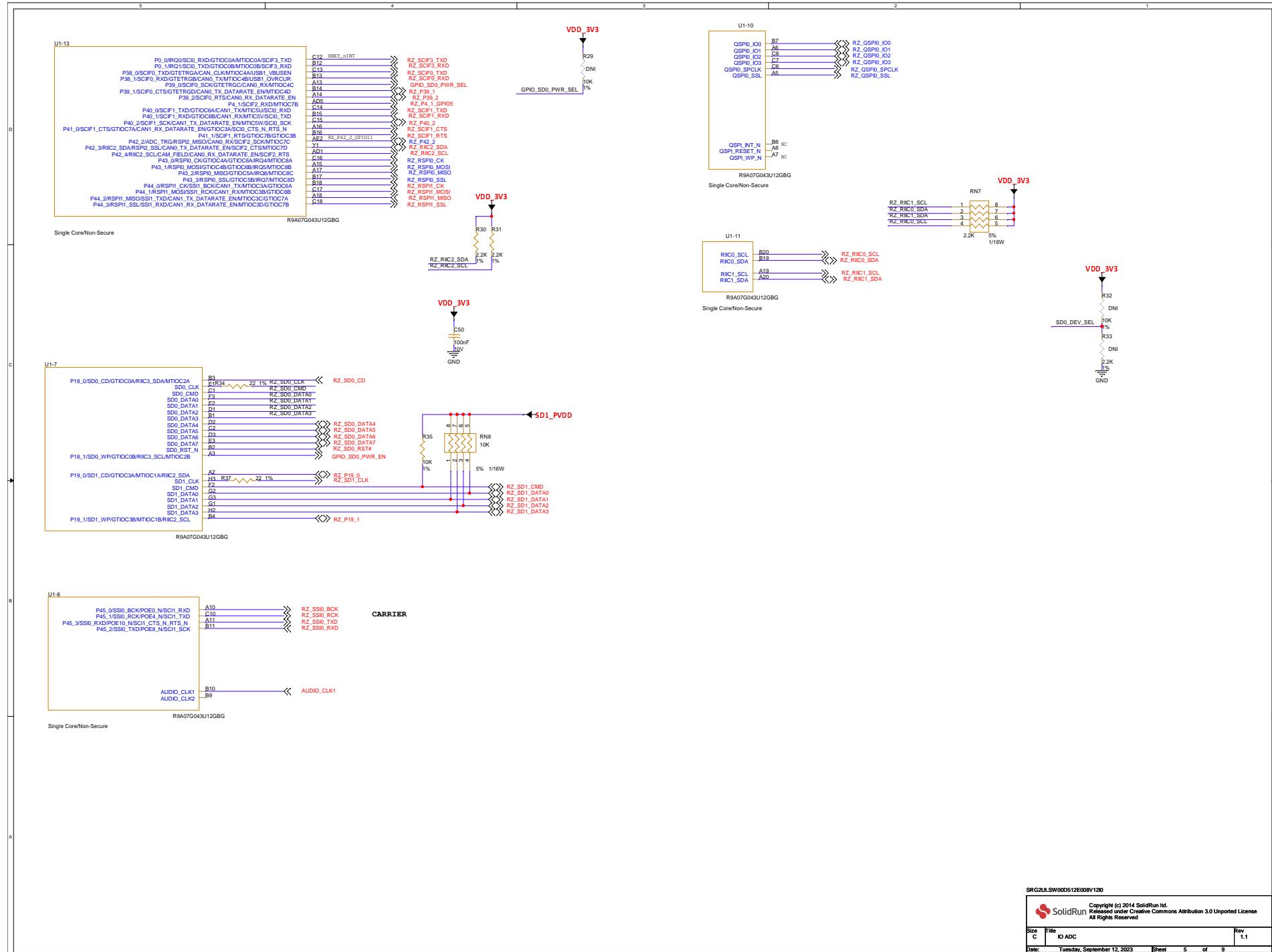
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Size	Title	Rev
C	PMIC	1.1

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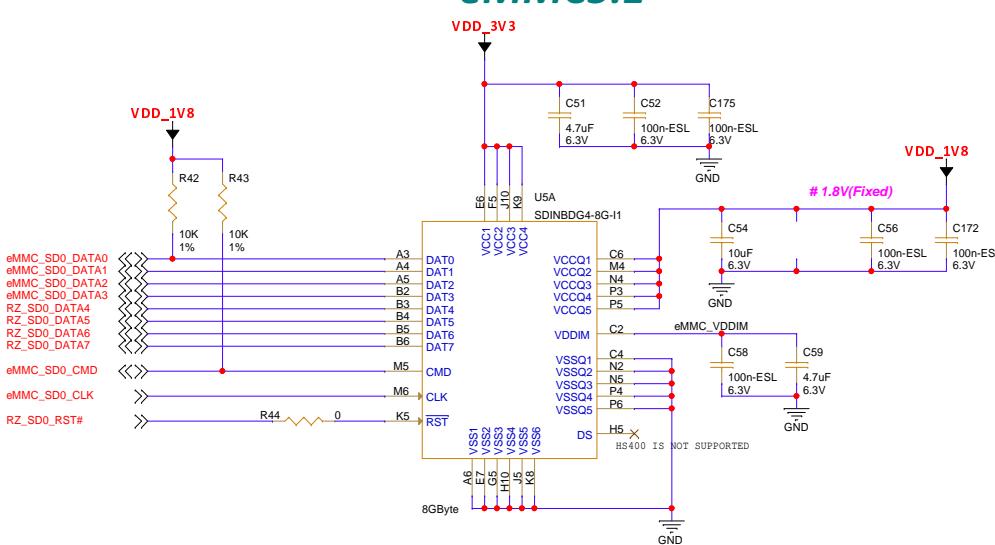




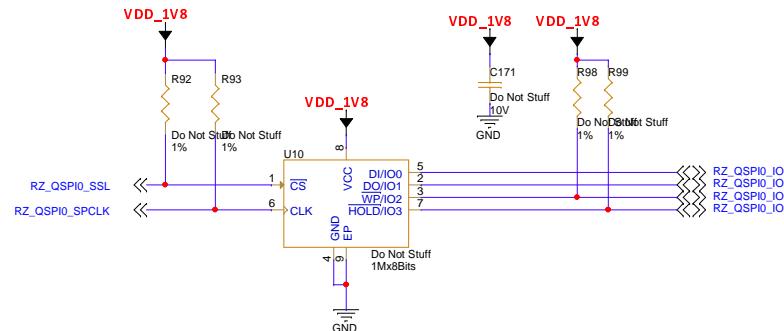


Storage

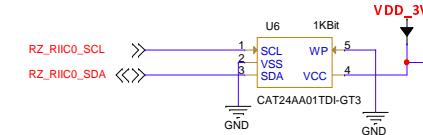
eMMC5.1



QSPI Flash (Default N/A)



EEPROM (Default N/A)



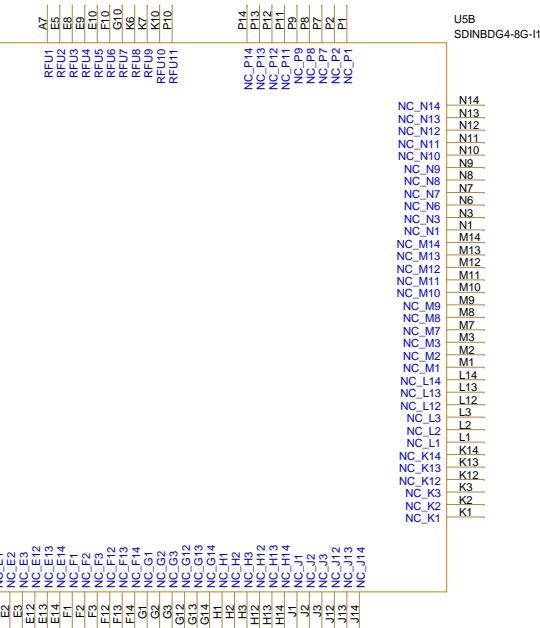
SRG2ULSW000512E008V120



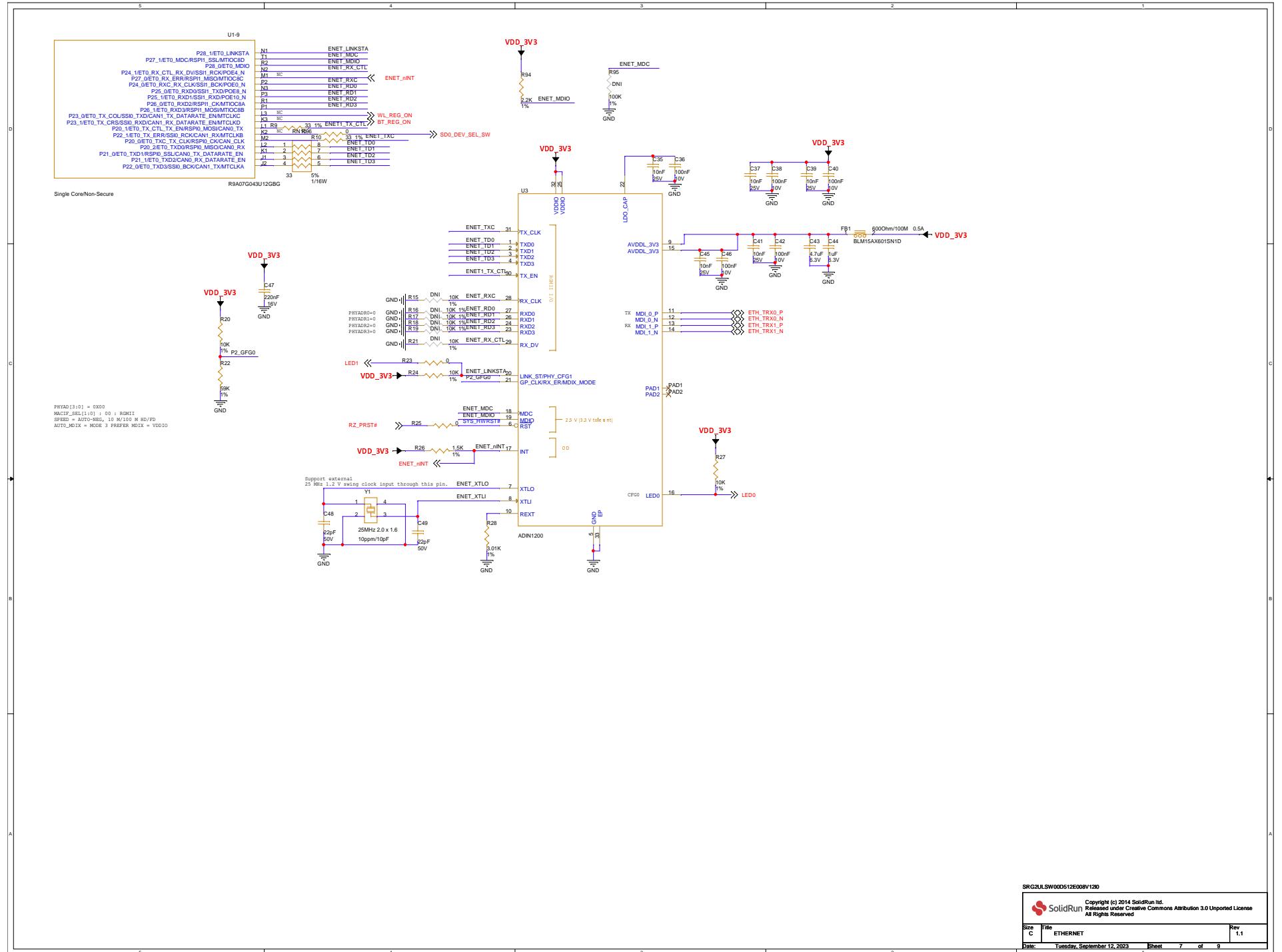
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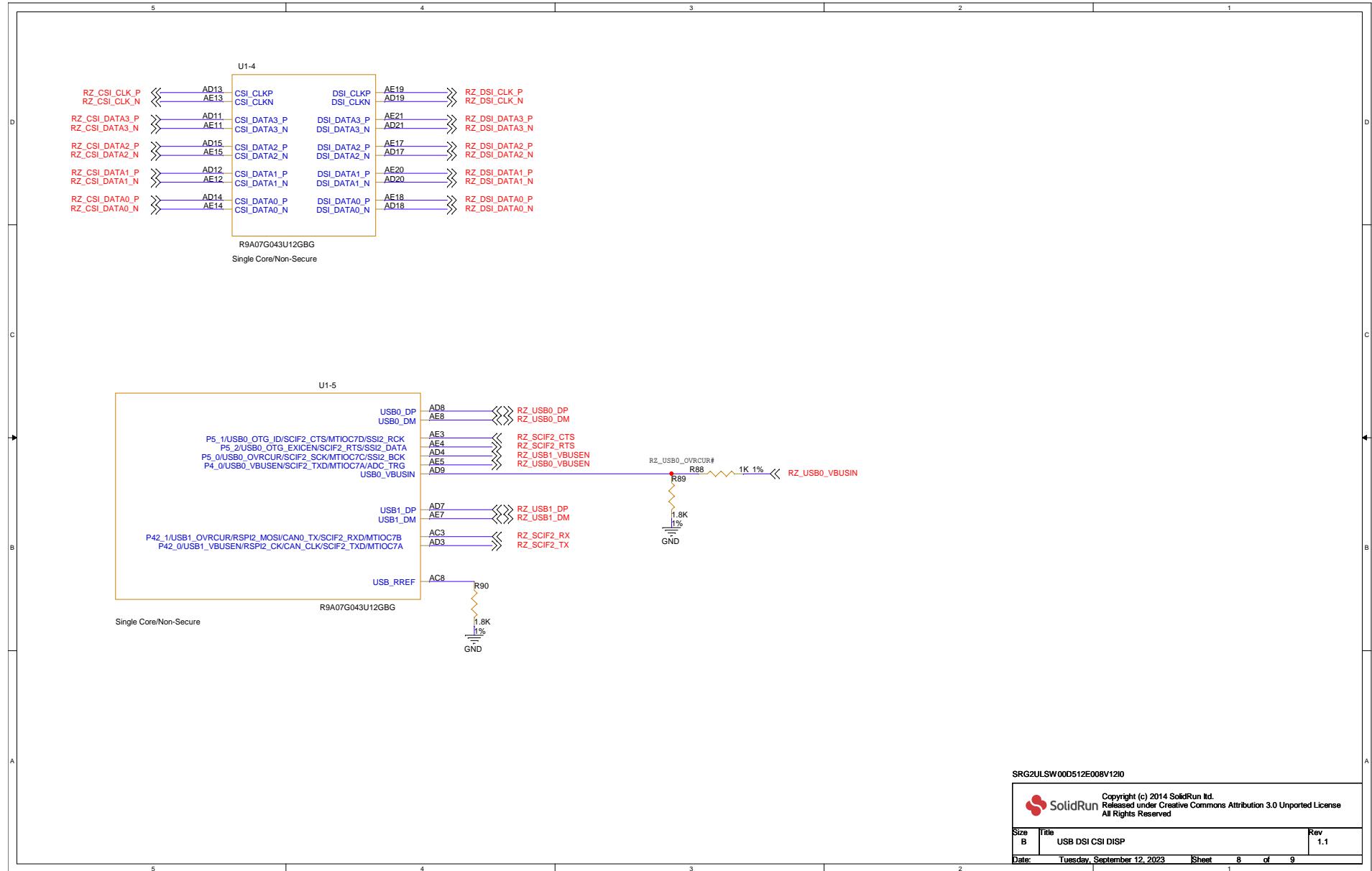
Size	Title	Rev
B	eMMC/EEPROM	1.1

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U5B SDINBDG4-8G-I1





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Size	Title	Rev
B	USB DS1 CSI DISP	1.1
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