

# CN9130 SOM

## SolidRun Ltd.

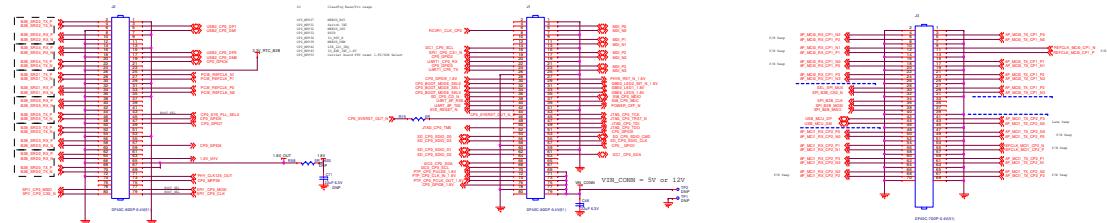
### Revision History

Rev#	Date	Owner	Description
1.0	Jan 2021	AlonR	First Release
1.1	June 2021	AlonR	<ul style="list-style-type: none"><li>1. Fix DDR bus width BUG</li><li>2. Update MFPs to support RGMIICP_GE2</li><li>3. Add RGMIICP support with DP_VDDO A &amp; D</li><li>4. Correct EEPROM address from 0x100 to 0x1000</li><li>5. changed EFUSE due to stock shortage</li><li>6. Change B2B DC-DC due to stock shortage</li><li>7. Removed MCU</li><li>8. Changed SPIOMUX</li></ul>

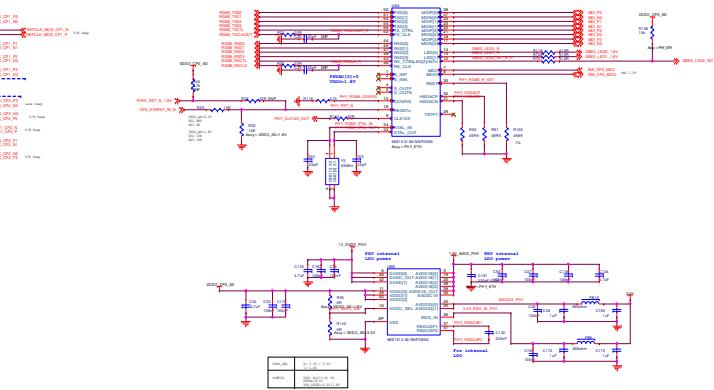
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1	B2B DC-DC Periph	Board to board connectors, MISC and power blocks
2	DDR	DDR4 64bit BUS
3	Processor	MFPs, CPU Power, GND and SERDES

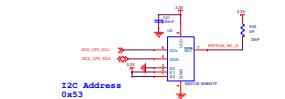
### SOM Conn A/B/C



### 1GbE PHY



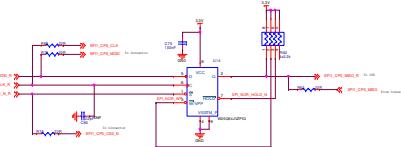
### 2Kbit EEPROM, MAC / SPD



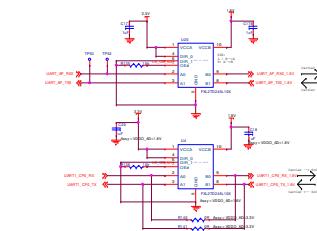
### SPI MUX



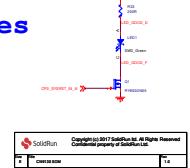
### SPI NOR Flash



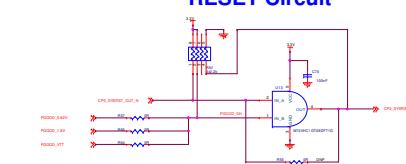
### UART Level Shifter



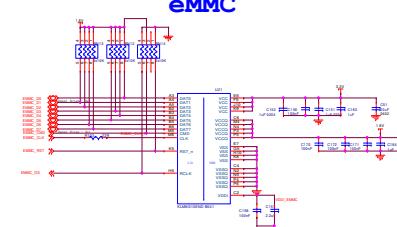
### LED



### RESET Circuit



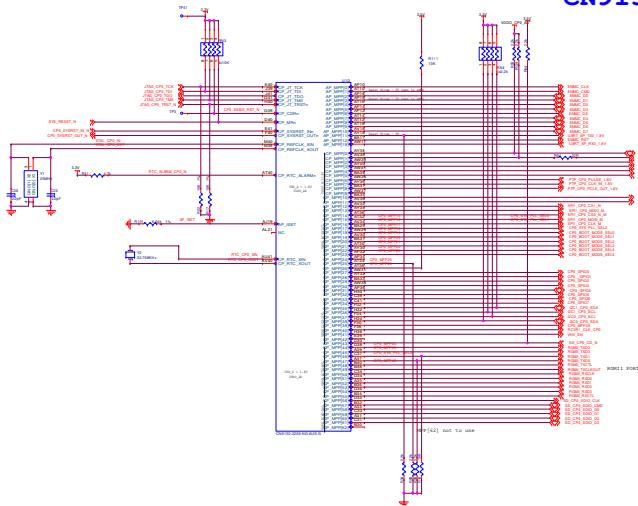
### eMMC



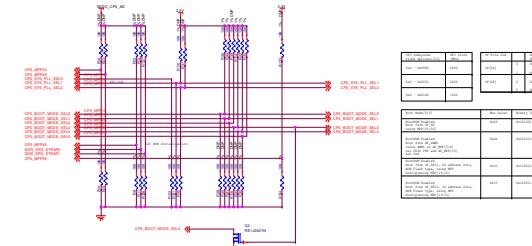
### Mechanical Holes



CN9130 CP0 MPPs



CP0 Boot Straps



CN9130 MoCi



CN9130 SERDES

