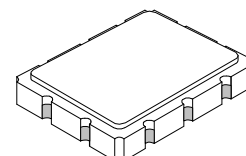


SF1080A

499.25 MHz SAW Filter



SM9171-10

- **Designed for CATV Applications (Pilot Tone)**
- **Tightly-controlled Insertion Loss**
- **9.1 x 7.1 mm Surface-mount Case**
- **Unbalanced Input and Output**
- **Complies with Directive 2002/95/EC (RoHS)**



Absolute Maximum Ratings

Rating	Value	Units
Maximum Incident Power in Passband	+10	dBm
Maximum DC Voltage between any 2 Terminals	30	VDC
Storage Temperature Range	-54 to +85	°C
Suitable for lead-free soldering - Max Soldering Profile	260°C for 30 s	

Electrical Characteristics

Characteristic	Sym	Notes	Min	Typ	Max	Units
Nominal Center Frequency	f_c	1	499.250			MHz
Passband	IL	1, 2	6.0	7.5	9.0	dB
Insertion Loss at f_c	$BW_{0.5}$		±100			kHz
0.5 dB Passband	BW_3		±800	±970		kHz
3 dB Passband					0.5	dB _{P-P}
Amplitude Ripple over f_c ±100 kHz						
Rejection	f_c -200 to f_c -3.0 and f_c +3.0 to f_c +200 MHz	1, 2, 3	35			dB
Ultimate			40			dB
Operating Temperature Range	T_A	1	-25		+75	°C

Impedance Matching to 50 Ω unbalanced	External L-C					
Case Style	SM9171-10 9.1 x 7.1 mm Nominal Footprint					
Lid Symbolization (XX = 2 character date code)	RFM SF1080A XX					

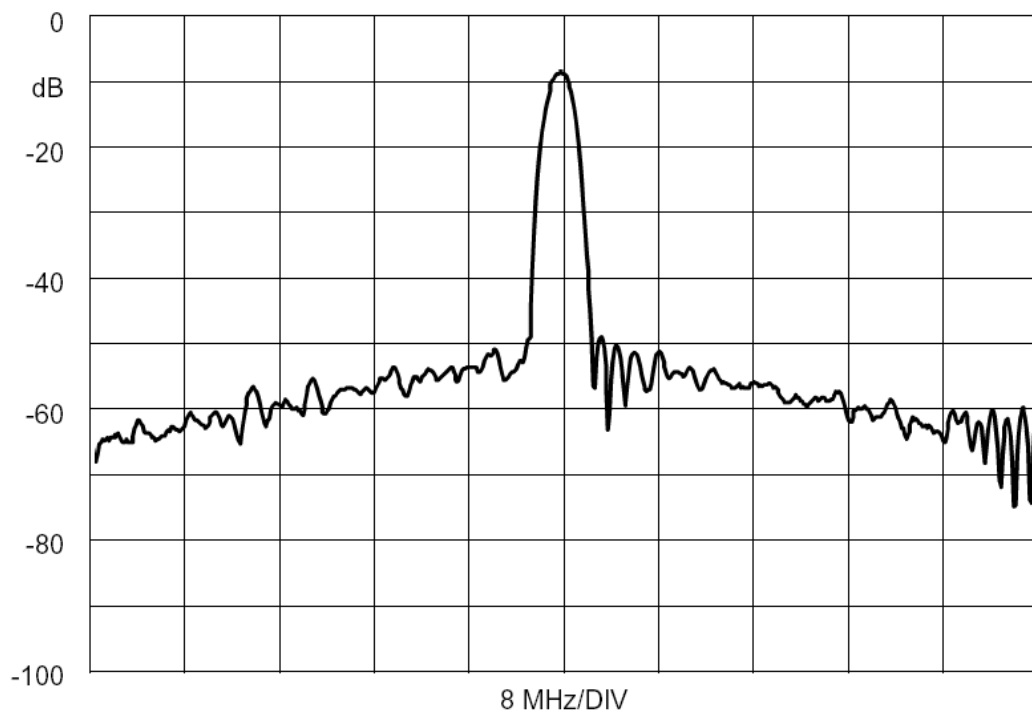
Electrical Connections

Connection	Terminals
Port 1 Hot	10
Port 1 Gnd Return	1
Port 2 Hot	5
Port 2 Gnd Return	6
Case Ground	All others

NOTES:

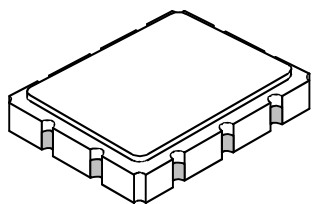
1. Unless noted otherwise, all specification apply over the operating temperature range with filter soldered to the specified demonstration board with impedance matching to 50 Ω network analyzer.
2. Unless noted otherwise, all frequency specifications are referenced to the nominal center frequency, f_c .
3. Rejection is measured as attenuation below the minimum IL point in the passband. Rejection in final user application is dependent on PCB layout and external impedance matching design. See Application Note No. 42 for details.
4. "LRIP" or "L" after the part number indicates "low rate initial production" and "ENG" or "E" indicates "engineering prototypes."
5. The design, manufacturing process, and specifications of this filter are subject to change.
6. Either Port 1 or Port 2 may be used for either input or output in the design. However, impedances and impedance matching may vary between Port 1 and Port 2, so that the filter must always be installed in one direction per the circuit design.
7. US and international patents may apply.
8. Murata, stylized Murata logo, and Murata N.A., Inc. are registered trademarks of Murata Manufacturing Co., Ltd.
9. Electrostatic Sensitive Device. Observe precautions for handling.





SM9171-10 Case

10-Terminal Ceramic Surface-Mount Case 9.1 x 7.1 mm Nominal Footprint



Case Dimensions						
Dimension	mm			Inches		
	Min	Nom	Max	Min	Nom	Max
A	8.86	9.09	9.40	0.349	0.358	0.370
B	6.88	7.11	7.40	0.271	0.280	0.291
C		1.91	2.00		0.075	0.079
D		0.99			0.039	
E		0.79			0.031	
H		1.0			0.039	
P		2.54			0.100	

Materials	
Solder Pad Plating	0.3 to 1.0 μ m Gold over 1.27 to 8.89 μ m Nickel
Lid Plating	2.0 to 3.0 μ m Nickel
Body	Al ₂ O ₃ Ceramic
Pb Free	

Electrical Connections		
Connection		Terminals
Port 1	Input or Return	6
	Return or Input	5
Port 2	Output or Return	1
	Return or Output	10
Ground		All others
Single Ended Operation		Return is ground
Differential Operation		Return is hot

