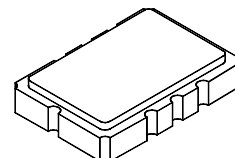


# HO4050B1

## 622.08 MHz Differential SAW Oscillator



SMC-08A

- **Quartz SAW Stabilized Differential Output Technology**
- **Very Low Jitter Fundamental-Mode Operation at 622.08 MHz**
- **Voltage Tunable for Phase Locked Loop Applications**
- **Complies with Directive 2002/95/EC (RoHS)**

The HO4050B1 is a voltage-controlled SAW clock (VCSC) designed for phase-locked loop (PLL) applications in optical data communications systems. The differential outputs of the HO4050B1 are generated by high-Q, fundamental mode quartz surface acoustic wave (SAW) technology. This technique provides very low output jitter and phase noise, plus excellent immunity to power supply noise. The HO4050B1 differential outputs feature typical  $\pm 1\%$  symmetry, and can be DC-configured to drive a wide range of high-speed logic families. The HO4050B1 is packaged in a hermetic metal-ceramic LCC.

### Absolute Maximum Ratings

| Rating            | Value      | Units |
|-------------------|------------|-------|
| DC Supply Voltage | 0 to 5.5   | Vdc   |
| Tune Voltage      | 0 to 5.5   | Vdc   |
| Case Temperature  | -55 to 100 | °C    |

### Electrical Characteristics

| Characteristic                                |   | Sym               | Notes      | Minimum | Typical   | Maximum | Units             |
|---|---|-------------------|------------|---------|-----------|---------|-------------------|
| Operating Frequency                           | Absolute Frequency  | $f_O$             | 1          |         | 622.08    |         | MHz               |
|   | Tuning Range  |                   | 2          |         | $\pm 100$ |         | ppm               |
|   | Tuning Voltage  |                   | 1          | 0       |           | 3.3     | Vdc               |
|   | Tuning Linearity  |                   | 1          |         | $\pm 5$   |         | %                 |
|   | Modulation Bandwidth  |                   |            | 200     |           |         | kHz               |
| Q and $\bar{Q}$ Output                        | Voltage into 50 $\Omega$ (VSWR $\leq 1.2$ )                             | $V_O$             | 1,3        | 0.60    |           | 1.1     | V <sub>P-P</sub>  |
|   | Operating Load VSWR   |                   | 1,3        |         |           | 2:1     |                   |
|   | Symmetry  |                   | 3, 4, 5    | 45      |           | 55      | %                 |
|   | Harmonic Spurious   |                   | 3, 4, 6    |         |           | -15     | dBc               |
|   | Nonharmonic Spurious  |                   | 3, 4, 6, 7 |         |           | -60     | dBc               |
| Phase Noise                                   | @ 100 Hz offset   |                   | 3, 6       |         | -70       |         | dBc/Hz            |
|   | @ 1 kHz offset  |                   | 3, 6       |         | -100      |         | dBc/Hz            |
|   | @ 10 kHz offset   |                   | 3, 6       |         | -125      |         | dBc/Hz            |
|   | Noise Floor   |                   | 3, 6       |         | -150      |         | dBc/Hz            |
| Q and $\bar{Q}$ Jitter                        | RMS Jitter (10kHz to 80MHz)   |                   | 3, 4, 6, 7 |         | 0.1       |         | ps                |
|   | No Noise on $V_{CC}$  |                   | 3, 4, 6, 7 |         | 12        |         | ps <sub>P-P</sub> |
|   | 200 mV <sub>P-P</sub> Noise, from 1 MHz to $\frac{1}{2}f_O$ on $V_{CC}$ |                   | 3          |         | 12        |         | ps <sub>P-P</sub> |
| Input Impedance (Tuning Port)                 |   |                   |            | 8       | 10        |         | K $\Omega$        |
| Output DC Resistance (between Q & $\bar{Q}$ ) |   |                   | 1, 3       | 50      |           |         | K $\Omega$        |
| DC Power Supply                               | Operating Voltage   | $V_{CC}$          | 1, 3       | 3.13    | 3.3       | 3.6     | Vdc               |
|   | Operating Current   | $I_{CC}$          | 1, 3       |         |           | 70      | mA                |
| Operating Case Temperature                    |   | $T_C$             | 1, 3       | -40°C   |           | +85°C   | °C                |
| Lid Symbolization (YY=Year, WW=Week)          |   | RFM HO4050B1 YYWW |            |         |           |         |                   |

**CAUTION: Electrostatic Sensitive Device. Observe precautions for handling. COCOM CAUTION: Approval by the U.S. Department of Commerce is required prior to export of this device.**

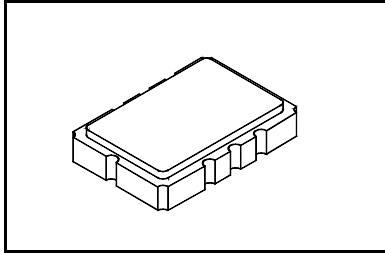
### NOTES:

1. Unless otherwise noted, all specifications include the combined effects of load VSWR,  $V_{CC}$  and  $T_C$ .
2. Net tuning range after tuning out the effects of initial manufacturing tolerances, VSWR pushing/pulling,  $V_{CC}$ ,  $T_C$  and aging.
3. The internal design, manufacturing processes, and specifications of this device are subject to change without notice.
4. Specified only for a balanced load with a VSWR  $< 1.2$  ( 50 ohms each side), and a  $V_{CC} = 3.0$  Vdc.
5. Symmetry is defined as the width in (% of total period) measure at 50% of the peak-to-peak voltage of either output.
6. Jitter and other noise outputs due to power supply noise or mechanical vibration are not included in this specification except where noted.
7. Applies to period jitter of either differential output. Measured with a Tektronix CSA803 signal analyzer with at least 1000 samples.
8. One or more of the following United States patents apply: 4, 616,197; 4,670,681; 4,760,352.

# To Be Discontinued

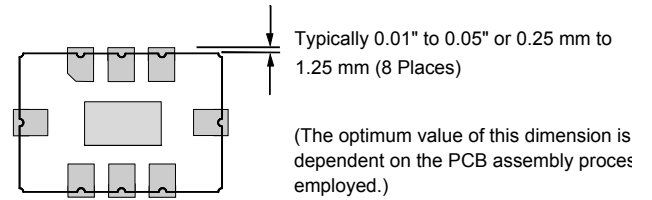
SMC-8A

8-Terminal Surface Mount Case



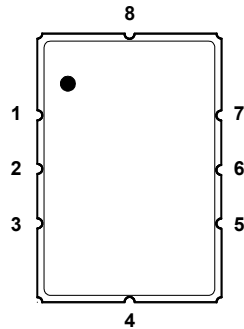
## Typical Printed Circuit Board Land Pattern

A typical land pattern for a circuit board is shown below. Grounding of the metallic center pad is optional.



## Electrical Connections

| Terminal Number | Connection       |
|-----------------|------------------|
| 1               | Tune             |
| 2               | *Enable          |
| 3               | Ground           |
| 4               | Ground           |
| 5               | Q Output         |
| 6               | $\bar{Q}$ Output |
| 7               | $V_{CC}$         |
| 8               | Ground           |
| LID             | Ground           |



## Dimensions

| Dimension | mm           |       | Inches        |       |
|-----------|--------------|-------|---------------|-------|
|           | MIN          | MAX   | MIN           | MAX   |
| A         | 13.46        | 13.97 | 0.530         | 0.550 |
| B         | 9.14         | 9.66  | 0.360         | 0.380 |
| C         | 1.93 Nominal |       | 0.076 Nominal |       |
| D         | 1.93 Nominal |       | 0.076 Nominal |       |
| E         | 2.54 Nominal |       | 0.100 Nominal |       |
| F         | 1.27 Nominal |       | 0.050 Nominal |       |

\*Enable Sense: Pin 2 Ground-Clock Off

