

SF2315E-1

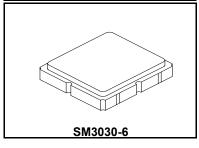
- Low-loss 782 MHz SAW Filter
- · Designed for 50 ohm Source/Load
- Complies with Directive 2002/95/EC (RoHS)



Absolute Maximum Ratings

Rating	Value	Units
Input Power Level	+25	dBm
DC Voltage on any Non-ground Terminal	3	V
Operating Temperature Range	-40 to +85	°C
Storage Temperature Range in Tape and Reel	-40 to +85	°C
Maximum Soldering Profile, 5 cycles/10 seconds maximum	265	°C

782 MHz **SAW Filter**



Electrical Characteristics

Characteristic	Sym	Notes	Min	Тур	Max	Units
Center Frequency	f _C			782		MHz
Insertion Loss, 777 to 787 MHz	IL			2.6	3.4	dB
Bandwidth			10	29		MHz
Amplitude Ripple, 777 to 787 MHz				0.3	1.00	dB _{P-P}
Attenuation, Referenced to 0 dB:						
300 to 700 MHz			28	46		dB
728 to 757 MHz			10	40		
880 to 1050 MHz			28	42		
Source Impedance	Z _S			100		Ω
Load Impedance	Z _L			50		Ω
Casa Style	İ	SM3030.6.3.0 v.3.0 mm Naminal Footprint				

Case Style	SM3030-6 3.0 x 3.0 mm Nominal Footprint	
Lid Symbolization (Y=year, WW=week, S=shift) dot=pin 1 indicator	A82, YWWS	
Standard Reel Quantity Reel Size 7 inch	500 Pieces/Reel	
Reel Size 13 inch	3000 Pieces/Reel	

Electrical Connections

Connection	Terminals
Input	1.3
Output	6
Case Ground	All others

Measurement Circuit: 100Ω



CAUTION: Electrostatic Sensitive Device. Observe precautions for handling.

Unless noted otherwise, all specifications apply over the operating temperature range with filter soldered to the specified demonstration board with impedance matching to 50 Ω and measured with 50 Ω network analyzer.

Unless noted otherwise, all frequency specifications are referenced to the nominal center frequency, fc.

Rejection is measured as attenuation below the minimum IL point in the passband. Rejection in final user application is dependent on PCB layout and external impedance matching decime. See Application Note No. 42 for details.

2. matching design. See Application Note No. 42 for details.

"LRIP" or "L" after the part number indicates "low rate initial production" and "ENG" or "E" indicates "engineering prototypes."

The design, manufacturing process, and specifications of this filter are subject to change.

Either Port 1 or Port 2 may be used for either input or output in the design. However, impedances and impedance matching may vary between Port 1 and Port 2, so that the filter must always be installed in one direction per the circuit design.

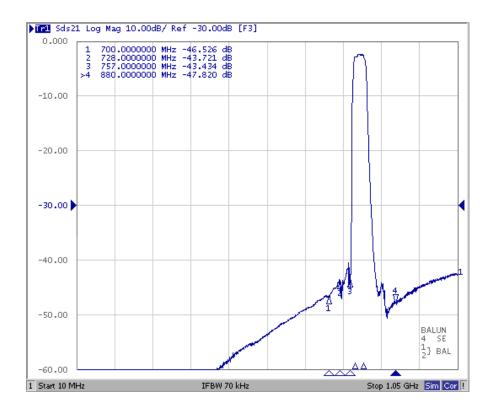
US and international patents may apply.

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50Ω

Filter Response Plots

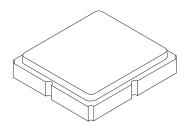


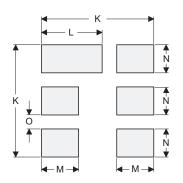


SM3030-6 Case

6-Terminal Ceramic Surface-Mount Case 3.0 X 3.0 mm Nominal Footprint







PCB Footprint Top View

Dimension	mm		Inches			
Dimension	Min	Nom	Max	Min	Nom	Max
Α	2.87	3.00	3.13	0.113	0.118	0.123
В	2.87	3.00	3.13	0.113	0.118	0.123
С	1.12	1.25	1.38	0.044	0.049	0.054
D	0.77	0.90	1.03	0.030	0.035	0.040
E	2.67	2.80	2.93	0.105	0.110	0.115
F	1.47	1.60	1.73	0.058	0.063	0.068
G	0.72	0.85	0.98	0.028	0.033	0.038
Н	1.37	1.50	1.63	0.054	0.059	0.064
I	0.47	0.60	0.73	0.019	0.024	0.029
J	1.17	1.30	1.43	0.046	0.051	0.056
K		3.20			0.126	
L		1.70			0.067	
М		1.05			0.041	
N		0.81			0.032	
0		0.38			0.015	

Case Materials

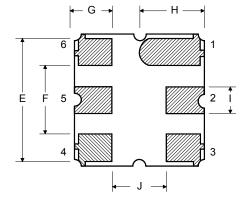
Materials				
Solder Pad Plating	0.3 to 1.0 μm Gold over 1.27 to 8.89 μm Nickel			
Lid Plating	2.0 to 3.0 µm Nickel			
Body	Al ₂ O ₃ Ceramic			
Pb Free				

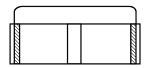
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TOP VIEW

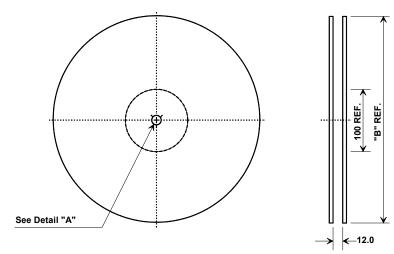
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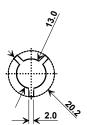




Tape and Reel Specifications



"B"		Quantity Per Reel	
Inches	millimeters	Quantity Fer Reer	
7	178	500	
13	330	3000	



COMPONENT ORIENTATION and DIMENSIONS

Carrier Tape Dimensions				
Ao	3.35 mm			
Во	3.35 mm			
Ko	1.40 mm			
Pitch	8.0 mm			
W	12.0 mm			

