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SF2086C

240.00 MHz

**Absolute Maximum Ratings** 

• 5.0 X 5.0 mm Surface-mount Case

· Complies with Directive 2002/95/EC (RoHS)

Single-ended Input and Output

· 240 MHz SAW Filter

<u> </u>		
Rating	Value	Units
Maximum Incident Power in Passband	+13	dBm
Maximum DC Voltage on any Non-ground Terminal	30	VDC
Storage Temperature Range in Tape and Reel	-40 to +85	°C
Suitable for Lead-free Soldering - Maximum Soldering Temperature	260 °C for 30 s	



SM5050-8

#### **Electrical Characteristics**

Characteristic	Sym	Notes	Min	Тур	Max	Units
Nominal Center Frequency	f <sub>C</sub>	1		240.00		MHz
Insertion Loss		1			13	dB
2 dB Bandwidth	BW <sub>2.0</sub>	1	30			MHz
Amplitude Ripple, f <sub>C</sub> ±15 MHz		1			2	dB <sub>P-P</sub>
Amplitude Ripple, f <sub>C</sub> ±15 MHz, within adjacent 5 MHz windows		1			1.5	dB <sub>P-P</sub>
Group Delay Deviation, f <sub>C</sub> ±15 MHz		1		100	180	ns <sub>P-P</sub>
Group Delay Deviation, f <sub>C</sub> ±15 MHz, within adjacent 5 MHz windows		1			75	ns <sub>P-P</sub>
VSWR at f <sub>C</sub>		1		2.5:1		
40 dB Rejection Bandwidth		1, 2			65	MHz
Operating Temperature Range			-40		85	°C
Case Style		SM5050-8 5 x 5 mm Nominal Footprint				
Lid Symbolization (Y=year, WW=week, S=shift)		RFM 588 YWWS				

#### **Electrical Connections**

Connection		Terminals		
Port 1	Input	1		
Port 2	Output	5		
	Ground	All others		
Dot indicates Pin 1				



### **CAUTION: Electrostatic Sensitive Device. Observe precautions for handling.**

#### NOTES:

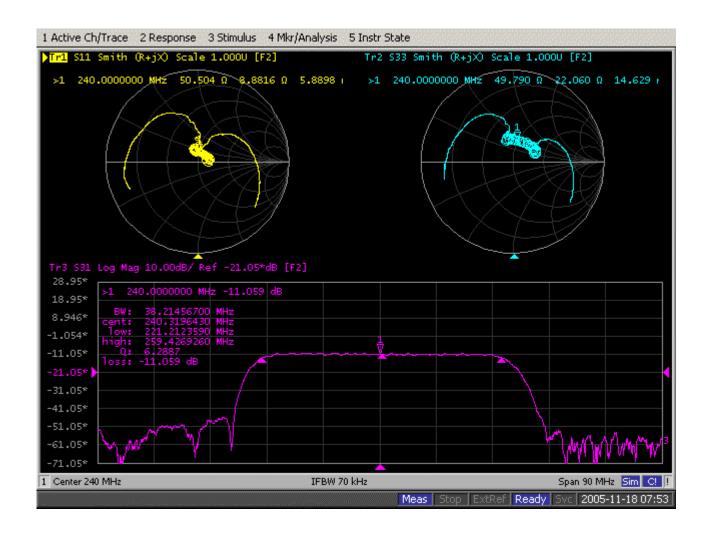
- Unless noted otherwise, all specifications apply over the operating temperature range with filter soldered to the specified demonstration board with impedance matching to 50  $\Omega$  and measured with 50  $\Omega$  network analyzer.
- Rejection is measured as attenuation below the minimum IL point in the passband. Rejection in final user application is dependent on PCB layout and external impedance matching design. See Application Note No. 42 for details.

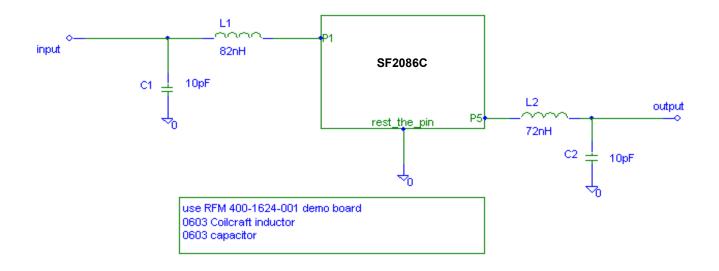
  The design, manufacturing process, and specifications of this filter are subject to change.

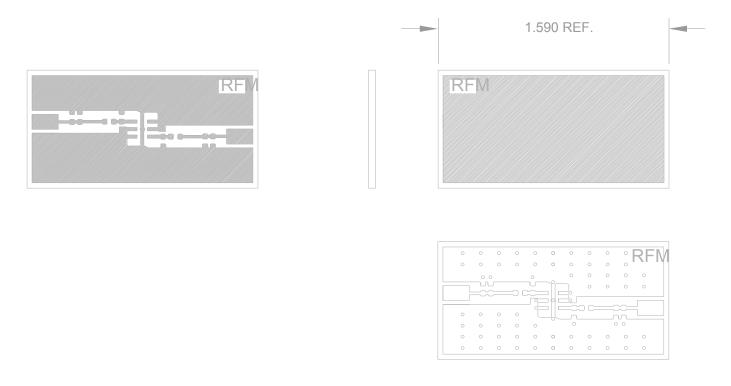
  Tape and Reel Standard ANSI / EIA 481.

- Either Port 1 or Port 2 may be used for either input or output in the design. However, impedances and impedance matching may vary between Port 1 and Port 2, so that the filter must always be installed in one direction per the circuit design.
- US and international patents may apply.

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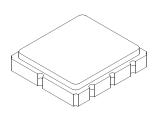


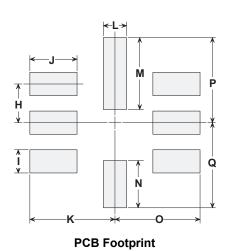


DRILL ALL HOLES #76 DRILL (0.020) ALL HOLES ARE PLATED THRU.

# SM5050-8 Surface-Mount 8-Terminal Ceramic Case 5.0 X 5.0 mm Nominal Footprint



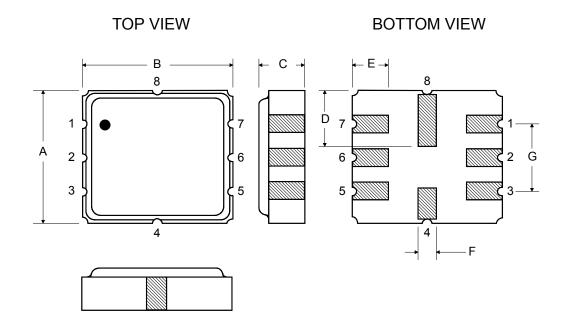




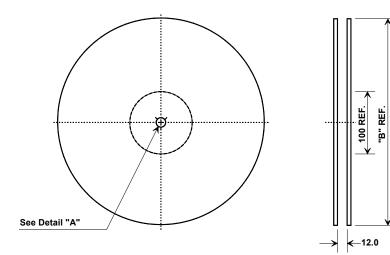
Dimension	mm		Inches			
Dimension	Min	Nom	Max	Min	Nom	Max
Α	4.80	5.00	5.20	0.189	0.197	0.205
В	4.80	5.00	5.20	0.189	0.197	0.205
С	1.30	1.50	1.70	0.050	0.060	0.067
D	1.98	2.08	2.18	0.078	0.082	0.086
E	1.07	1.17	1.27	0.042	0.046	0.050
F	0.50	0.64	0.70	0.020	0.025	0.028
G	2.39	2.54	2.69	0.094	0.100	0.106
Н		1.27			0.050	
I		0.76			0.030	
J		1.55			0.061	
K		2.79			0.110	
L		0.76			0.030	
М		2.36			0.093	
N		1.55			0.061	
0		2.79			0.110	
P		2.79			0.110	
Q		2.79			0.110	

#### **Case Materials**

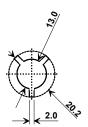
Materials			
Solder Pad Plating	0.3 to 1.0 µm Gold over 1.27 to 8.89 µm Nickel		
Lid Plating	2.0 to 3.0 µm Nickel		
Body	Al <sub>2</sub> O <sub>3</sub> Ceramic		
Pb Free			



## **Tape and Reel Specifications**



	'B" nal Size	Quantity Per Reel
Inches	millimeters	
7	178	500
13	330	3000



#### **COMPONENT ORIENTATION and DIMENSIONS**

Carrier Tape Dimensions	
Ao	5.3 mm
Во	5.3 mm
Ko	2.0 mm
Pitch	8.0 mm
W	12.0 mm

