

- · SAW Filter, 895 MHz, 15 MHz BW
- 7.0 X 5.0 mm 10 pin Surface-Mount Case
- Complies with Directive 2002/95/EC (RoHS)

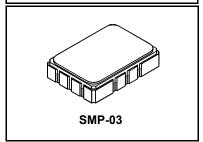


Absolute Maximum Ratings

Rating	Value	Units
Maximum Incident Power in Passband	+20	dBm
Maximum DC Voltage Between any Two Terminals	0	VDC
Operating Temperature Range	-20 to +70	°C

SF2145B

895 MHz **SAW Filter**



Electrical Characteristics

Characteristic		Sym	Notes	Min	Тур	Max	Units
Center Frequency		f _C	1		895	•	MHz
Source Impedance, single	e ended				50		Ω
Load Impedance, single e	nded				50		Ω
1 dB Bandwidth					18		MHz
Insertion Loss, 894 to 895	5.5 MHz	IL			8.5	9.0	dB
Insertion Loss Variation:	Any point within 894 to 895.5 MHz measured at a constant temperature with the RF input level varying from +10 to +20 dBm					0.3	ΔdB
Amplitude Ripple, 894 to 8	395.5 MHz				.35	.75	dB
Average Group Delay, 894	4 to 895.5 MHz			804	814	824	ns
Group Delay Variation, 894.0 to 895.5 MHz					10	25	ns _{P-P}
Input / Output Return Loss	s at f _C			10	15		dB
Temperature Coefficient					-18		ppm/K

Case Style	SMP-03 7.0 x 5.0 mm Nominal Footprint
Lid Symbolization (YY=year, WW=week) See note 4	RFM SF2145B YYWW



CAUTION: Electrostatic Sensitive Device. Observe precautions for handling. NOTES:

- Unless noted otherwise, all specifications apply over the operating temperature range with filter soldered to the specified demonstration board with impedance matching to 50 Ω and measured with 50 Ω network ana-
- Únless noted otherwise, all frequency specifications are referenced to the
- nominal center frequency, fc.

 Rejection is measured as attenuation below the minimum IL point in the passband. Rejection in final user application is dependent on PCB layout and external impedance matching design. See Application Note No. 42 for
- details.

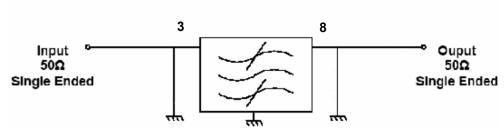
 "LRIP" or "L" after the part number indicates "low rate initial production" and "ENG" or "E" indicates "engineering prototypes."
- The design, manufacturing process, and specifications of this filter are subject to change.

 Either Port 1 or Port 2 may be used for either input or output in the design.
- However, impedances and impedance matching may vary between Port 1 and Port 2, so that the filter must always be installed in one direction per the circuit design.
- US and international patents may apply.

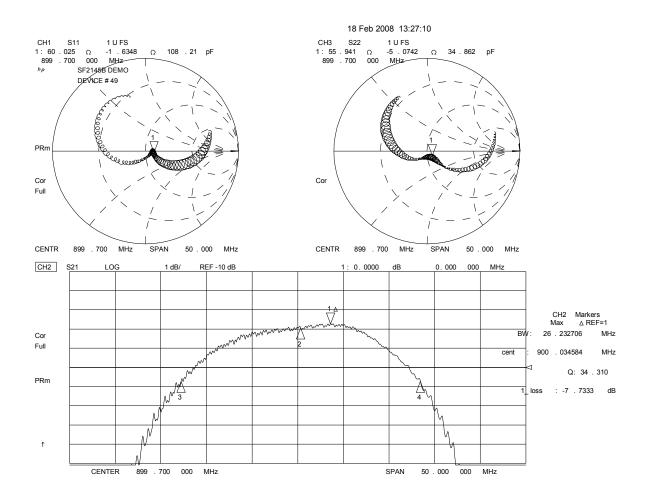
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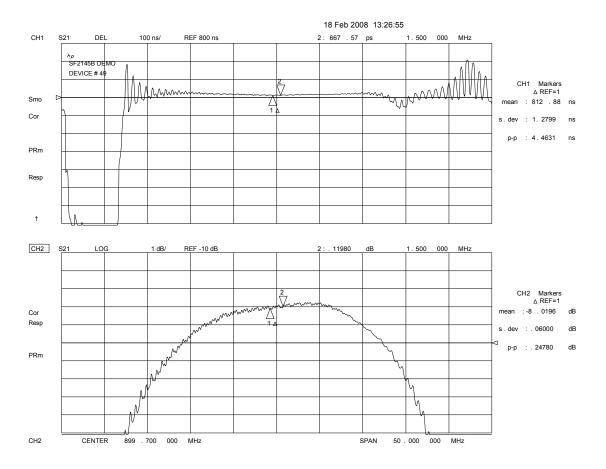
Tesing Environment

$50 \Omega / 50 \Omega$ Configuration

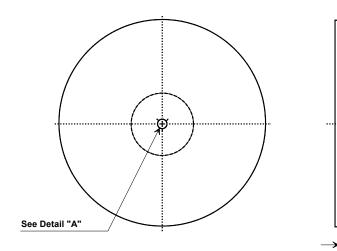


Note: Unmatched Filter to 50Ω Input / Output.

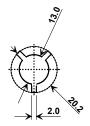




Tape and Reel Specifications

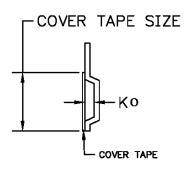


"B " Nominal Size		Quantity Per Reel
Inches	millimeters	
7	178	500
13	330	2000

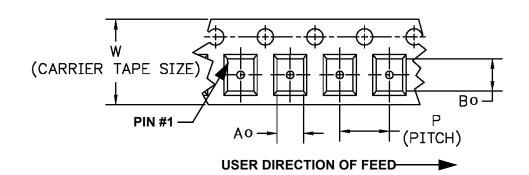


COMPONENT ORIENTATION and DIMENSIONS

←16.0

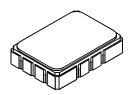


Carrier Tape Dimensions		
Ао	5.5 mm	
Во	7.5 mm	
Ко	2.0 mm	
Pitch	8.0 mm	
W	W 16.0 mm	

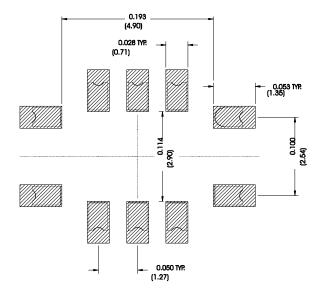


SMP-03 Case

10-Terminal Ceramic Surface-Mount Case 7 x 5 mm Nominal Footprint



Recommended PCB Footprint



Case Dimensions Dimension Inches mm Nom Max Min Nom Max Min 6.80 7.00 7.20 0.268 0.276 0.283 4.80 5.00 5.20 0.189 0.197 0.205 В 1.65 2.00 0.065 0.079 C .47 0.60 .73 0.019 0.024 0.029 D 2.41 2.54 2.67 0.095 0.100 0.105 Ε 0.039 0.044 0.87 1.0 1.13 0.034 4.87 5.00 5.13 0.192 0.197 0.202 2.87 3.00 3.13 0.113 0.118 0.123 Κ 1.27 1.40 0.045 0.050 0.055 1.14

Electrical Connections	
Connection	Terminals
Input (Port 1)	3
Output (Port 2)	8
Case Ground	2, 4, 7, 9
To Be Grounded	1, 2, 4, 5, 6, 7, 9, 10

Materials		
Solder Pad Plating	0.3 to 1.0 μm Gold over 1.27 to 8.89 μm Nickel	
Lid Plating	2.0 to 3.0 µm Nickel	
Body	Al ₂ O ₃ Ceramic	
Pb Free		

