BFU610F

NPN wideband silicon RF transistor

Rev. 0.8 — 23 November 2010

Product data sheet

1. Product profile

1.1 General description

NPN silicon microwave transistor for high speed, low noise applications in a plastic, 4-pin dual-emitter SOT343F package.

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

1.2 Features and benefits

- Low noise high gain microwave transistor
- Noise figure (NF) = 1.7 dB at 5.8 GHz
- High associated gain 13.5 dB at 5.8 GHz
- 40 GHz f_T silicon technology

1.3 Applications

- Low current battery equiped applications
- Low noise amplifiers for microwave communications systems
- Analog/digital cordless applications
- Ku band oscillators DRO's
- RKE
- AMR
- GPS
- ZigBee
- LTE, cellular, UMTS
- FM radio
- Mobile TV
- Bluetooth



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1.4 Quick reference data

Table 1. Quick reference data

						70
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	-	16	V %
V_{CEO}	collector-emitter voltage	open base	-	-	5.5	V
V_{EBO}	emitter-base voltage	open collector	-	-	2.5	V
I _C	collector current		-	2	10	mA
P _{tot}	total power dissipation	$T_{sp} \le 90 ^{\circ}C$	[1] -	-	136	mW
h _{FE}	DC current gain	$I_C = 1 \text{ mA}; V_{CE} = 2 \text{ V};$ $T_j = 25 \text{ °C}$	90	135	180	
C_{CBS}	collector-base capacitance	$V_{CB} = 2 \text{ V}; f = 1 \text{ MHz}$	-	19	-	fF
f _T	transition frequency	$I_C = 4 \text{ mA}; V_{CE} = 2 \text{ V};$ f = 2 GHz; $T_{amb} = 25 \text{ °C}$	-	15	-	GHz
$G_{p(max)}$	maximum power gain	$I_C = 5 \text{ mA}; V_{CE} = 2 \text{ V};$ f = 5.8 GHz; $T_{amb} = 25 ^{\circ}\text{C}$	[2] -	17.0	-	dB
NF	noise figure	I_C = 2 mA; V_{CE} = 2 V; f = 5.8 GHz; Γ_S = Γ_{opt}	-	1.7	-	dB
P _{L(1dB)}	output power at 1 dB gain compression	I_{C} = 10 mA; V_{CE} = 1.5 V; Z_{S} = Z_{L} = 50 Ω ; f = 5.8 GHz; T_{amb} = 25 °C	-	3	-	dBm

^[1] T_{sp} is the temperature at the solder point of the emitter lead.

2. Pinning information

Table 2. Discrete pinning

Pin	Description	Simplified outline	Graphic symbol
1	emitter		
2	base	3 4	4
3	emitter		2
4	collector		1, 3
		2 1	mbb159

3. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
BFU610F	-	plastic surface-mounted flat pack package; reverse pinning; 4 leads	SOT343F		

^[2] $G_{p(max)}$ is the maximum power gain, if K > 1. If K < 1 then $G_{p(max)}$ = Maximum Stable Gain (MSG).

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Marking 4.

Table 4. **Marking**

Type number	Marking	Description
BFU610F	D1*	* = p : made in Hong Kong
		* = t : made in Malaysia
		* = w : made in China

Limiting values

Table 5. **Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	16	V
V_{CEO}	collector-emitter voltage	open base	6	5.5	V
V_{EBO}	emitter-base voltage	open collector		2.5	V
I _C	collector current		-	10	mA
P _{tot}	total power dissipation	T _{sp} ≤ 90 °C	[1] -	136	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C

^[1] T_{sp} is the temperature at the solder point of the emitter lead.

Thermal characteristics

Thermal characteristics Table 6.

Symbol	Parameter	Conditions	Тур	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point		440	K/W

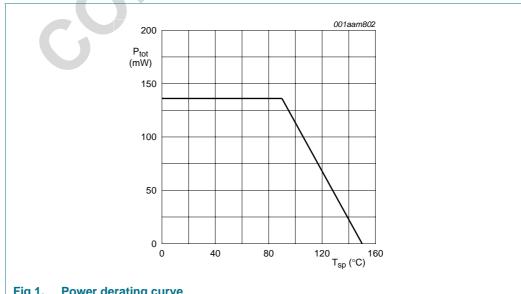


Fig 1. Power derating curve

Characteristics

Table 7. **Characteristics**

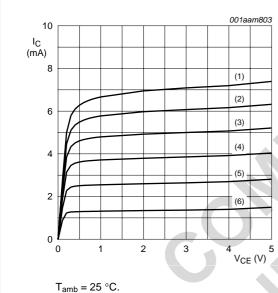
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7. Ch	naracteristics				PA	PA
able 7. 7 = 25 °C	Characteristics unless otherwise specified	NPN widel				PAS
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	$I_C = 2.5 \mu A; I_E = 0 \text{ mA}$	16	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 1 mA; I _B = 0 mA	5.5	-	-	V
lc	collector current		-	2	10	mA
Ісво	collector-base cut-off current	$I_E = 0 \text{ mA}; V_{CB} = 8 \text{ V}$	-	-	100	nA
) _{EE}	DC current gain	I _C = 1 mA; V _{CE} = 2 V	90	135	180	
C _{CES}	collector-emitter capacitance	V _{CB} = 2 V; f = 1 MHz	-	187	-	fF
CEBS	emitter-base capacitance	V _{EB} = 0.5 V; f = 1 MHz	-	227	-	fF
C _{CBS}	collector-base capacitance	V _{CB} = 2 V; f = 1 MHz	-	19	-	fF
f _T	transition frequency	$I_C = 4 \text{ mA}; V_{CE} = 2 \text{ V}; f = 2 \text{ GHz};$	-	15	-	GHz
	· •	T _{amb} = 25 °C				
$G_{p(max)}$	maximum power gain	$I_C = 5 \text{ mA}$; $V_{CE} = 2 \text{ V}$; $T_{amb} = 25 \text{ °C}$	[1]			
		f = 1.5 GHz	-	26	-	dB
		f = 1.8 GHz	-	25	-	dB
		f = 2.4 GHz	-	24	-	dB
		f = 5.8 GHz	-	17	-	dB
$ S_{21} ^2$	insertion power gain	$I_C = 5 \text{ mA}; V_{CE} = 2 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$				
		f = 1.5 GHz	-	17.5	-	dB
	() ·	f = 1.8 GHz	-	17	-	dB
		f = 2.4 GHz	-	16	-	dB
		f = 5.8 GHz	-	10.5	-	dB
NF	noise figure	I_C = 2 mA; V_{CE} = 2 V; Γ_S = Γ_{opt} ; T_{amb} = 25 °C				
		f = 1.5 GHz	-	0.9	-	dB
		f = 1.8 GHz	-	0.95	-	dB
		f = 2.4 GHz	-	1.1	-	dB
		f = 5.8 GHz	-	1.7	-	dB
3 _{ass}	associated gain	I_C = 2 mA; V_{CE} = 2 V; Γ_S = Γ_{opt} ; T_{amb} = 25 °C				
		f = 1.5 GHz	-	23.5	-	dB
		f = 1.8 GHz	-	23	-	dB
		f = 2.4 GHz	-	20.5	-	dB
		f = 5.8 GHz	-	13.5	-	dB
L(1dB)	output power at 1 dB gain compression	$I_C = 10$ mA; $V_{CE} = 1.5$ V; $Z_S = Z_L = 50 \Omega$; $T_{amb} = 25$ °C				
		f = 1.5 GHz	-	3.5	-	dBm
		f = 1.8 GHz	-	3	-	dBm
		f = 2.4 GHz	-	3	-	dBm
		f = 5.8 GHz	-	3	-	dBm

Table 7. Characteristics ...continued

 $T_i = 25$ °C unless otherwise specified

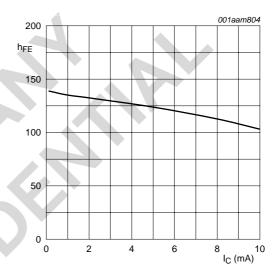
NXP Semiconductors Table 7. Characteristics continued		NPN wi	ideband si	7	U6′	,	7
$I_j = 25 \text{ °C}$ Symbol	Sunless otherwise specified Parameter	Conditions	Min	Тур	Max	Unit	Op
IP3	third-order intercept point	I_C = 10 mA; V_{CE} = 1.5 V; Z_S = Z_L = 50 Ω; T_{amb} = 25 °C				1/2/0/	, 4V
		f = 1.5 GHz	-	14.5	-	dBm	8/2
		f = 1.8 GHz	-	15	-	dBm	P
		f = 2.4 GHz	-	15	-	dBm	,
		f = 5.8 GHz	-	18	-	dBm	

[1] $G_{p(max)}$ is the maximum power gain, if K > 1. If K < 1 then $G_{p(max)} = MSG$.



- (1) $I_B = 60 \mu A$
- (2) $I_B = 50 \mu A$
- (3) $I_B = 40 \mu A$
- (4) $I_B = 30 \mu A$
- (5) $I_B = 20 \mu A$
- (6) $I_B = 10 \mu A$

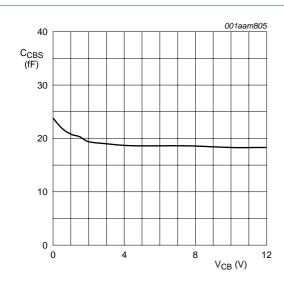
Fig 2. Collector current as a function of collector-emitter voltage; typical values



 V_{CE} = 2 V; T_{amb} = 25 °C.

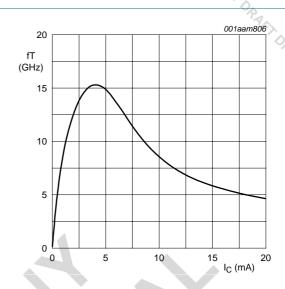
Fig 3. DC current gain as a function of collector current; typical values

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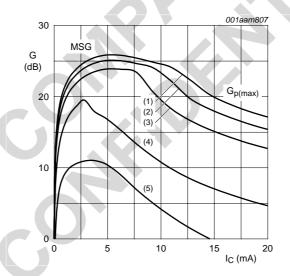
f = 1 MHz, $T_{amb} = 25$ °C.

Fig 4. Collector-base capacitance as a function of collector-base voltage; typical values



 $V_{CE} = 2 \text{ V}$; f = 2 GHz; $T_{amb} = 25 \text{ °C}$.

Fig 5. Transition frequency as a function of collector current; typical values

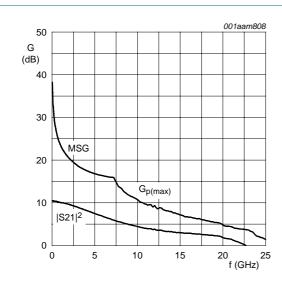


 $V_{CE} = 2 \text{ V}; T_{amb} = 25 \text{ }^{\circ}\text{C}.$

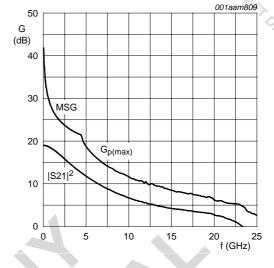
- (1) f = 1.5 GHz
- (2) f = 1.8 GHz
- (3) f = 2.4 GHz
- (4) f = 5.8 GHz
- (5) f = 12 GHz

Fig 6. Gain as a function of collector current; typical value

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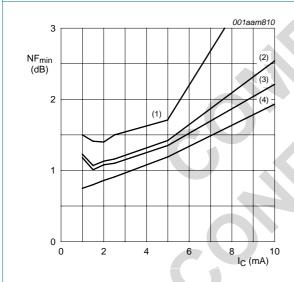
 V_{CE} = 2 V; I_{C} = 1 mA; T_{amb} = 25 °C.



 V_{CE} = 2 V; I_{C} = 5 mA; T_{amb} = 25 °C.

Fig 7. Gain as a function of frequency; typical values

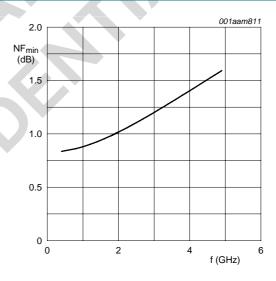




 $V_{CE} = 2 \text{ V}; T_{amb} = 25 \text{ °C}.$

- (1) f = 5.8 GHz
- (2) f = 2.4 GHz
- (3) f = 1.8 GHz
- (4) f = 1.5 GHz

Fig 9. Minimum noise figure as a function of collector current; typical values



 V_{CE} = 2 V; I_{C} = 2 mA; T_{amb} = 25 °C.

Fig 10. Minimum noise figure as a function of frequency; typical values

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8. Package outline

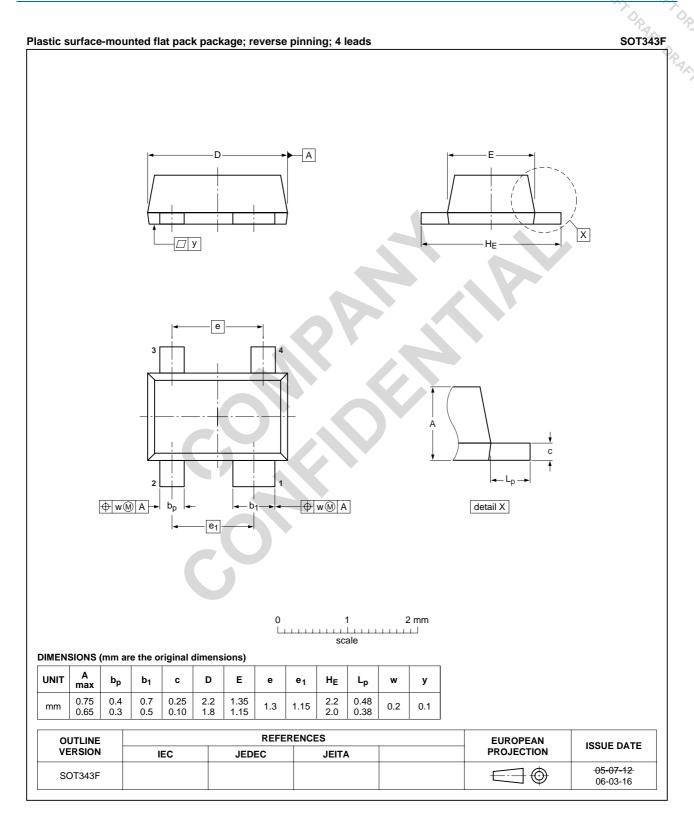


Fig 11. Package outline SOT343F



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9. Abbreviations

Table 8. Abbreviations

	7 to 5 to 1	
Acronym	Description	
AMR	Automatic Meter Reading	77
DBS	Direct Broadcast Satellite	
DC	Direct Current	
DRO	Dielectric Resonator Oscillator	
FM	Frequency Modulation	
GPS	Global Positioning System	
Ka	Kurtz above	
LNA	Low Noise Amplifier	
LNB	Low Noise Block	
LTE	Long Term Evolution	
NPN	Negative-Positive-Negative	
RF	Radio Frequency	
RKE	Remote Keyless Entry	
UMTS	Univeral Mobile Telecommunications System	

10. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BFU610F v.1	<tbd></tbd>	Product data sheet	-	-

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11. Legal information

11.1 Data sheet status

NXP Semiconduc	tors	BFU610F
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11. Legal infor	mation	ANT ORAN ORAN ORAN
11.1 Data sheet	status	DRAKT DRAKT D
Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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