

BFQ18A

NPN 4 GHz wideband transistor

Rev. 03 — 28 September 2007

Product data sheet

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NXP Semiconductors Product specification

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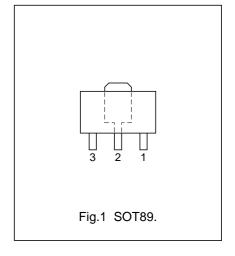
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DESCRIPTION

NPN transistor in a plastic SOT89 envelope intended for application in thick and thin-film circuits. It is primarily intended for MATV purposes.

PINNING

PIN DESCRIPTION				
	Code: FF			
1	1 emitter			
2	collector			
3	base			



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	25	٧
V _{CEO}	collector-emitter voltage	open base	_	18	V
Ic	DC collector current		_	150	mA
P _{tot}	total power dissipation	up to T _s = 155 °C (note 1)	_	1	W
f _T	transition frequency	$I_C = 100 \text{ mA}; V_{CE} = 10 \text{ V}; f = 500 \text{ MHz};$ $T_j = 25 \text{ °C}$	4	_	GHz
C _{re}	feedback capacitance	I _C = 0; V _{CE} = 10 V; f = 10.7 MHz	1.2	_	pF
d _{im}	intermodulation distortion	I_C = 80 mA; V_{CE} = 10 V; R_L = 75 Ω; V_o = 700 mV; measured at $f_{(p+q-r)}$ = 793.25 MHz	_	-60	dB

LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	-	25	V
V _{CEO}	collector-emitter voltage	open base	-	18	V
V _{EBO}	emitter-base voltage	open collector	-	2	V
Ic	DC collector current		-	150	mA
P _{tot}	total power dissipation	up to T _s = 155 °C (note 1)	-	1	W
T _{stg}	storage temperature		-65	150	°C
Tj	junction temperature		-	175	°C

Note

1. T_s is the temperature at the soldering point of the collector tab.

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THERMAL RESISTANCE

SYMBOL	PARAMETER	CONDITIONS	THERMAL RESISTANCE
R _{th j-s}	thermal resistance from junction to soldering point	up to $T_s = 155$ °C (note 1)	20 K/W

Note

1. T_s is the temperature at the soldering point of the collector tab.

CHARACTERISTICS

 $T_i = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	UNIT
h _{FE}	DC current gain	I _C = 100 mA; V _{CE} = 10 V	25	_	
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = 10 \text{ V}$; $f = 1 \text{ MHz}$	_	2	pF
Ce	emitter capacitance	$I_C = I_c = 0$; $V_{EB} = 0.5 \text{ V}$; $f = 1 \text{ MHz}$	_	11	pF
C _{re}	feedback capacitance	I _C = 0; V _{CE} = 10 V; f = 10.7 MHz	_	1.2	pF
f _T	transition frequency	I _C = 100 mA; V _{CE} = 10 V; f = 500 MHz	_	4	GHz
d _{im}	intermodulation distortion (see Fig.2)	note 1	_	-60	dB

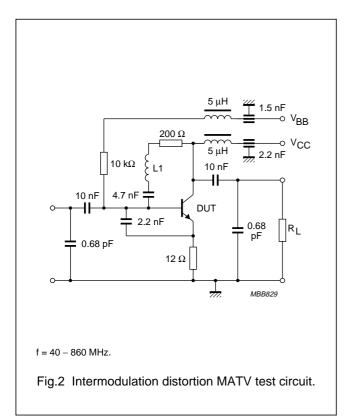
Note

1. I_c = 80 mA; V_{CE} = 10 V; R_L = 75 Ω ; V_p = V_o = 700 mV; f_p =795.25 MHz; V_q = V_o -6 dB; f_q = 803.25 MHz; V_r = V_o -6 dB; f_r = 805.25 MHz; measured at $f_{(p+q-r)}$ = 793.25 MHz.

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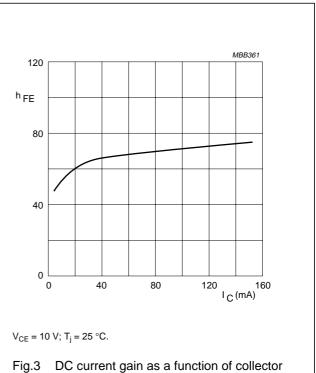
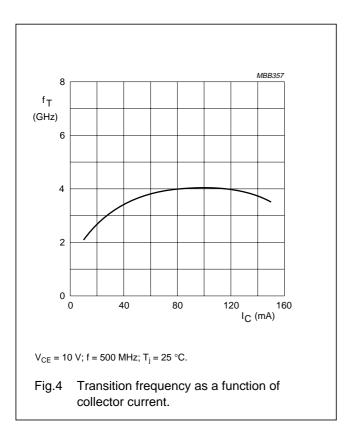


Fig.3 DC current gain as a function of collector current.



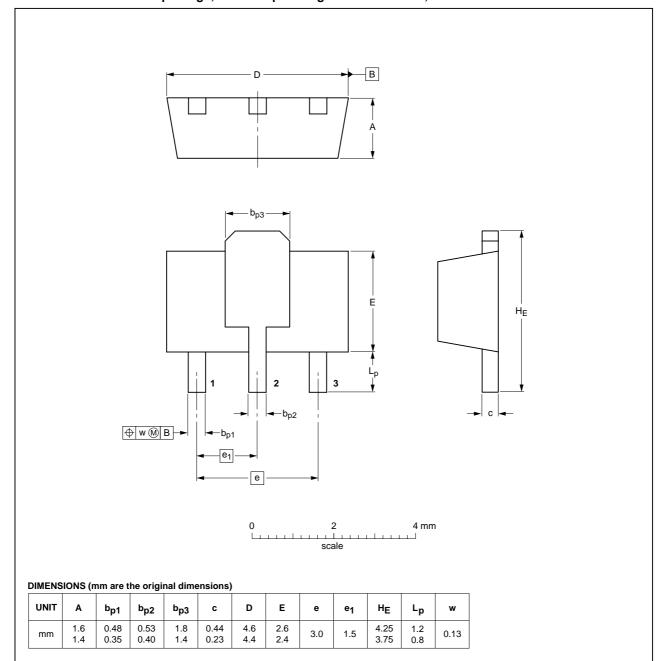
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PACKAGE OUTLINE

Plastic surface-mounted package; collector pad for good heat transfer; 3 leads

SOT89



OUTLINE		REFERENCES EUROPEAN IS			ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT89		TO-243	SC-62			06-03-16 06-08-29	



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Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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Revision history

Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BFQ18A_N_3	20070928	Product data sheet	-	BFQ18A_CNV_2
Modifications:	Fig. 1 and p	ackage outline updated		
BFQ18A_CNV_2	19950901	Product specification	-	-

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