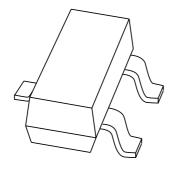
DISCRETE SEMICONDUCTORS

DATA SHEET



BAP50-05General purpose PIN diode

Product specification
Supersedes data of 1999 Feb 01

1999 May 10





General purpose PIN diode

BAP50-05

FEATURES

- Two elements in common cathode configuration in a small-sized plastic SMD package
- Low diode capacitance
- Low diode forward resistance.

APPLICATIONS

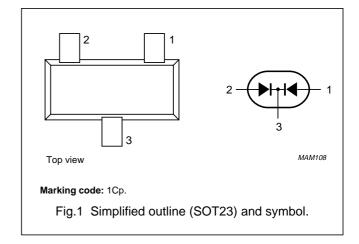
· General RF applications.

DESCRIPTION

Two planar PIN diodes in common cathode configuration in a SOT23 small plastic SMD package.

PINNING

PIN	DESCRIPTION
1	anode
2	anode
3	common cathode



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per diode					
V _R	continuous reverse voltage		_	50	V
I _F	continuous forward current		_	50	mA
P _{tot}	total power dissipation	T _s = 90 °C	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-65	+150	°C

Philips Semiconductors Product specification

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ELECTRICAL CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Per diode			•	•		
V _F	forward voltage	I _F = 50 mA	_	0.95	1.1	V
V _R	reverse voltage	I _R = 10 μA	50	_	_	V
I _R	reverse current	V _R = 50 V	_	_	100	nA
C _d	diode capacitance	V _R = 0; f = 1 MHz	_	0.45	_	pF
		V _R = 1 V; f = 1 MHz	_	0.35	0.6	pF
		V _R = 5 V; f = 1 MHz	_	0.3	0.5	pF
r _D	diode forward resistance	I _F = 0.5 mA; f = 100 MHz; note 1	_	25	40	Ω
		I _F = 1 mA; f = 100 MHz; note 1	_	14	25	Ω
		I _F = 10 mA; f = 100 MHz; note 1	_	3	5	Ω

Note

1. Guaranteed on AQL basis: inspection level S4, AQL 1.0.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R _{th j-s}	thermal resistance from junction to soldering point		K/W

General purpose PIN diode

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GRAPHICAL DATA

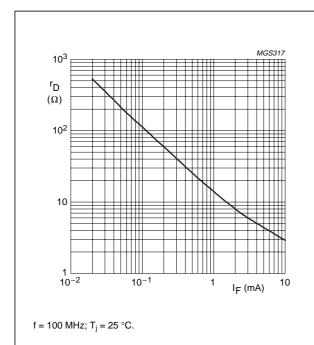
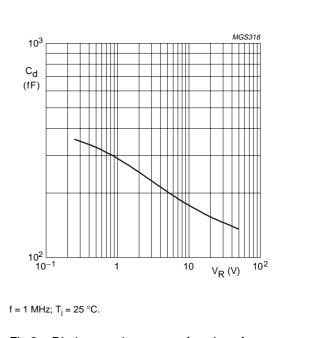
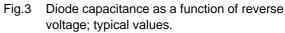


Fig.2 Forward resistance as a function of the forward current; typical values.





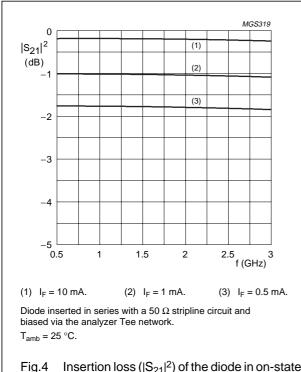
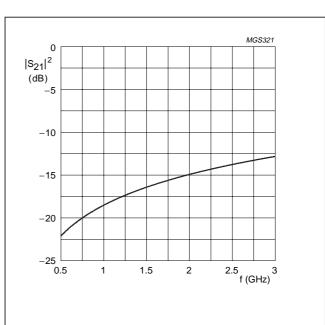


Fig.4 Insertion loss ($|S_{21}|^2$) of the diode in on-state as a function of frequency; typical values.



Diode zero biased and inserted in series with a 50 Ω stripline circuit. T_{amb} = 25 $^{\circ}C.$

Fig.5 Isolation ($|S_{21}|^2$) of the diode in off-state as a function of frequency; typical values.

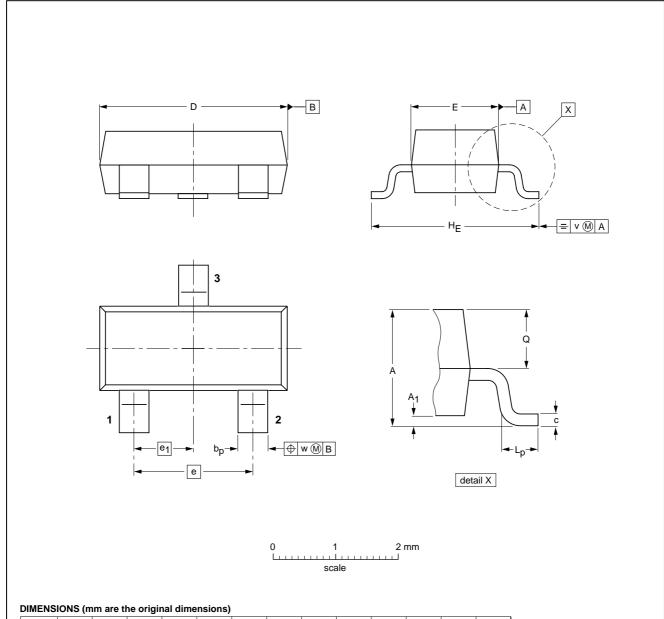
General purpose PIN diode

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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



UNIT	Α	A ₁ max.	bp	С	D	E	е	e ₁	HE	Lp	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE	
SOT23					97-02-28	

Philips Semiconductors Product specification

General purpose PIN diode

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DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

Philips Semiconductors Product specification

General purpose PIN diode

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