North South University
Department of Electrical and Computer Engineering

EEE111L - Analog Electronics-I Lab
Lab Outline

Instructor:
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Student consultation hour/s:
Saturday: 12:30pm - 02:30pm

Office room: LIB600(C1)
*For extra consultation hour, make an appointment through text or email.

Tentative Class Plan:

Week	Topic	Assessment
1	**Introduction to Hardware Lab Apparatus**	
2,3	**I-V Characteristics of diode.**	
4,5	**Diode rectifier circuits.**	
6,7	**Clipper and Clamper circuits.**	**Quiz-1 (week-6)**
8,9	**Zener Diode applications.**	
10,11	**The output characteristics of CE-BJT.**	**Midterm (week-10)**
12,13	**The BJT Biasing Circuits.**	
14,15	**Study of Switching Characteristics**	**Quiz-2 (week-14)**
16		**Setup Test, Viva, Final**

Tentative Marking Rubric:

Category	Marks (in %)
Attendance	10
Class Performance	5
Lab Report (groupwise) & Simulation (Individual)	20
Quiz (Written)	15
Midterm Exam (Written)	20
Final Exam (Written)	20
Setup-test (Individual)	10
Total	100

Lab Report Marks Distribution (20 Points):

- **Cover Page-0.5**
- **Experiment Name-0.5**
- **Objective-0.5**
- **Apparatus-0.5**
- **Theory-1**
- **Circuit Diagram-1**
- **Experimental Procedure-1**
- **Results-2**
- **Question/Answer-4**
- **Discussion-3**
- **Data Sheet-1**
- **Simulation-5**

*Check the file- 'Lab Report Instructions' for further details.