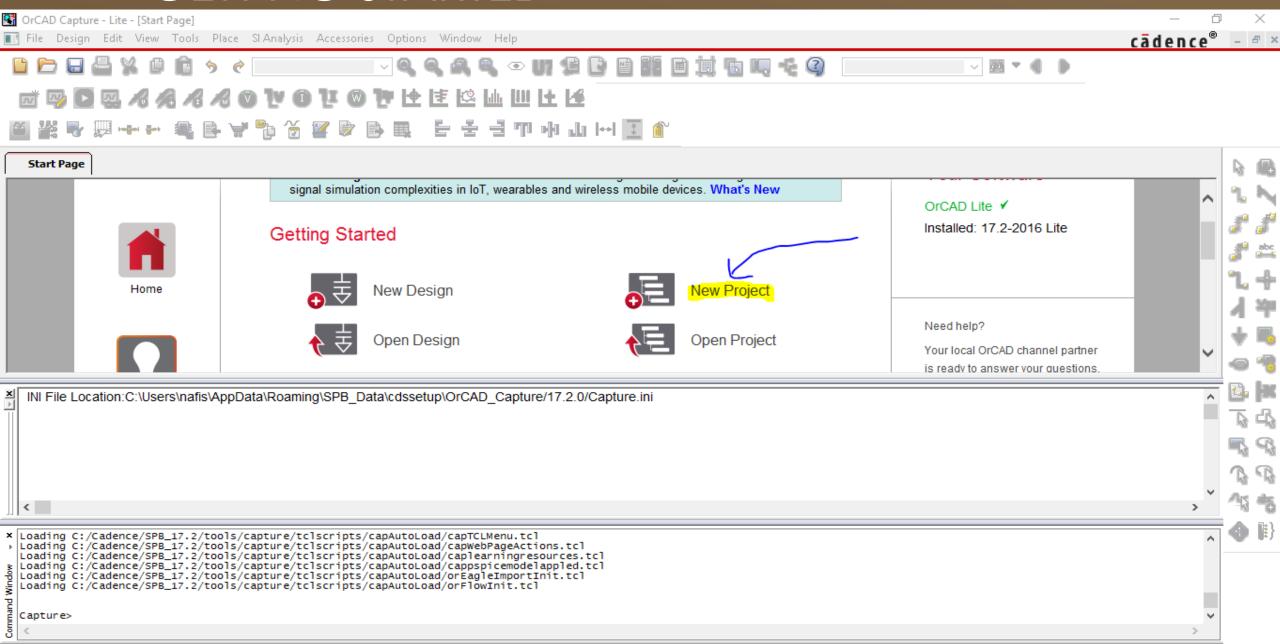
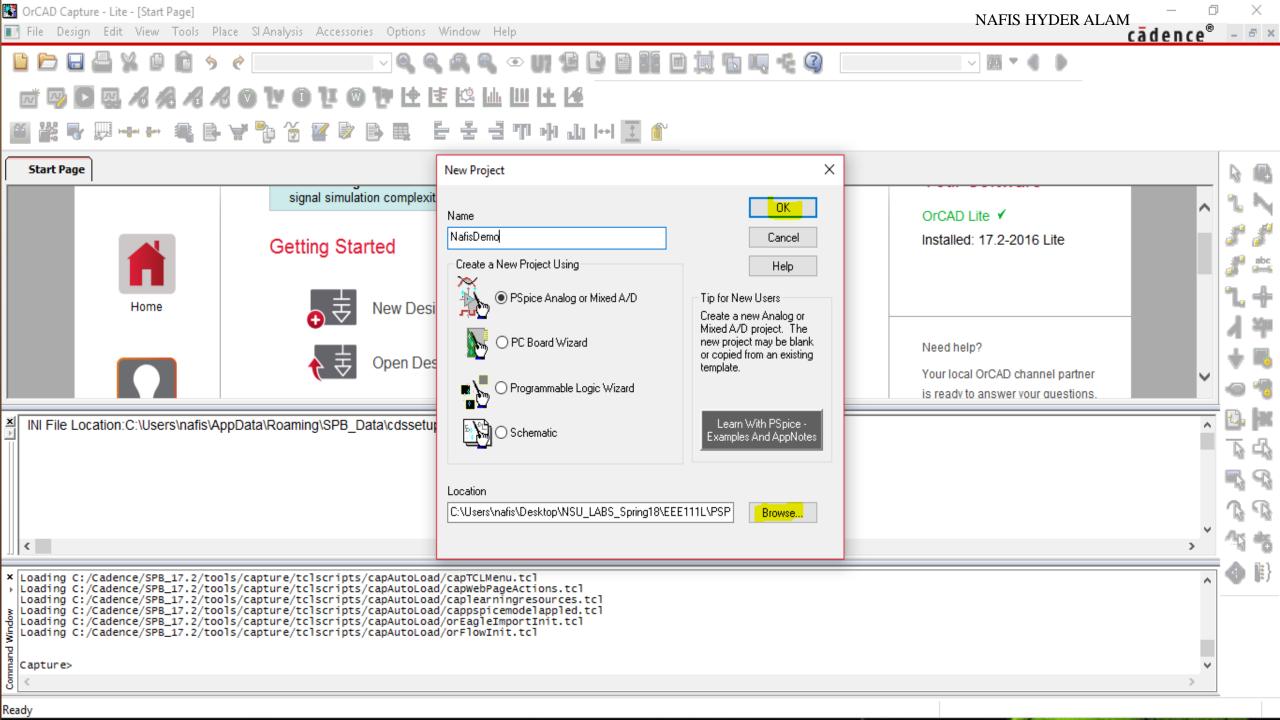
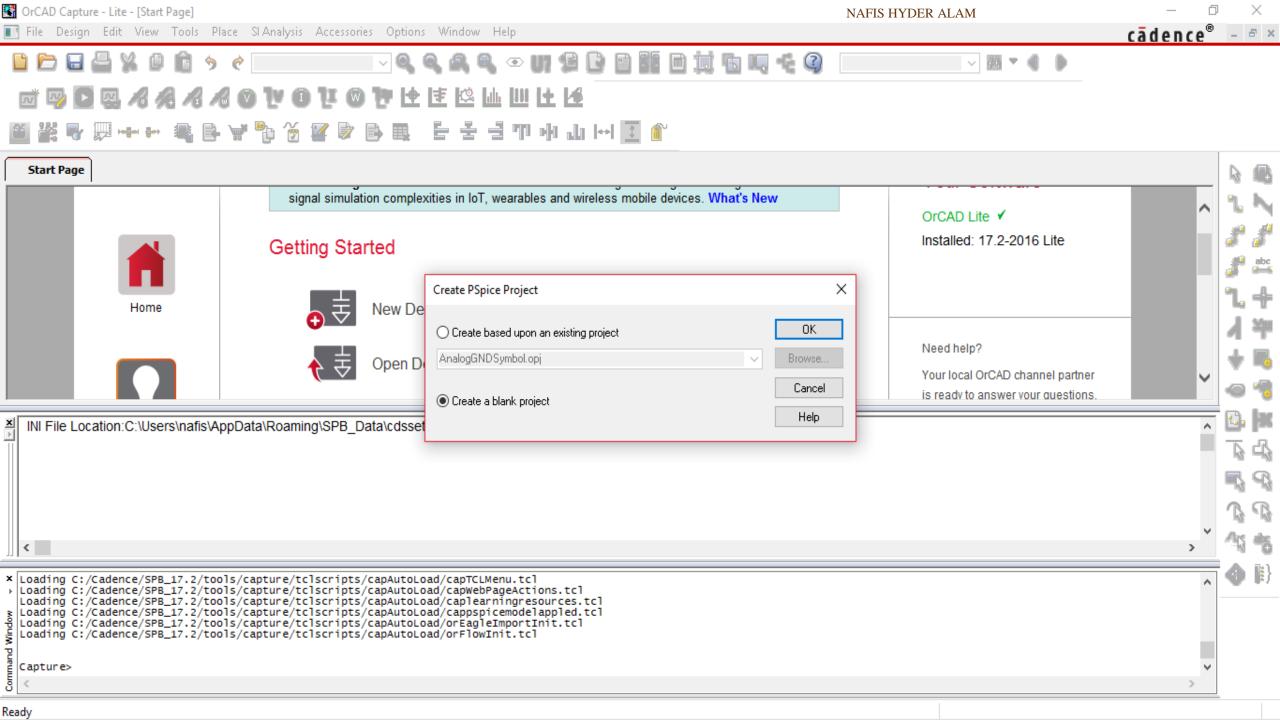
ORCAD CAPTURE LITE, 17.2

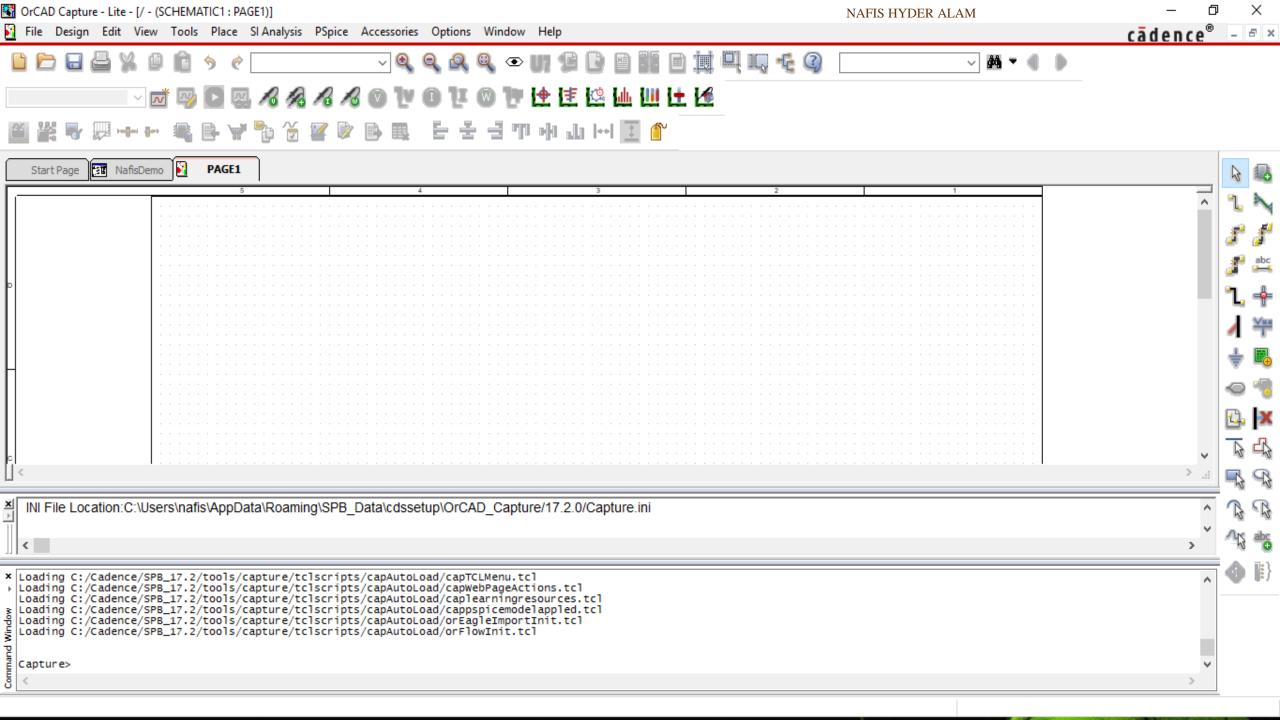
Prepared BY: Nafis Hyder Alam

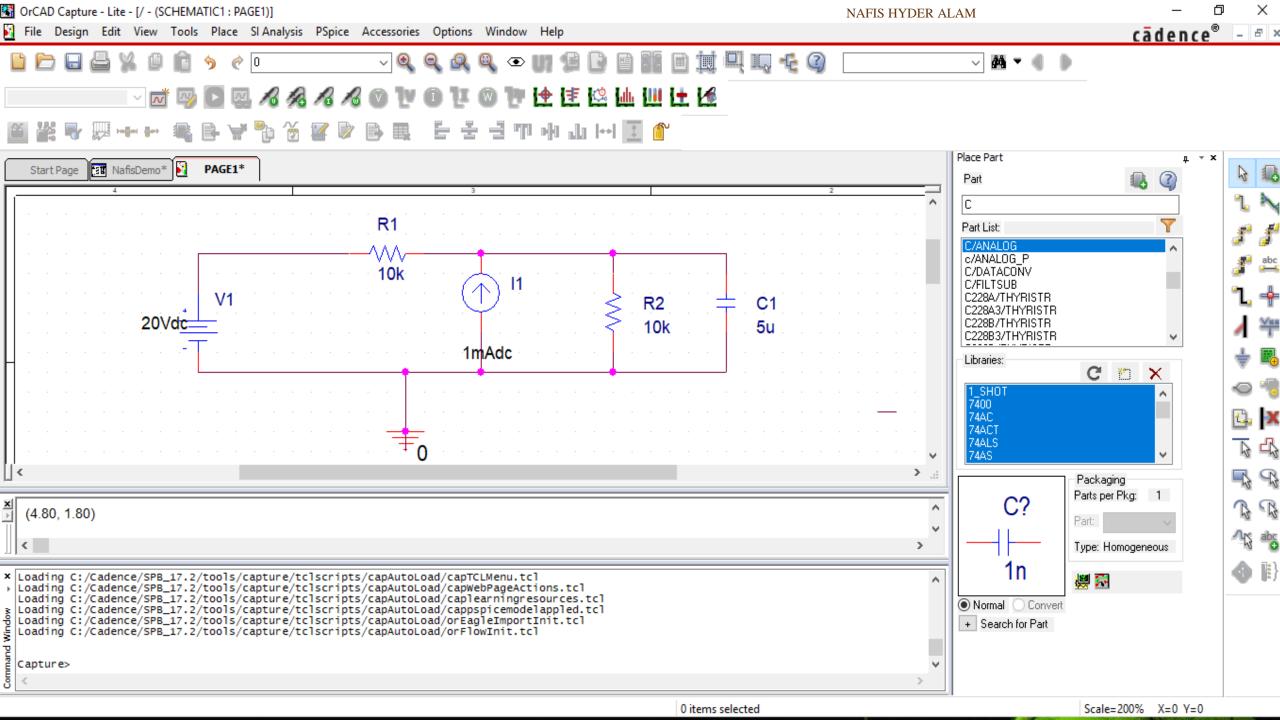
GETTING STARTED







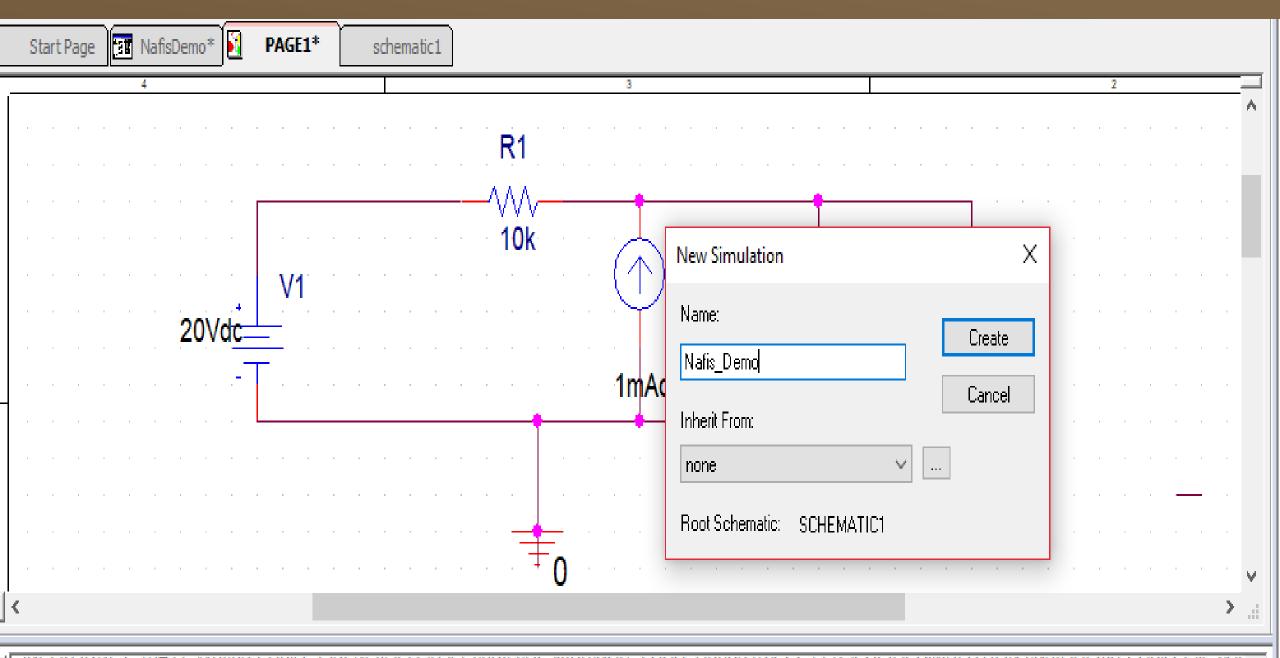


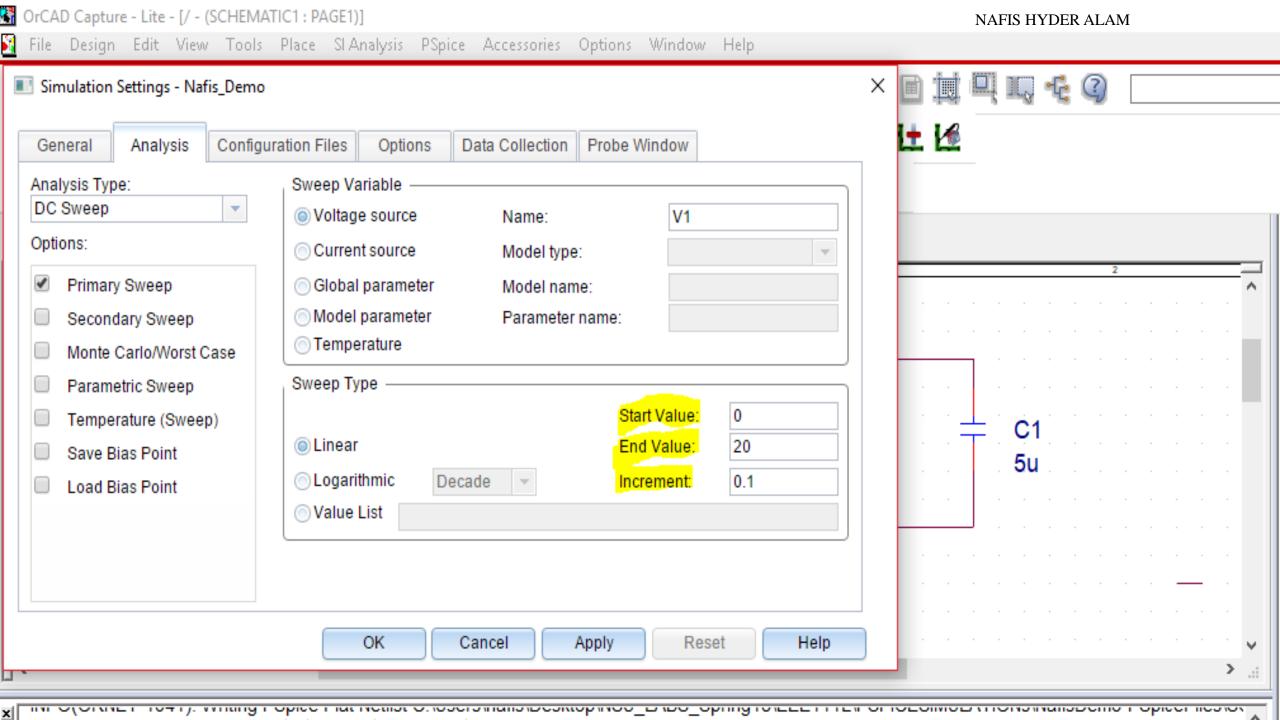


<u>cādence</u>® File Design Edit View Tools Place SI Analysis Accessories Options Window Help 를 볼 글 때 해 때 [4] 🗊 🧨 Place Part *NafisDemo Start Page PAGE1* schematic1 Part source NAFISDEMO R R1 N00147 N00154 10k TC=0,0 : R R2 0 N00154 10k TC=0,0 Part List: 1: V V1 N00147 0 20Vdc 5: I II 0 N00154 DC 1mAdc c/ANALOG P 6: C Cl C/DATACONV 0 N00154 5u TC=0,0 C/FILTSUB C228A/THYRISTR C228A3/THYRISTR C228B/THYRISTR C228B3/THYRISTR Libraries: C M < > Packaging Parts per Pkg: THE OCCUPANT TO THE PROPERTY IN THE PROPERTY AND ADDITIONAL PROPERTY OF THE PR INFO(ORNET-1156): PSpice netlist generation complete Type: Homogeneous Loading C:/Cadence/SPB_17.2/tools/capture/tclscripts/capAutoLoad/capTCLMenu.tcl Loading C:/Cadence/SPB_17.2/tools/capture/tclscripts/capAutoLoad/capWebPageActions.tcl Loading C:/Cadence/SPB_17.2/tools/capture/tclscripts/capAutoLoad/caplearningresources.tcl 🚇 🖼 Normal Convert Loading C:/Cadence/SPB_17.2/tools/capture/tclscripts/capAutoLoad/cappspicemodelappled.tcl Loading C:/Cadence/SPB_17.2/tools/capture/tclscripts/capAutoLoad/orEagleImportInit.tcl Loading C:/Cadence/SPB_17.2/tools/capture/tclscripts/capAutoLoad/orFlowInit.tcl + Search for Part

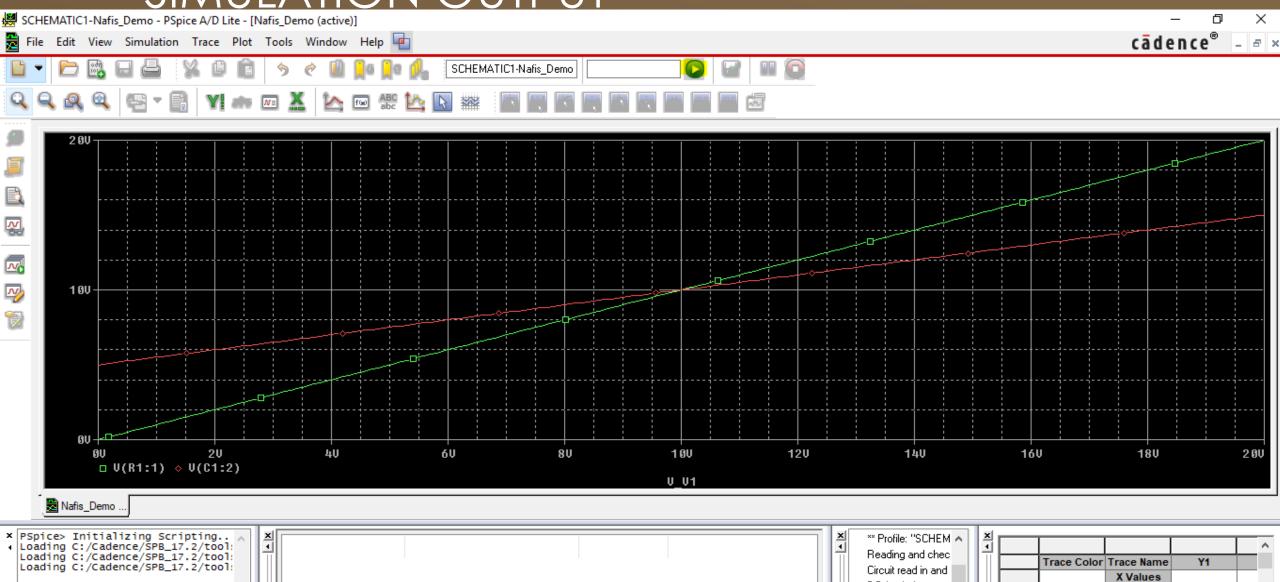
Capture>

NEW SIMULATION PROFILE





SIMULATION OUTPUT





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