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1. b) A silicon pn junction at  $T = 300\text{K}$  has the following parameters:  $N_A = 2 \times 10^{15} \text{ cm}^{-3}$ ,  $N_D = 5 \times 10^{16} \text{ cm}^{-3}$ ,  $D_n = 26 \text{ cm}^2/\text{s}$ ,  $D_p = 16 \text{ cm}^2/\text{s}$ ,  $\tau_{n0} = 8.2 \times 10^{-7} \text{ s}$ ,  $\tau_{p0} = 3.4 \times 10^{-7} \text{ s}$ . The cross sectional area is  $A = 3.2 \times 10^{-3} \text{ cm}^2$  and the forward bias voltage is  $V_a = 0.6 \text{ V}$ . Calculate the total current  $I_{\text{diode}}(\text{A})$  in the p-n junction diode. Value of  $n_i$  at  $T = 300\text{K}$  is  $1.5 \times 10^{10} \text{ cm}^{-3}$ .

Solve:

We know,

$$\begin{aligned} J_p(x_n) &= \frac{e D_p P_{n0}}{L_p} \left[ \exp\left(\frac{q V_a}{k T}\right) - 1 \right] \\ &= \frac{1.6 \times 10^{-19} \times 16 \times 4500}{3.2 \times 10^{-3}} \left[ e^{\frac{0.6}{0.0259}} - 1 \right] \\ &= 0.015 \text{ A/cm}^2 \end{aligned}$$

$$P_{n0} = \frac{n_i^2}{N_D} = \frac{(1.5 \times 10^{10})^2}{5 \times 10^{16}} = 4500 \text{ cm}^{-3}$$

$$\begin{aligned} L_p^2 &= D_p \times \tau_{p0} = 16 \times 3.4 \times 10^{-7} \\ &= 5.44 \times 10^{-6} \end{aligned}$$

$$\therefore L_p = 2.332 \times 10^{-3} \text{ cm}$$

$$\begin{aligned}
 J_n(-x_p) &= \frac{e D_n n_{p0}}{L_n} \left( \exp \frac{q V_A}{n T} - 1 \right) \\
 &= \frac{1.6 \times 10^{-19} \times 26 \times 112500}{2.97 \times 10^{-3}} \left( \exp \frac{0.625}{0.0259} - 1 \right) \\
 &= 1.57 \times 10^{-10} (1.11 \times 10^{10}) \\
 &= 1.744
 \end{aligned}$$

$$\begin{aligned}
 L_n^2 &= D_n \tau_{p0} = 26 \times 3.4 \times 10^{-7} \\
 &= 8.84 \times 10^{-6}
 \end{aligned}$$

$$\therefore L_n = 2.97 \times 10^{-3} \text{ cm}$$

$$n_{p0} = \frac{n_i^2}{N_A} = \frac{(1.5 \times 10^{10})^2}{2 \times 10^{15}} = 112500 \text{ cm}^3$$

$$\begin{aligned}
 J_{\text{diode}} &= (0.015 + 1.744) \\
 &= 1.759 \text{ A/cm}^2
 \end{aligned}$$

$$\begin{aligned}
 I &= I_{\text{diode}} \times A = 1.759 \times 3.2 \times 10^{-3} \\
 &= 5.628 \times 10^{-3} \text{ A.}
 \end{aligned}$$

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2. c) For a p-substrate silicon MOS capacitor, at  $T=300\text{K}$ , the substrate doping is  $N_A = 1.3 \times 10^{16}/\text{cm}^3$ .

i) What is bulk potential,  $\varphi_B$ ?

ii) At inversion condition what is  $\varphi_s$ ? (Hint:  $\varphi_s$  is total band bending at the surface)

iii) For  $\varphi_m = 4.35\text{eV}$  and  $\chi = 4.01\text{eV}$  (for silicon), what is the flatband voltage  $V_{FB} = \varphi_{ms}$ ?

iv) Find the maximum depletion width  $x_{dT}(\mu\text{m})$  at the inversion condition of the surface. Value of  $n_i$  at  $T=300\text{K}$  is  $1.5 \times 10^{10}\text{cm}^{-3}$ .

Useful equations:

$$x_{dT} = \left( \frac{2\epsilon_s \varphi_s}{q N_A} \right)^{1/2}$$

$$\epsilon_s = 11.7 \epsilon_0 \quad \varphi_B = \frac{KT}{q} \ln \left( \frac{N_A}{n_i} \right)$$

Solve:

$$\text{i) } \varphi_B = \frac{KT}{q} \ln \left( \frac{N_A}{n_i} \right)$$

$$= 0.0259 \ln \left( \frac{1.3 \times 10^{16}}{1.5 \times 10^{10}} \right)$$

$$= 0.354 \text{ V}$$

$$\text{ii) } \varphi_s = 2 \varphi_B$$

$$= 2 \times 0.354 \text{ V}$$

$$= 0.708 \text{ V}$$

$$N_A = 1.3 \times 10^{16}$$

$$n_i = 1.5 \times 10^{10}$$

$$\begin{aligned}
 \text{iii) } V_{FB} &= \frac{\phi_m - \phi_s}{q} \\
 &= \frac{(4.35 - 4.924) \text{ eV}}{1 \text{ e}} \\
 &= -0.574 \text{ V}
 \end{aligned}$$

Here,

$$\phi_m = 4.35 \text{ eV}$$

$$\begin{aligned}
 \phi_s &= \chi_{Si} + \frac{e\pi}{2} + \phi_0 \\
 &= 4.01 + \frac{1.12}{2} + 0.354 \\
 &= 4.924 \text{ eV}
 \end{aligned}$$

$$\begin{aligned}
 \text{iv) } x_{dT} &= \left( \frac{2 \epsilon_s \phi_s}{q N_A} \right)^{1/2} \\
 &= \left( \frac{2 \times 11.7 \times 8.854 \times 10^{-14} \times 0.708}{1.6 \times 10^{-19} \times 1.3 \times 10^{16}} \right)^{1/2} \\
 &= (7.067 \times 10^{-10})^{1/2} \text{ cm} \\
 &= 2.65 \times 10^{-5} \text{ cm} \\
 &= 0.265 \text{ } \mu\text{m}.
 \end{aligned}$$

Here,

$$\epsilon_s = 11.7 \times 8.854 \times 10^{-14}$$

$$\phi_s = 0.708 \text{ V}$$

$$N_A = 1.3 \times 10^{16} / \text{cm}^3$$

Ans.

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3. b) An ideal n-channel MOSFET has the following parameters  $V_T = 0.4 \text{ V}$ ,  $\mu_n = 740 \text{ cm}^2/\text{V}\cdot\text{s}$ ,  $t_{ox} = 10.9 \text{ nm}$ ,  $W = 9 \text{ }\mu\text{m}$ , and  $L = 0.9 \text{ }\mu\text{m}$ . Find the drain current  $I_{ds}$  for this MOSFET when

- i)  $V_{gs} = 0.2 \text{ V}$  and  $V_{ds} = 0.15 \text{ V}$ .
- ii)  $V_{gs} = 1.5 \text{ V}$  and  $V_{ds} = 0.4 \text{ V}$ .
- iii)  $V_{gs} = 1.8 \text{ V}$  and  $V_{ds} = 2.2 \text{ V}$ .

Make sure to apply the above bias conditions to determine cut off, linear case and saturation case for calculation of drain current

Useful equations:

$$I_{ds} = \mu_n C_{ox} \frac{W}{L} \left\{ (V_{gs} - V_T) V_{ds} - \frac{V_{ds}^2}{2} \right\}$$

$$I_{ds} = \mu_n C_{ox} \frac{W}{L} \frac{(V_{gs} - V_T)^2}{2}$$

Solution:

①  $V_{gs} < V_T$

So, channel is no cut off  $\therefore I_{ds} = 0 \text{ A}$

②  $V_{gs} - V_T = 1.5 - 0.4 = 1.1 \text{ V} > V_{ds} = 0.5 \text{ V}$

$\therefore$  Linear region of operation

$$I_{ds} = \mu_n C_{ox} \frac{W}{L} \left\{ (V_{gs} - V_T) V_{ds} - \frac{V_{ds}^2}{2} \right\}$$

$$= 740 \times 3.18 \times 10^{-7} \times \frac{9}{0.9} \times \left\{ (1.1)(0.4) - \frac{0.4^2}{2} \right\}$$

$$= 8.46 \times 10^{-4}$$

$$C_{ox} = \frac{\epsilon_{ox}}{t_{ox}}$$

$$= \frac{3.9 \times 8.85 \times 10^{-14}}{10.9 \times 10^{-7}}$$

$$= 3.18 \times 10^{-7} \text{ F}/\mu\text{m}^2$$

$$\textcircled{iii} \quad V_{gs} - V_T = 1.8 - 0.4$$

$$= 1.4 < V_{ds} = 2.2 \text{ V}$$

$\therefore$  Saturation case

$$I_{ds} = \mu_n C_{ox} \frac{W}{L} \frac{(V_{gs} - V_T)^2}{2}$$

$$= 740 \times 3.18 \times 10^{-7} \times \frac{9}{0.9} \times \frac{(1.4)^2}{2}$$

$$= 2.306 \times 10^{-3} \text{ A (saturation)}$$

Ans.



Last class.

For an n-channel enhancement mode silicon MOSFET at  $T = 300\text{ K}$ , doping  $N_A = 2 \times 10^{16}/\text{cm}^3$  and oxide thickness  $t_{ox} = 9\text{ nm}$ . Find the threshold voltage of the device. If  $L = 0.95\text{ }\mu\text{m}$  and  $W = 7\text{ }\mu\text{m}$ , mobility  $\mu_m = 900 \frac{\text{cm}^2}{\text{V}\cdot\text{s}}$ , what is the value of drain current  $I_{ds}$ . Assume metal gate with  $\phi_m = 4.22\text{ eV}$

①  $V_{gs} < V_T$

②  $V_{gs} = 0.8\text{ V}$  and  $V_{ds} = 0.2\text{ V}$

③  $V_{gs} = 1.8\text{ V}$  and  $V_{ds} = 2.2\text{ V}$

Solution:

$$V_T = V_{FB} + 2\phi_B + \sqrt{\frac{2q\epsilon_s 2\phi_B N_A}{C_{ox}}}$$

$$\begin{aligned}\phi_B &= \frac{kT}{2} \ln\left(\frac{N_A}{n_i}\right) \\ &= 0.0259 \ln\left(\frac{2 \times 10^{16}}{1.5 \times 10^{10}}\right) \\ &= 0.365\text{ V}\end{aligned}$$

$$\begin{aligned}V_{FB} &= \phi_m - \left(x_{Si} + \frac{E_G}{2} + \phi_B\right) \\ &= 4.22 - \left(4.01 + \frac{1.12}{2} + 0.365\right) \\ &= -0.715\text{ V}\end{aligned}$$

$$V_T = -0.715 + (2 \times 0.365) + \sqrt{\frac{2 \times 1.6 \times 10^{-19} \times 11.7 \times 8.854 \times 10^{-14} \times 2 \times 0.365 \times 2 \times 10^{16}}{3.84 \times 10^{-7}}}$$

$$= 0.196 \text{ V}$$

①  $V_x < 0.196 \text{ V}$

no cutoff. no.  $q_x$

$$I_{ds} = 0$$

$$\left\{ \begin{aligned} C_{ox} &= \frac{\epsilon_x}{t_{ox}} \\ &= \frac{3.9 \times 8.854 \times 10^{-14}}{9 \times 10^{-7}} \\ &= 3.84 \times 10^{-7} \text{ F/cm}^2 \end{aligned} \right.$$

②  $V_{gs} = 0.8 \text{ V} > V_T = 0.196 \text{ V}$

$$V_{gs} - V_T = 0.8 - 0.196$$

$$= 0.604 \text{ V}$$

$$V_{gs} - V_T > V_{ds} = 0.2$$

Linear on mode

$$I_{ds} = \mu_m C_{ox} \frac{W}{L} \left[ V_{gs} - V_T - \frac{V_{ds}}{2} \right] V_{ds}$$

$$= 900 \times 3.84 \times 10^{-7} \times \frac{7}{0.95} \times \left[ 0.8 - 0.196 - \frac{0.2}{2} \right] 0.2$$

$$= 2.37 \times 10^{-4} \text{ A}$$

$$= 0.237 \text{ mA}$$