



EEE141L/ETE141L

Lab 4: Delta-Wye Conversion

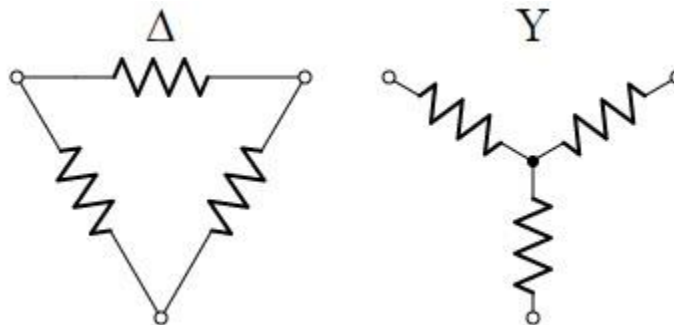
Objectives:

1. To perform Delta-Wye Conversion
2. To verify the results with measured data.
3. Solve a complex circuit using Delta-Wye Conversion.

The Delta-Wye transformation is an extra technique for transforming certain resistor combinations that cannot be handled by the series and parallel equations. This is also referred to as a Pi - T transformation

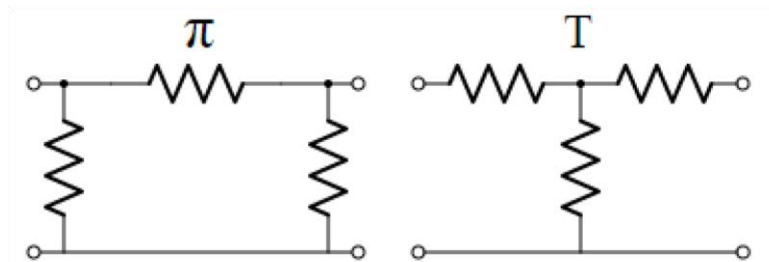
Sometimes when you are simplifying a resistor network, you get stuck. Some resistor networks cannot be simplified using the usual series and parallel combinations. This situation can often be handled by trying $\Delta - Y$ transformation, or 'Delta-Wye' transformation.

The names Delta and Wye come from the shape of the schematics, which resemble letters. The transformation allows you to replace three resistors in a Δ configuration by three resistors in a Y configuration, and the other way around.



The $\Delta - Y$ drawing style emphasizes these are 3-terminal configurations. Something to notice is the different number of nodes in the two configurations. Δ has three nodes, while Y has four nodes (one extra in the center).

The configurations can be redrawn to square up the resistors. This is called π -T configuration,



The π -T style is a more conventional drawing you would find in a typical schematic. The transformation equations developed next apply to π -T as well.



$\Delta \rightarrow Y$ transformation

Equations for transforming a Δ network into a Y network:

$$R1 = \frac{Rb Rc}{Ra + Rb + Rc}$$

$$R2 = \frac{Ra Rc}{Ra + Rb + Rc}$$

$$R3 = \frac{Ra Rb}{Ra + Rb + Rc}$$

Transforming from Δ to Y introduces one additional node.



$Y \rightarrow \Delta$ transformation

Equations for transforming a Y network into a Δ network:

$$R_a = \frac{R_1 R_2 + R_2 R_3 + R_3 R_1}{R_1}$$

$$R_b = \frac{R_1 R_2 + R_2 R_3 + R_3 R_1}{R_2}$$

$$R_c = \frac{R_1 R_2 + R_2 R_3 + R_3 R_1}{R_3}$$

Transforming from Y to Δ removes one node.



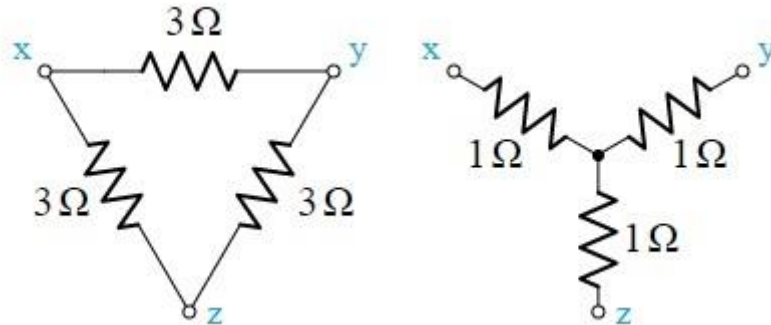
Example

Let's do a symmetric example. Assume we have a Δ circuit with $3\ \Omega$ resistors. Derive the Y equivalent by using the $\Delta \rightarrow Y$ equations.

$$R1 = \frac{Rb Rc}{Ra + Rb + Rc} = \frac{3 \cdot 3}{3 + 3 + 3} = 1\ \Omega$$

$$R2 = \frac{Ra Rc}{Ra + Rb + Rc} = \frac{3 \cdot 3}{3 + 3 + 3} = 1\ \Omega$$

$$R3 = \frac{Ra Rb}{Ra + Rb + Rc} = \frac{3 \cdot 3}{3 + 3 + 3} = 1\ \Omega$$





Going in the other direction, from $Y \rightarrow \Delta$, looks like this,

$$R_a = \frac{R_1 R_2 + R_2 R_3 + R_3 R_1}{R_1} = \frac{1 \cdot 1 + 1 \cdot 1 + 1 \cdot 1}{1} = 3 \Omega$$

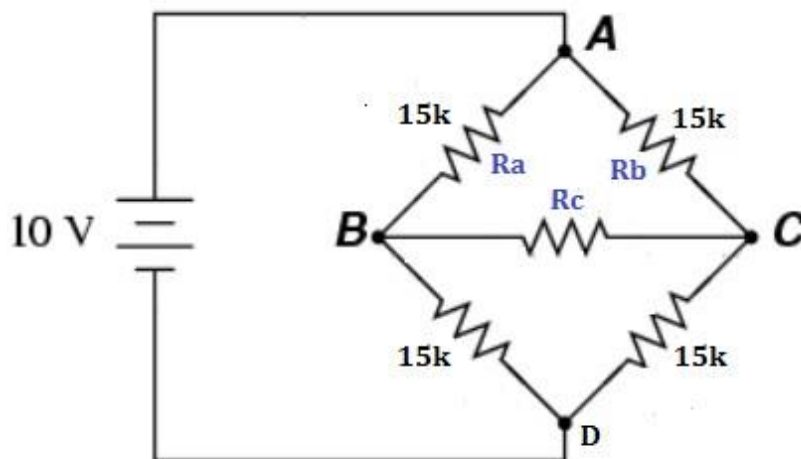
$$R_b = \frac{R_1 R_2 + R_2 R_3 + R_3 R_1}{R_2} = \frac{1 \cdot 1 + 1 \cdot 1 + 1 \cdot 1}{1} = 3 \Omega$$

$$R_c = \frac{R_1 R_2 + R_2 R_3 + R_3 R_1}{R_3} = \frac{1 \cdot 1 + 1 \cdot 1 + 1 \cdot 1}{1} = 3 \Omega$$

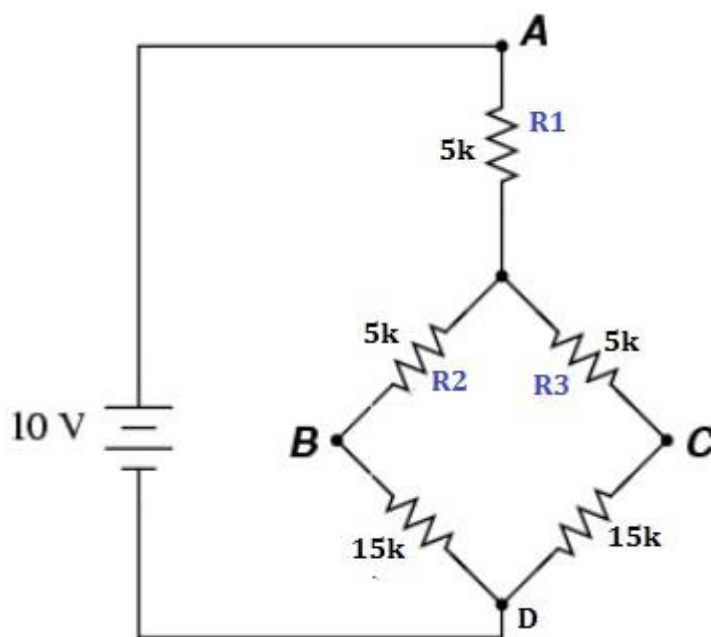
The transformed network and the original network has the same equivalent resistance (although the individual resistors between the nodes are different). The two networks will be electrically identical as measured from the three terminals (A, B, and C). i.e The node voltages of both the circuits would be equal.



Circuit Diagram:



Circuit 1



Circuit 2



List of Equipment

- Bread Board
- DMM
- 5 x 15k Ω resistor
- 3 x 5 k Ω resistor
- DC power source

Procedure

1. Measure the resistor values with DMM and note down in Table 1.
2. Setup the circuit as shown in the circuit 1
3. Measure the voltage V_{AD} , V_{BD} , V_{CD} (D is the reference node) and note down in Table 2
4. Measure the voltage V_{AB} , V_{BC} , V_{AC} and note down in Table 2
5. Setup Circuit 2.
6. Measure the voltage V_{AD} , V_{BD} , V_{CD} (D is the reference node) and note down in Table 2
7. Measure the voltage V_{AB} , V_{BC} , V_{AC} and note down in Table 2

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Data Collection for Lab 4:

Group No. _____

Instructor's Signature _____

Table 1:

Theoretical R	Measured R	% Error
15k		
5k		

Table 2:

Readings	Circuit 1	Circuit 2	% Error
V_{AD}			
V_{BD}			
V_{CD}			
V_{AB}			
V_{BC}			
V_{AC}			



Report:

1. The resistors in Circuit 1 are in series or in parallel combination?
2. What technique would you use to find the equivalent resistance?
3. Perform Delta-Wye conversion for ΔABC (*upper portion*) of circuit 1. Show all your steps to find the equivalent resistance R_1 , R_2 , R_3 from R_a , R_b , R_c .
1. Redraw the equivalent circuit after applying the Delta-Wye conversion for ΔABC . Is it same as circuit 2?
2. Calculate R_{eq} .
3. Calculate the voltage of R_1 , R_2 , R_3 .
4. Calculate V_{AB} , V_{BC} , V_{AC} and V_{AD} , V_{BD} , V_{CD} . Do your calculated values match the measured values for circuit 2? Find the % Error.
5. Using Table 2, analyze whether Circuit 2 is equivalent to Circuit 1? Was Delta-Wye conversion successful?