

North South University Department of Electrical & Computer Engineering

LAB REPORT

Course (Code:	EEE211
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Course Title: Digital Electronics

Section: 01

Experiment Number: 06

Experiment Name: Binary Arithmatic

Experiment Date: 14.12.2020

Date of Submission: 21.12.2020

Course Instructor: Fahimul Haque

Submitted To: Fatema Zahra

Experiment Names

BCD do seven segment decoder

Objectiven:

Learn the conversion & displaying value in a seven segment display.

Theory.

An ABCD to seven segment decoder is a combinational circuit that converts a decimal digit in BD to an appropriate code for the selection of segments in on indicator used to display decimal digit in a form.

each elemt of (a, b, c, d, e, f, g) of the seven segment display is durined on when a logic low is applied to it's corresponding input pin.

Equipments:

- Trainer levard.
- 10 7447, resistors, as
- Resisters
- Seven segment display.
- Wire

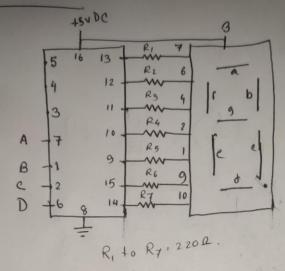
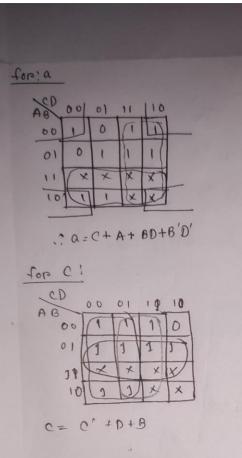
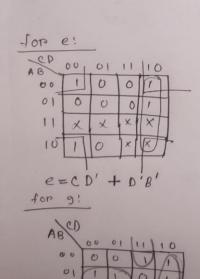
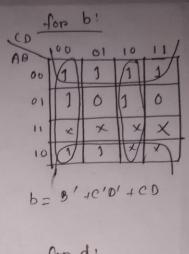


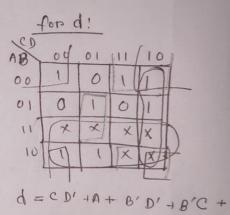
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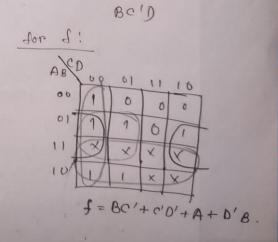
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Discussion:

Due to pandemic we are attending in online. lab session. Through software simulation we have completed this experiment. This simulation procedure helped us to be seen the use of 7 segment display using leasic gates & decoders. It helped us to relate the theoretical knowledge with our practical session.

Attachments:

