North South University
Electrical and Computer Engineering Department
QUIZ 2
Spring Semester-2021
Course: CSE231
Marks: 15, Time: 30 minutes

1. Derive a truth table for Full adder. Write the Boolean equation for full adder and minimize the equation and finally draw a full adder circuit.   **5*1=5 marks**

2. Simplify the Boolean function using K map

1. F (A, B, C, D)= $\sum (0,1,2,5,8,9,10$ **5*1 marks=5 marks**

3. Draw a 2 to four line decoder with enable input. Show the truth table and equation for the circuit if we want to use and gate to design it.   **5*1 marks=5 marks**