NORTH SOUTH UNIVERSITY

DEPARTMENT OF ELECTRICAL & COMPUTER ENGINEERING Summer 2022

EEE 141/ ETE 141 Lab | Electrical Circuit I Lab Course Outline

Course Faculty: Ms. Syeda Sarita Hassan (SSH1)

Lab Instructor: Naziza Mehzabin Hossain Email: naziza.hossain@northsouth.edu;

Mobile: 01993095099

Office Room: LIB600-C5

Office Hours: S (10:10 AM- 11:10 AM)

(2:40 PM - 4:10 PM)

T (11:00 PM - 3:00 PM)

R (10:10 AM- 11:10 AM)

(2:40 PM - 4:10 PM)

A (10:10 AM- 11:10 AM)

(2:40 PM - 3:40 PM)

An appointment must be made through email

Lab Section: 08

Lab Timing: S (11:20 AM – 2:30 PM)

Lab Room: SAC504

Lab Outline

Lab 1	Verification of Ohm's Law, KVL, VDR using Series Circuit
Lab 2	KCL, Parallel Networks & Ladder Circuit
Lab 3	Ladder Circuit and Loading Effect of Voltage Divider Circuit
Lab 4	Delta-Wye conversion techniques
Lab 5	Superposition Theorem
Lab 6	Thevenin and Maximum power transfer theorem
Lab 7	RC network

Marks Distribution (Tentative)

Attendance	10 %
Lab Performance	10 %
Lab Report (Group-wise)	20 %
Mid term	20 %
Final Setup Exam	20 %
Final Written	20 %
Total	100 %

Software: NI MultiSim

RULES:

- Do not talk during class.
- If you are inattentive and miss your attendance in the process, you will be marked as absent.
- If any lab is missed, one will get 0% in Attendance and Lab performance.
- Use of mobile phones during the offline class is strictly prohibited.
- Lab Report copied from others will be marked negatively.
- You must bring printed lab manuals to the class.
- Marks will be deducted for late submissions.
- All information in the cover page must be typed; <u>handwritten cover pages will be rejected.</u>
- All submissions are due at the beginning of the class.
- Ensure that your group prints all necessary materials well ahead of the class.
- Grading will be done according to NSU Grading Policy.