

North South University Department of Electrical & Computer Engineering

LAB REPORT

Course Name: EEE141 Lab

Lab No: 03

Experiment Name: Loading Effect of Voltage Divider Circuit

Faculty: SSH1

Experiment Date: 03-07-22

Report Submission Date: 17-07-22

Section: 08

Group: 04

Name and ID:

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Remarks:

Score

Experiment Name: Loading Effect of Voltage Divider Cineuit.

Objective: (1) Analyzing how the voltag divident cincuit behaves when there is no load nesistance connected.

10 Evaluating the performance of voltage divider cincult due to loading.

List of Equipment:

- (1) Trainer Board
- (11) DMM
- (III) 2×560s2 resistors
- (IV) 1x (0-1052) variable nesistan

Theory: Voltage divider eincuit gives a basic method for switching a De voltage over completely to another lower De voltage.

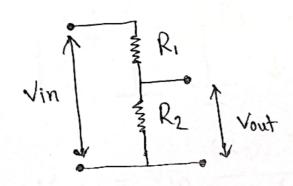
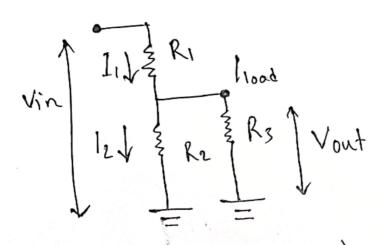


Figure 1: A voltage divider

when a load nesistane Rz is connected across the output terminals of the voltage divider is said to be loaded. In Figure 1, the is no, load (RL) connected in parallel to Rz hence we call it a No-load cincuit. According to voltage divider rule

Vout = Vin R1+R2

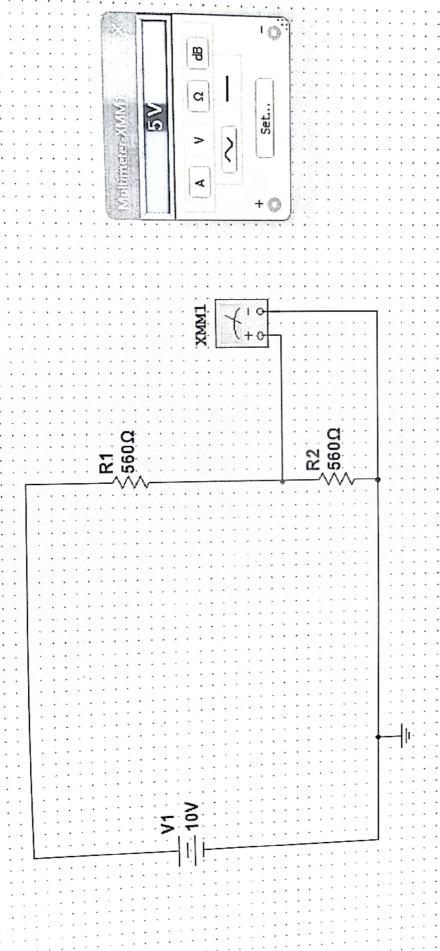
Now we connect an output load, R3 in panallel to R2



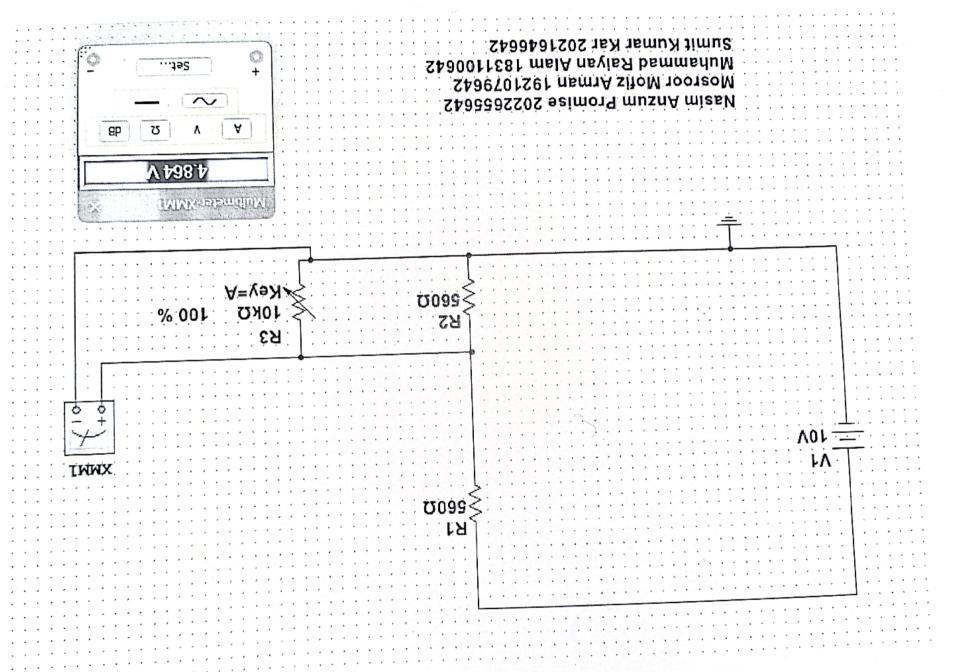
So, Vout = Vin
$$\frac{(R_2/|R_3)}{R_1 + (R_2/|R_3)}$$

The loading of voltage divider has tollowing effects

- (i) the output voltage deeneases depending upon the value of load resiston RL.
- (1) After connected the load nesistan, the voltage divider cineuit turned into a series -parallel cineuit. Therefore the total nesistance of the cincuit is reduced.
- (11) The cincuit europent inencases because the total nesistance of the cincuit is decreased.



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Department of Electrical & Computer Engineering

EEE141/ETE141

Data Collection for Lab 3:

Group No. 04

Instructor's Signature

Table 1.

RL	Vout (Measured)	Vout (Calculated)	%Error
No resistor	5.00V	5.00V	00/0
1k	3.94V	3.90V	1.02 %
4k	4.631	4.67 V	0.86%
7k	4.73 V	4.801	1.47.10
10k	4.800	9.86V	1.25 %

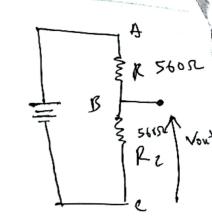
Report Question:

1. Explain the loading effect of your circuit (i.e. explain how does your Vout vary with increasing Load resistor)

2. Showing all steps in details, theoretically calculate the value of Vout for each load resistor.

3. Comparing the theoretical data to the experimental data, comment how far the loading effect of your circuit supports.

Data and Table:



Vout (IK)

$$Vout = V \times \frac{RBCe9}{Re9} = 10 \times \frac{358.95}{918.97} = 3.90V$$

Vout (4K)

Nout (7K)

$$\sqrt{604} = 10 \times \frac{1000.30}{530.30} = 4.86 \vee$$

Questions and Answer:

1. In our cincuit without any load the Vout is 5.00V. Now increasing the load bed nestston we can observe that the Vout almost neaches no load Vout. The mone we increase the load nesiston the closer it neaches no load state. But Vout on load will never be equal to Vout on no load.

2. Vout (1K)

$$R_{BCeq} = \frac{560 \times 1000}{560 + 1000} = 358.97\%$$

$$R_{eq} = R_{AB} + R_{BCeq} = 560\% + 358.97\%$$

$$= 018.97\%$$

$$Vout = V \times \frac{R_{BCeq}}{R_{eq}} = 10 \times \frac{358.95}{918.97} = 3.90\%$$

Vout (4K)

$$RBCeq = \frac{560 \times 4000}{560 + 4000} = 491.22 = 1051.22$$

$$Req = RAB + RBeeq = 560 + 491.22 = 1051.22$$

$$Vout = V \times \frac{RBeeq}{Req} = 10 \times \frac{1051.22}{1051.22} = 4.67V$$

Vout (7K)

Reg = 560 + 518.51 = 1078.50

$$Vout = 10 \times \frac{1078.5}{518.21} = 4.80 \times$$

Vout (104)

Req = 560 + 530.30sc = 1090.30sc

3. The theonetical data and experimental data are almost same. The more we increase the load nesistan the closer it neaches no load state. But vont on bad heaches no load state. But vont on bad will never be equal to vout on no load.

Result analysis and Discussion:

In this lab we learned about Loading effect of voltage divider cincuit.

In the equipment, we were provided two 56052 nesistons and one (0-10km) variable nesiston. We were also provided with a Digital Multimeter (DMM) to measure the cincuit voltage of the neg eincutt registon: First we took the negistors and completed the circuit in breadboard. First we measured Vout without variable negiston. After that we connected our vantable negiston and measured value at 1K, 4K, 7K and 10K ohm and wrote the values in the table. Then we calculated Vout with theorytical knowledge and also calculated % ennon and wrote in the table. The measured values and theoryti. themitical values are almost same.

Table of Contributions:

Nasim Anzum Promise 2022655642

- Vout (Measured), Vout Calculation,
ofo Error and lab reports

Mosnoon Mofiz Anman 1921079642

— Cincuit baild and Value check

Sumit Kumar Kar 2021646642

Value check and cincuit build

— Value check and cincuit build

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