

Fall 2021  
EEE/ETE 141L  
Electrical Circuits-I Lab(Sec-5)  
Faculty : Md. Abu Obaidah (AbO)  
Instructor: Farhana Atuyar Saleh

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**Name:**

- 1. Tahsin Tabassum Ali**
- 2. Tabassoom Rahman**
- 3. Kanta Saha**

**ID:**

- 1. 2011059642**
- 2. 2011062642**
- 3. 2011731642**

## Lab 4: Delta-Wye Conversion

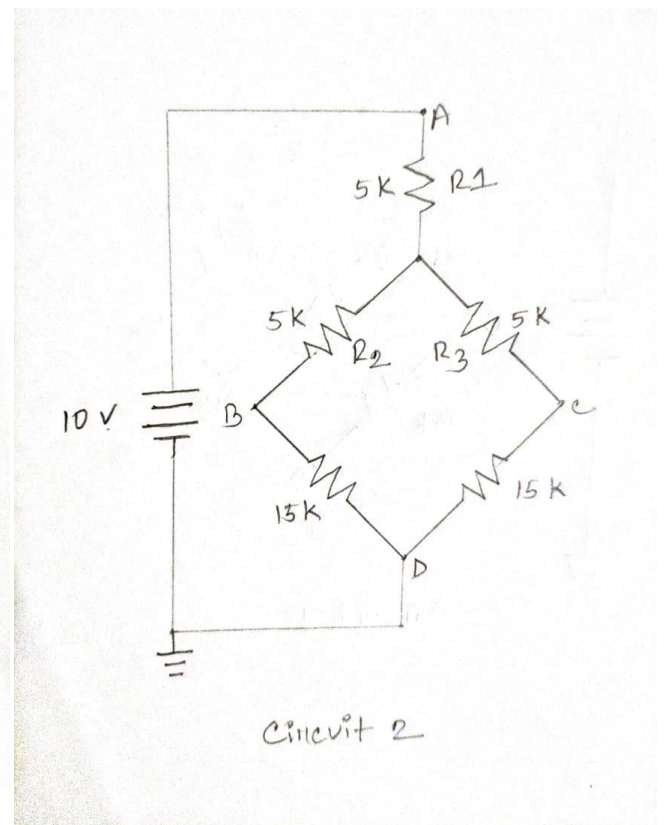
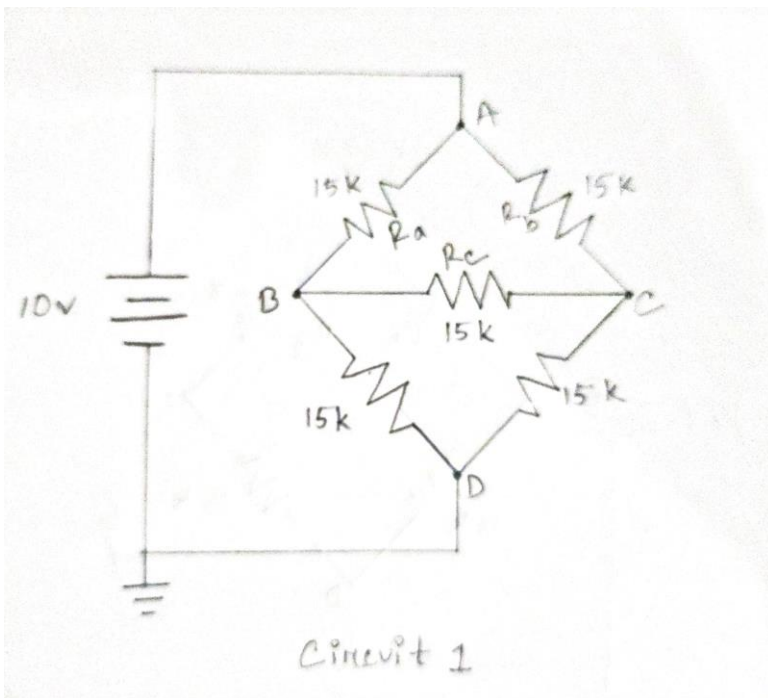
### Objectives:

1. We have to perform Delta-Wye Conversion
2. We have to verify the results with measured data.
3. We have to solve a complex circuit using Delta-Wye Conversion.

### List of Equipment

- Trainer Board
- DMM
- 5 x  $15\text{k}\Omega$  resistor
- 3 x  $5\text{k}\Omega$  resistor
- Multisim.

### Circuit Diagram:



## Data Table:

Table 1:

Theoretical R	Measured R	% Error
15k	Brown, Green, Orange, Gold	0%
5k	Green, Black, Red, Gold	0%

Table 2:

Readings	Circuit 1	Circuit 2	% Error
$V_{AD}$	10V	10V	0%
$V_{BD}$	5V	5V	0%
$V_{CD}$	5V	5V	0%
$V_{AB}$	5V	5V	0%
$V_{BC}$	0V	0V	0%
$V_{AC}$	5V	5V	0%

## Results:

All the results of the tables are shown in the Question/Answer section.

## Question/Answer:

- 1. The resistors in Circuit 1 are in series or in parallel combination?**

**Ans:**

The resistors in Circuit 1 are neither in series or in parallel combination. They are in a complex combination.

- 2. What technique would you use to find the equivalent resistance?**

**Ans:**

I would use Delta-Wye conversion technique to find the equivalent resistance.

- 3. Perform Delta-Wye conversion for  $\Delta ABC$  (upper portion) of circuit 1. Show all your steps to find the equivalent resistance  $R_1$ ,  $R_2$ ,  $R_3$  from  $R_a$ ,  $R_b$ ,  $R_c$ .**

**Ans:**

Here,  $R_a = 15k\Omega$ ,  $R_b = 15k\Omega$ ,  $R_c = 15k\Omega$

According to Delta-Wye conversion formula,

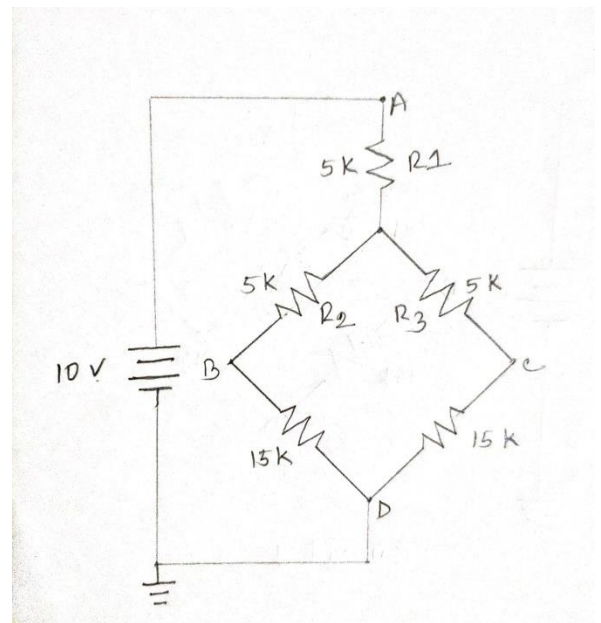
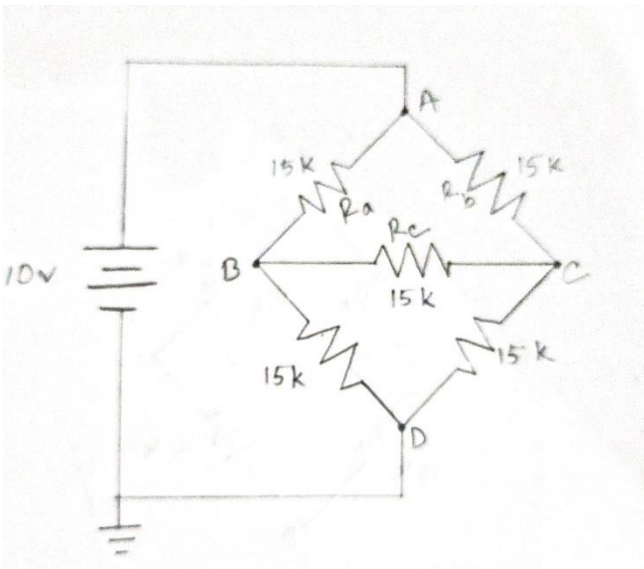
$$\begin{aligned} R_1 &= (R_b * R_c) / (R_a + R_b + R_c) \\ &= (15k * 15k) / (15k + 15k + 15k) \\ &= 5k \end{aligned}$$

$$\begin{aligned} R_2 &= (R_a * R_c) / (R_a + R_b + R_c) \\ &= (15k * 15k) / (15k + 15k + 15k) \\ &= 5k \end{aligned}$$

$$\begin{aligned} R_3 &= (R_a * R_b) / (R_a + R_b + R_c) \\ &= (15k * 15k) / (15k + 15k + 15k) \\ &= 5k \end{aligned}$$

**4. Redraw the equivalent the circuit after applying the Delta-Wye conversion for  $\Delta ABC$ . Is it same as circuit 2?**

**Ans:**



After redrawing the equivalent circuit after applying the Delta-Wye conversion for  $\Delta ABC$ . Everything is exactly same except the values of individual resistances of the Delta and Wye schematics.

### 5. Calculate Req.

**Ans:**

We are calculating  $R_{eq}$  from circuit 2,

$R_2$  and  $R_{BD}$  are in series, so,  $R_2' = 5k + 15k = 20k\Omega$

$R_3$  and  $R_{CD}$  are in series, so,  $R_3' = 5k + 15k = 20k\Omega$

$R_2'$  and  $R_3'$  are in parallel, so,  $1/R_p = (1/20k) + (1/20k) = 1/10k\Omega$

$R_p = 10\text{ k}\Omega$

$R_1$  and  $R_p$  are in series, So,  $R_{eq} = 5k + 10k = 15\text{ k}\Omega$

### 6. Calculate the voltage of R1, R2, R3.

Calculate  $V_{AB}$ ,  $V_{BC}$ ,  $V_{AC}$  and  $V_{AD}$ ,  $V_{BD}$ ,  $V_{CD}$ . Do your calculated values match the measured values for circuit 2? Find the % Error.

**Ans:**

$R_1 = 5k\Omega$

$V_{R1} = (5 \cdot 10) / (5 + 10) = 3.33\text{ V}$

Voltage across  $10k\Omega$  in circuit 2 and the  $20k\Omega$  resistors being parallel in circuit 1 =  $(10 - 3.33) = 6.67\text{ V}$

$V_{R2} = V_{R3} = (5 / (5 + 15)) \cdot 6.67 = 1.67\text{ V}$

$V_{AB} = V_{R1} + V_{R2} = 3.33 + 1.67 = 5\text{ V}$

$V_{AC} = V_{R1} + V_{R3} = 3.33 + 1.67 = 5\text{ V}$

$V_{BD} = V_{CD} = 6.67 \cdot (15 / (15 + 5)) = 5\text{ V}$

$V_{AD} = V_{AB} + V_{BD} = 5 + 5 = 10\text{ V}$

$I_s = V / R_{eq} = 10 / 15k = 0.67\text{ mA}$

Using CDR,  $I_{R2} = ((R_3 + R_5) / (R_2 + R_3 + R_4 + R_5)) \cdot I_s$   
 $= (20 / 40) \cdot 0.67 = 0.335\text{ mA}$

$V_2 = I_2 \cdot R_2 = 0.335 \cdot 10^{-3} \cdot 5k$   
 $= 1.675\text{ V}$

$I_3 = I_s - I_2$   
 $= 0.67 - 0.335$   
 $= 0.335\text{ mA}$

$$V_3 = I_3 \cdot R_3 = 0.335 \cdot 10^{-3} \cdot 5k$$

$$= 1.675V$$

Using KVL,

$$V_2 - V_3 - V_{bc} = 0$$

$$\rightarrow V_{bc} = V_2 - V_3$$

$$\rightarrow 1.675V - 1.675V = 0V$$

The measured values of  $V_{AB}$ ,  $V_{BC}$ ,  $V_{AC}$  and  $V_{AD}$ ,  $V_{BD}$ ,  $V_{CD}$  from multisim are 5V, 0V, 5V and 10V, 5V, 5V. So, the calculated and measured values are Exactly the same. And the error% for  $V_{AB}$ ,  $V_{BC}$ ,  $V_{AC}$  and  $V_{AD}$ ,  $V_{BD}$ ,  $V_{CD}$  are all 0%

**7. Using Table 2, analyze whether Circuit 2 is equivalent to Circuit 1? Was Delta-Wye conversion successful?**

**Ans:**

If we analyze Table 2, we are able to see that the terminal voltages are absolutely same for each circuit. For example,  $V_{AB}$  is 5V,  $V_{BC}$  is 0V,  $V_{AC}$  is 5V and  $V_{AD}$  is 10V,  $V_{BD}$  is 5V,  $V_{CD}$  is 5V for each circuit. So, the error is 0%. As a result, we can that Delta-Wye conversion is successful.

**Discussion:**

From the lab 4, we learned how to solve a complex circuit using Delta-Wye conversion method.

As, it was an online lab, we had to use multisim to do the experiments. So, we didn't have to face many errors or faults. We could find the theoretical values easily. But when evaluating  $V_{BC}$ , I faced some technical error in multisim and got unusual value. Restarting multisim solved the problem.

If we would have done the lab offline, we could have faced many errors such as human errors, environmental errors or mechanical errors. Also, we could have faced errors using DMM, cables, breadboard connection etc.