North South University
Department of Electrical and Computer Engineering

EEE141L - Electrical Circuits I Lab
Lab Outline

Instructor:
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Student consultation hour/s:
Saturday: 12:30pm-02:30pm

Office room: LIB600 [C1]
***If you need extra office hours, make an appointment through text or email.**

Tentative Class Plan:

Week	Topic	Assessment
1	**Introduction to Hardware Lab Apparatus**	
2,3	**Ohm's Law, KVL, and Voltage Divider Rule using Series Circuit**	
4,5	**KCL, Current Divider Rule with Parallel and Ladder Circuit**	
6,7	**Ladder Circuit and Loading Effect of Voltage Divider Circuit**	**Quiz-1 (week-6)**
8,9	**Balanced Bridge Circuit and Delta- Wye Conversion**	
10,11	**Superposition Theorem**	
12,13	**Thevenin's, Norton's and Maximum Power Transfer Theorem**	
14,15	**Charging and Discharging of RC circuits, Assignment will be assigned (week-14)**	**Quiz-2 (week-14)**
16	***Assignment submission deadline**	**Setup Test, Viva, Final**

Tentative Marking Rubric:

Category	Marks (in %)
Attendance	10
Class Performance	10
Lab Report [Groupwise]	15
Simulation [Individual]	05
Quiz [Written]	10
Assignment	10
Setup-test	10
Viva	10
Final Exam [Written]	20
Total	100

Lab Report Marks Distribution (20 Points):

- Cover Page-0.5
- Experiment Name-0.5
- Objective-0.5
- Apparatus-0.5
- Theory-1
- Circuit Diagram-1
- Experimental Procedure-1
- Results-2
- Question/Answer-4
- Discussion-3
- Data Sheet-1
- Simulation-5

*Check the file- 'Lab Report Instructions' for further details.