

## **North South University**

#### CSE231L

#### Experiment #3

Name of Experiment: Combinational Logic Design

Date of Performance: 23 October, 2019

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Section: 13

Group: 3

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## Objectives

- We have to become familianized with the analysis of combinational logic networks.
- We have to learn the implementation of networks using the two canonical forms.

# Equipments

- Trainer board.
- 1 x IC7411 Triple 3-input AND gates.
- 2 x JC 4075 Triple 3-input OR gates.
- -1 x I C 7404 Hex Inverters (NOT gates)

### Theory =

Minterms and Maxterms: A binary variable may appear either in its normal form (x) or in its complement form (x). Now consider two binary variables x and y combined with AND operation. Since each variable may appear in either form; there are four possible combinations: x'y', xy, xy', xy. Each of there four AND terms is called a minterm, or a standard product. If we have n variables, they can be combined to form 2" minterms.

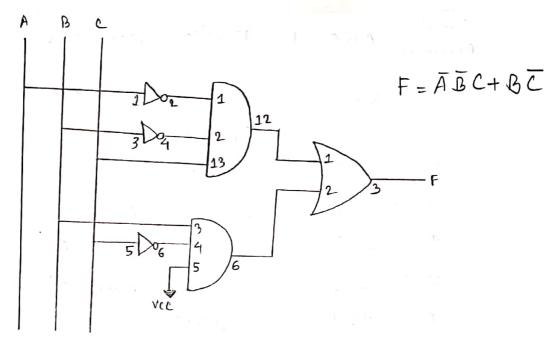
The four minterms and maxterms for 2 variables, together with symbolic deg designations, are listed in the table.

	1	Minterms		Maxterms	
ス	Z	Torm	Designation	Torm	Designation
0	D	ж′у′	mo	X+7	Mo
D	1	メダ	ma	1441	Mı
1	D	NY	m <sub>2</sub>	71/+>	M <sub>2</sub>
1	4	УY	mz	31/471	M <sub>3</sub>

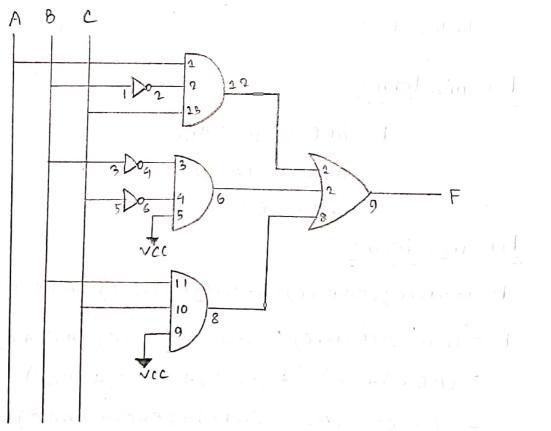
#### Canonical Forms?

Boolean functions expressed as a sum of minterms of products of maxterms are said to be in 1st canonical from and 2nd canonical form nespectively. Functions in their canonical form can also be expressed in a brief notation. For example, the function F = x'y' + ny' (1st canonical form) can be expressed as  $F(x,y) = \frac{1}{2}(0,2)$  and the function X = (A+B)(A+B') can be expressed as X(A,B) = T(0,1). The numbers following the sum and product symbols are the indices of the minterms and maxterms of the respective functions.

## Circuit Diagrams



Figure's 1st canonical form.



<u>Figure</u>: 2nd cononical form.

### Results

Input Reference	ABC	F	Min term	Max term
6	000	0	. /	A+B+C
1	001	1	A'B'C	
2	010	1	A'BC'	
3	011	0		A+B4c'
4	100	O		A1+B+C
5	101	0		A'+B+ C'
G	110	1	ABC'	
.7	111	D		A'+6+4

Table: Truth-table to a combinational circuit

#### For mintermo

### For max term?

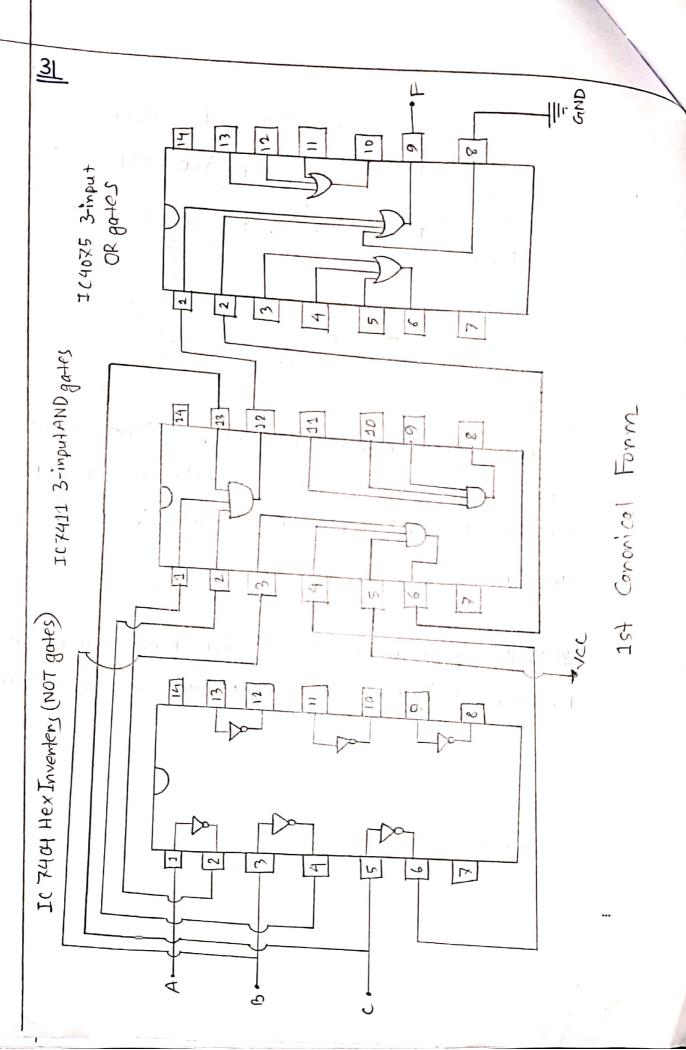
	Shorthand Notation	Function
1st Canonical Form	F= 2 (1,2,6)	F= ABC + ABC + ABC
2nd Canonical Form	F=TT (0,3,45,7).	F=ABC+BC+BC

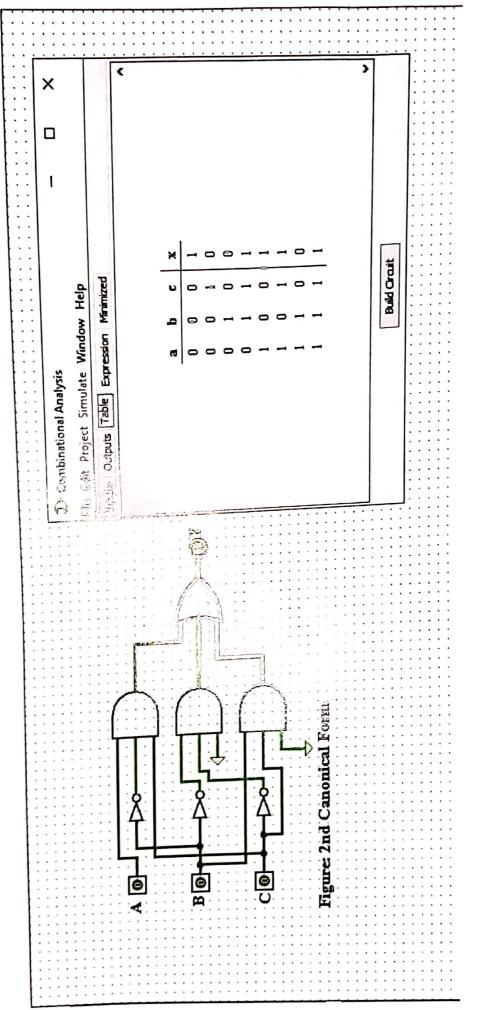
## Questions

First canonical form; Boolean functions expressed as a sum of mintenms are said to be in 1st Canonical Form. Example; F=x'y'+xy'.

The given expression.

Yes, the given expression is in first canonical form. Because, it is expressed as a sum of minterms.





21 Printbut of the logisim circuit screenshot and thuth table screenshot of the 2nd conovical Form.

### Discussion

Because of human error and equipment error, we didn't get our expected results. In the lab, we observed that completing the truth table from the given inputs we made circuit of it. In this experiment, we first found out the minterms and the maxterms of the given function. Before writing the 1st canonical form, we first simplified the equation so that our circuit gets simplier. We did the same for the 2nd canonical form. That's how we got much simplier forms of circuits. In this experiment, we used Ic 7411 Triple 3-input AND gates, IC 4075 Triple 3-input OR gates, IC 7404 Hex inverters (NOT gates). We connected one min term at a time and checked the output. Once all minterms had been connected and verified we got our results. Similarly, we did for the max terms.

#### CSE231L/EEE211L Lab 3 - Combinational Logic Design (Canonical Forms)

Instructor's Signature: ..... Data Sheet: Date: 23 October, 2019 Section: 13 Group No.: 3

		I.		
Input Reference	ABC	$F_{\parallel}$	Min term	Max term
0	000	0	,	ABIC
1	0 0 1	1	A'B'C	
2	0 1 0	1	A'BC'	1
3	0 1 1	0		A+B'+C
4	100	0	7 7 7	A'+B+C
5	101	0	, i -,	A'+B+C'
6	110	1	AB C/	
7	111	0		A+B+C'

Table F.1 - Truth table to a combinational circuit

,		Shorthand Notation	Function	
			F = ABC + ABC + ABC	
	1 <sup>st</sup> Canonical Form	$F = \Sigma \left( \frac{1}{2}, \frac{2}{6} \right)$	F = 7/3	
: d		F = 11 (0,3,4,5,7)	F = ABC + BC + BC	
NIC	Form			-

W

Table F.2 - 1st and 2nd canonical forms of the combinational circuit of Table F.1

$$F = (A+B+C) (A+B+C) (A+B+C) (A+B+C) (A+B+C) (A+B+C)$$

$$F = (A+B+C) (A+B+C) (A+B+C) (A+B+C) (A+B+C) (A+B+C)$$

$$= (ABC) + (ABC) + (ABC) + (ABC) + (ABC)$$

$$= (A'B'(C') + (ABC') + (ABC) + (ABC) + (ABC)$$

$$= (A'B'(C') + (ABC') + (ABC) + (ABC) + (ABC)$$

$$= (A'B'(C') + (ABC') + (ABC) + (ABC')$$

$$= (A'B'(C') + (ABC') + (ABC') + (ABC')$$

$$= (ABC') + (ABC') + (ABC') + (ABC') + (ABC')$$

$$= (ABC') + (ABC') + (ABC') + (ABC') + (ABC')$$

$$= (ABC') + (ABC') + (ABC') + (ABC') + (ABC')$$

$$= (ABC') + (ABC') + (ABC') + (ABC') + (ABC') + (ABC')$$

$$= (ABC') + (ABC') +$$

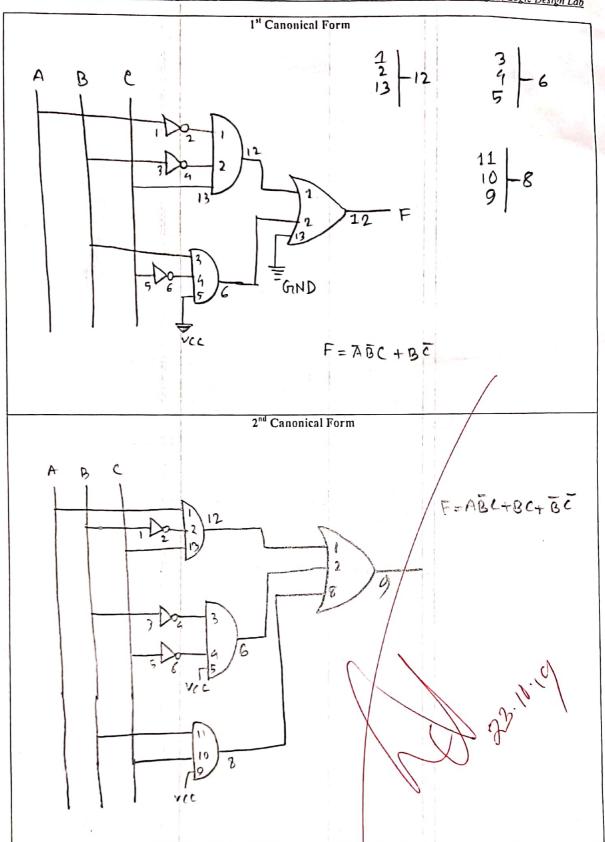


Figure F.1 - 1<sup>st</sup> and 2<sup>nd</sup> canonical circuit diagrams of the combinational circuit of Table F.1

4|Page