

# Ass\_Formal 2.0

Referring to **Ass\_DFT\_1.0** Assignment. You have to run **Formal Verification** of the generated **gate level netlist post dft** verses the **RTL files** of **UART\_RX** using **Formality Tool**

Required: -

A) You have to deliver the following files: -

- 0- Formality script >> **dft\_fm\_script.tcl**
- 1- Formality log >> **dft\_fm.log**
- 2- Matched points report >> passing\_points.rpt
- 3- Unmatched points report >> failing\_points.rpt
- 4- Unverified Points report >> unverified\_points.rpt
- 5- Aborted Points report >> aborted\_points.rpt