LAB 2

Using STM32F103C8T6 Chip with ARM Cortex-M3 32-Bit Microcontroller Everything will be written from scratch:

- main.c
- Platform_Types.h
- Startup.c and another assembly version (startup.s)
- Makefile
- linker_script.ld

Symbols in output objects and elf file:

```
HP@LAPTOP-PPV7E6PE MINGW64 /e/Embedded System/Basic/Embedded_System_Online_Diplo
ma/Embedded_C/Assignment3/lab2 (master)
$ arm-none-eabi-nm.exe main.o
00000003 C bss_var
00000000 R const_variables
00000004 D g_variables
00000000 T main
00000000 D R_ODR
```

```
HP@LAPTOP-PPV7E6PE MINGW64 /e/Embedded System/Basic/Embedded_System_Online_Diplo
ma/Embedded_C/Assignment3/lab2 (master)

$ arm-none-eabi-nm.exe startup.o

U_E_bss
U_E_DATA
U_E_text
U_S_bss
U_S_DATA
U_stack_top

00000000 W Bus_Fault

00000000 T Default_Handler
U main

00000000 W MM_Fault_Handler
00000000 W System_Online_Diplo
0000000 W MM_Fault_Handler
00000000 W System_Online_Diplo
0000000 W MM_Fault_Handler
00000000 W Descorts
```

```
HP@LAPTOP-PPV7E6PE MINGW64 /e/Embedded System/Basic/Embedded_System_Online_Diplo
ma/Embedded_C/Assignment3/lab2 (master)

$ arm-none-eabi-nm.exe learn_in_depth_cortex_m3.elf
20000008 B _E_bss
20000008 D _E_DATA
080001dc T _E_text
080001dc T _Estext
20000008 B _S_bss
20000000 D _S_DATA
20001008 B _stack_top
20001008 B _stack_top
20001008 B _stack_top
20001008 B _const_variables
08000114 T _Default_Handler
20000004 D _q_variables
08000114 W _H_fault_Handler
20000004 D _variables
08000114 W M_Fault_Handler
0800006c T _main
08000114 W MM_Fault_Handler
080000114 W MM_Fault_Handler
08000114 W MM_Fault_Handler
08000114 W MM_Fault_Handler
08000114 W MM_Fault_Handler
08000114 W _M_S_DOR
08000114 Usage_Fault_Handler
08000116 t _Vector_handler
080001000 T _Vectors
```