## Using VersatilePB virtual board in QEMU and ARM toolchain

1. Writing source files, getting object files (with and without debug information) and analysing them.

```
HP@LAPTOP-PPV7E6PE MINGW64 /e/Embedded System/Basic/Embedded_System_Online_Diplo
ma/Embedded_C/Assignment2/lab1 (master)
$ arm-none-eabi-gcc.exe -c -g -I . -mcpu=arm926ej-s app.c -o app.o

HP@LAPTOP-PPV7E6PE MINGW64 /e/Embedded System/Basic/Embedded_System_Online_Diplo
ma/Embedded_C/Assignment2/lab1 (master)
$ arm-none-eabi-gcc.exe -c -g -I . -mcpu=arm926ej-s uart.c -o uart.o

HP@LAPTOP-PPV7E6PE MINGW64 /e/Embedded System/Basic/Embedded_System_Online_Diplo
ma/Embedded_C/Assignment2/lab1 (master)
$ ls *.o
app.o uart.o
```

```
P@LAPTOP-PPV7E6PE MINGW64 /e/Embedded System/Basic/Embedded_System_Online_Diploa/Embedded_C/Assignment2/lab1 (master)
$ arm-none-eabi-objdump.exe -h app.o
app.o:
              file format elf32-littlearm
Sections:
Idx Name
0 .text
                                                  LMA
                                                                File off
                                                                             Algn
2**2
                       00000018
                                    00000000 00000000
                                                                00000034
                                                                             CODE 2**2
                       CONTENTS,
00000064
                                    ALLOC, LOAD, RELOC, 00000000 00000000
                                                                READONLY,
  1 .data
                                                                0000004c
                       CONTENTS,
00000000
                                    ALLOC, LOAD, DATA
00000000 00000000
 2 .bss
                                                                000000b0 2**0
                       ALLOC
  3 .debug_info
                       0000006b
                                    00000000 00000000 000000b0
 CONTENTS,
4 .debug_abbrev 00000058
                                    RELOC, READONLY, DEBUGGING
00000000 00000000 0000011b 2**0
                                    READONLY,
00000000
                       CONTENTS,
                                                  DEBUGGING
 5 .debug_loc
                       0000002c
                                                                00000173 2**0
                                                  00000000
 CONTENTS, READONLY, DEBUGGING
6 .debug_aranges 00000020 00000000 00000000
                                                  00000000
                                                                 0000019f 2**0
                       CONTENTS, RELOC, READONLY, DEBUGGING 00000035 00000000 00000000 0000001
    .debug_line
                                    RELOC, READONLY, DEBUGGING
00000000 00000000 000001f4
                       CONTENTS,
 8 .debug_str
                       00000090
                                                                             2**0
                       CONTENTS,
00000012
                                    READONLY,
                                                  DEBUGGING
                                                  00000000 00000284 2**0
 9 .comment
                                    00000000
CONTENTS, READONLY
10 .ARM.attributes 00000032 00000000 00000000 00000296 2**0
                       CONTENTS, READONLY
0000002c 00000000 00000000 0000002c
CONTENTS, RELOC, READONLY, DEBUGGING
 11 .debug_frame
                                                  00000000 000002c8
```

```
HP@LAPTOP-PPV7E6PE MINGW64 /e/Embedded System/Basic/Embedded_System_Online_Diplo
ma/Embedded_C/Assignment2/lab1 (master)
$ arm-none-eabi-gcc.exe -c -I . -mcpu=arm926ej-s app.c -o app.o

HP@LAPTOP-PPV7E6PE MINGW64 /e/Embedded System/Basic/Embedded_System_Online_Diplo
ma/Embedded_C/Assignment2/lab1 (master)
$ arm-none-eabi-gcc.exe -c -I . -mcpu=arm926ej-s uart.c -o uart.o
```

2. Writing startup code, getting object file and analysing it.

3. Writing the linker script, linking all objects, getting the elf file and analyzing it.

```
HP@LAPTOP-PPV7E6PE MINGW64 /e/Embedded System/Basic/Embedded_System_Online_Diploma/Embedded_C/Assignment2/lab1
(master)
$ arm-none-eabi-ld.exe -T linker_script.ld.exe startup.o app.o uart.o -o learn-in-depth.elf -Map=outMap.map
C:\ARW_TOOLCHAIN\bin\arm-none-eabi-ld.exe: cannot open linker script file linker_script.ld.exe: No such file or
directory
```

4. Getting the symbol table for the object files and the final elf file.

5. Getting the binary file and simulating the application using QEMU

```
HP@LAPTOP-PPV7E6PE MINGW64 /e/Embedded System/Basic/Embedded_System_Online_Diploma/Embedded_C/Assignment 2/labl (master)
$ arm-none-eabi-objcopy.exe -0 binary learn-in-depth.elf learn-in-depth.bin
```

```
HP@LAPTOP-PPV7E6PE MINGW64 /e/Embedded System/Basic/Embedded_System_Online_Diploma/Embedded_C/Assignment
2/lab1 (master)
$ ../../../Unit3/Setup/qemu/qemu-system-arm -M versatilepb -m 128M -nographic -kernel learn-in-dep
th.bin
Learn_in_depth: Mostafa
```