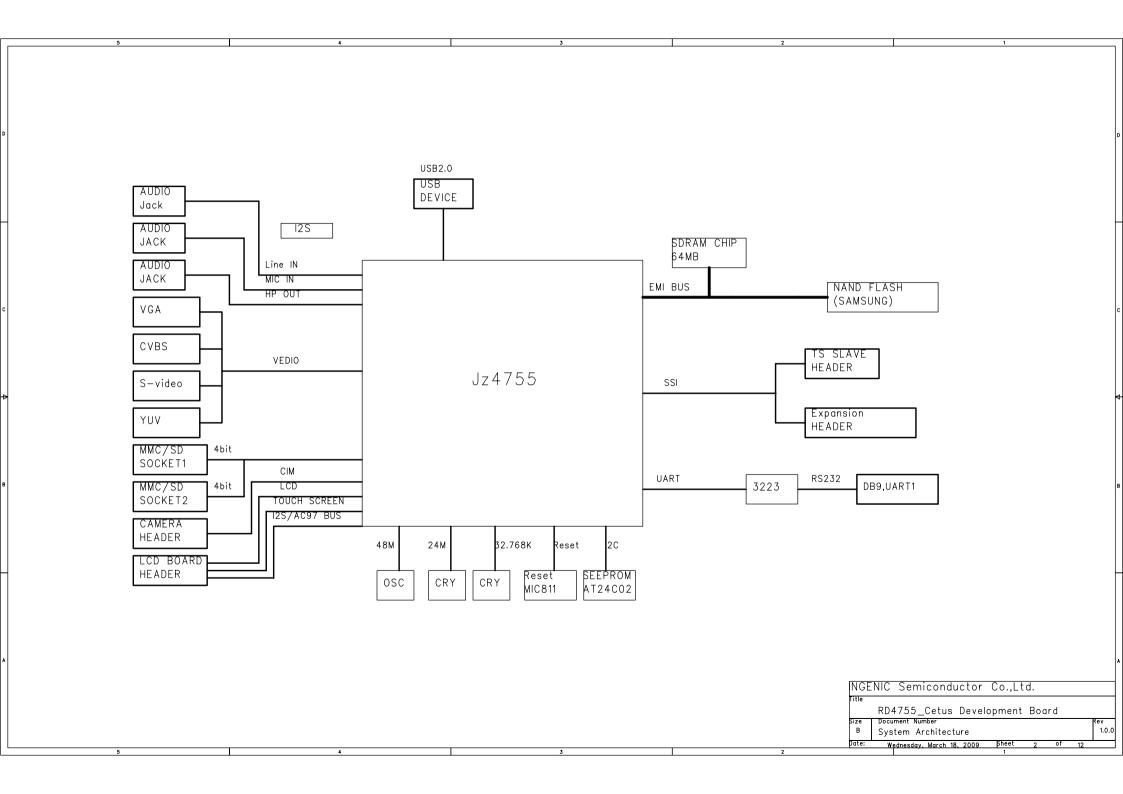


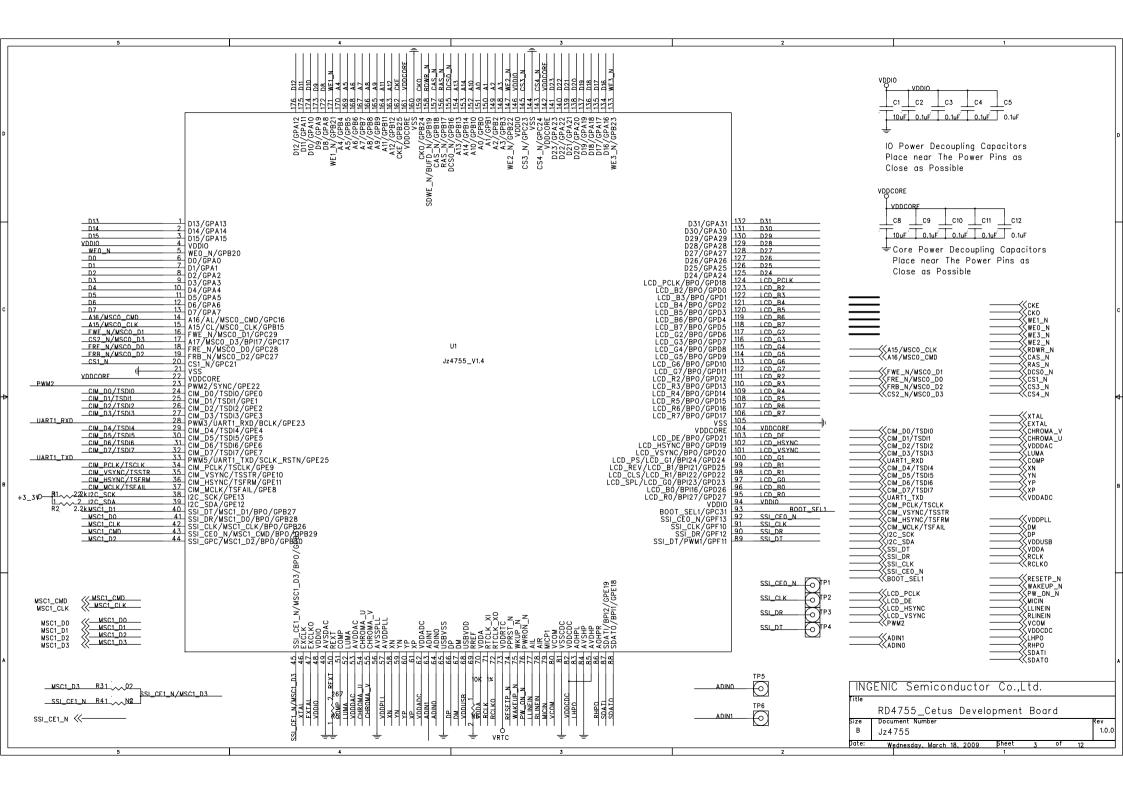
RD4755_Cetus Development Board Schematic Revision 1.0.0

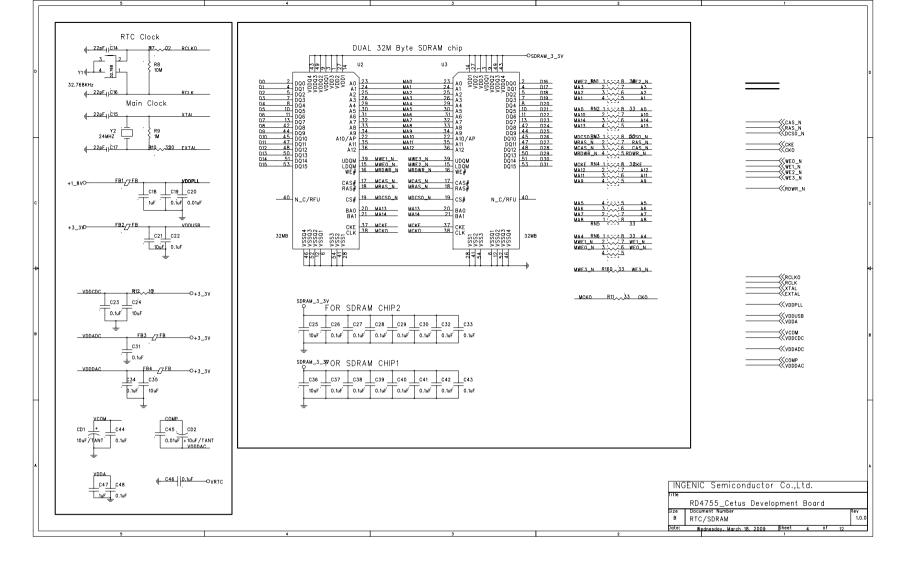
Title	Page
Cover Sheet	1
System Architecture	2
Jz4755	3
RTC/SDRAM	4
Static Memory/TS/Camera	5
POWER/Boot/Reset	6
SD/USB	7
Audio/FM	8
Video	9
VGA	10
LCD	11
History	12

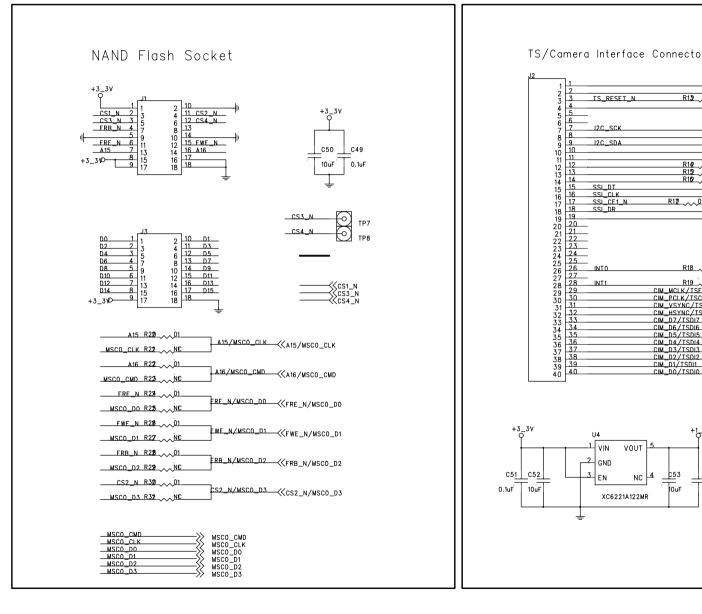
INGENIC Semiconductor Corporation System Technology Department

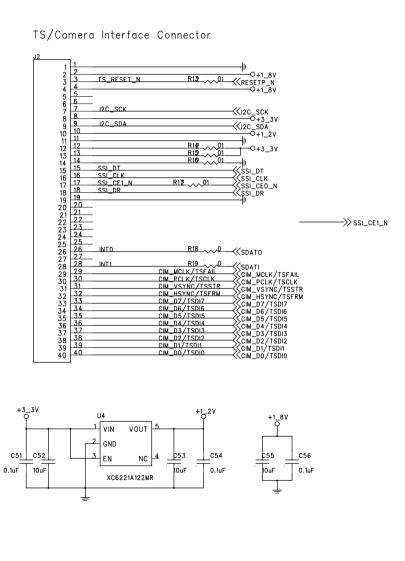
	GENIC Semiconductor Co.,Ltd.	
Title		
	RD4755_Cetus Development Board	
Size	Document Number	Rev
В	Cover	1.0.0
Date:	Wednesday, March 18, 2009 Sheet 1 of 12	



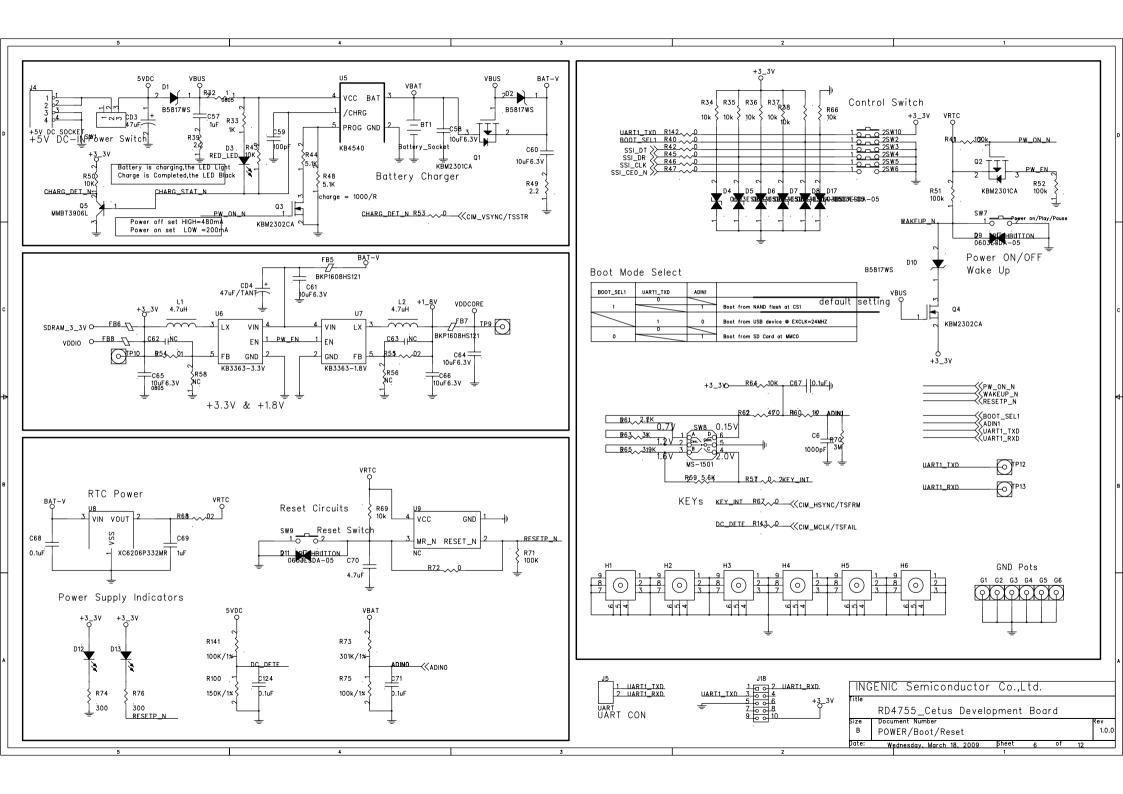


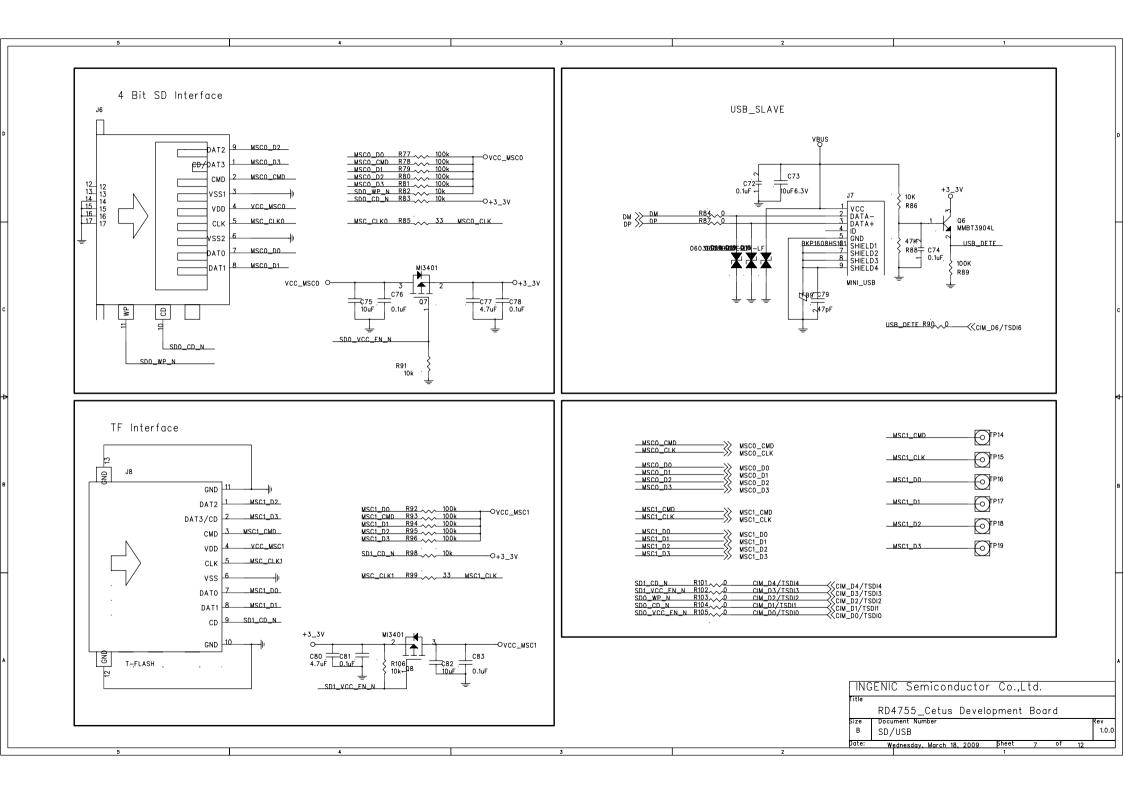


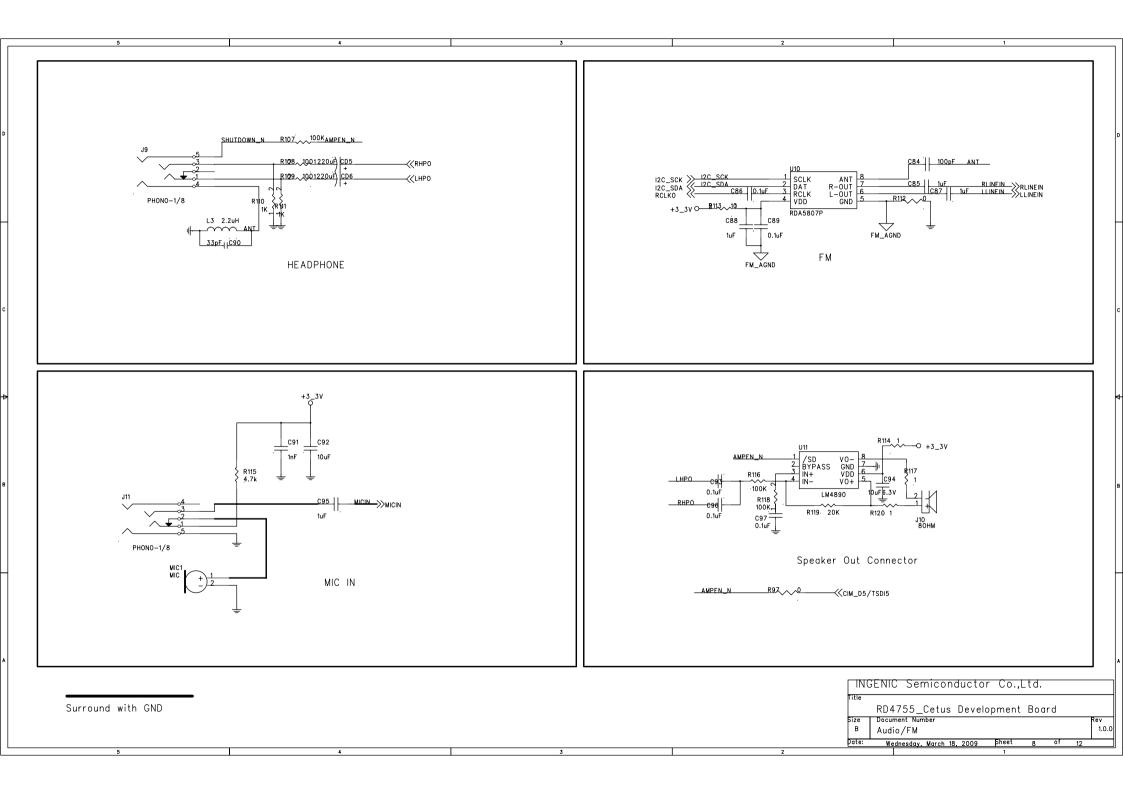


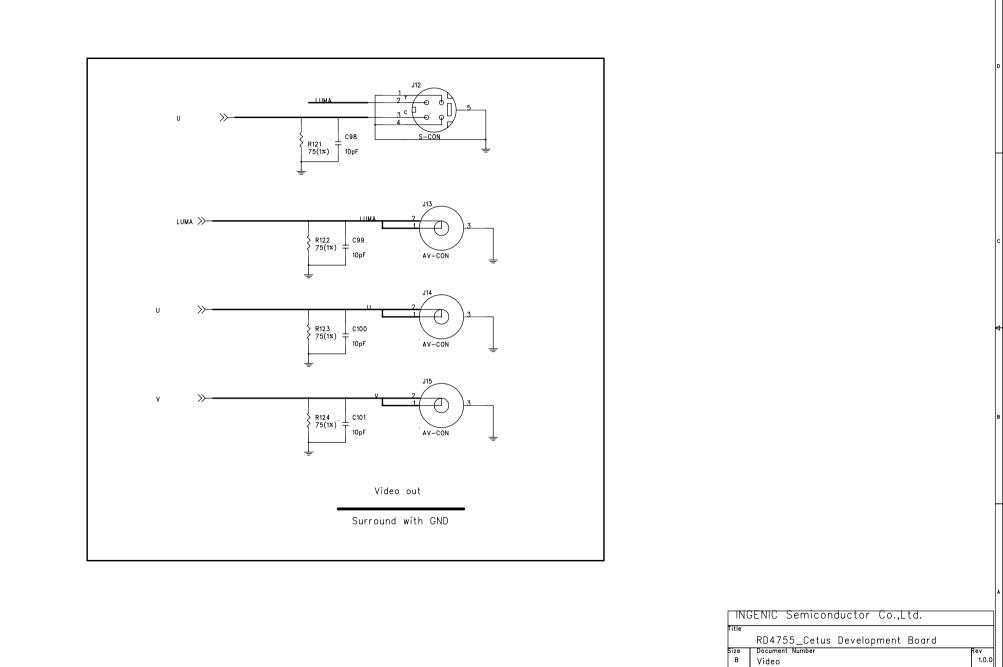


INGENIC Semiconductor Co.,Ltd. RD4755_Cetus Development Board 1.0.0 Static Memory/TS/Camera Wednesday, March 18, 2009

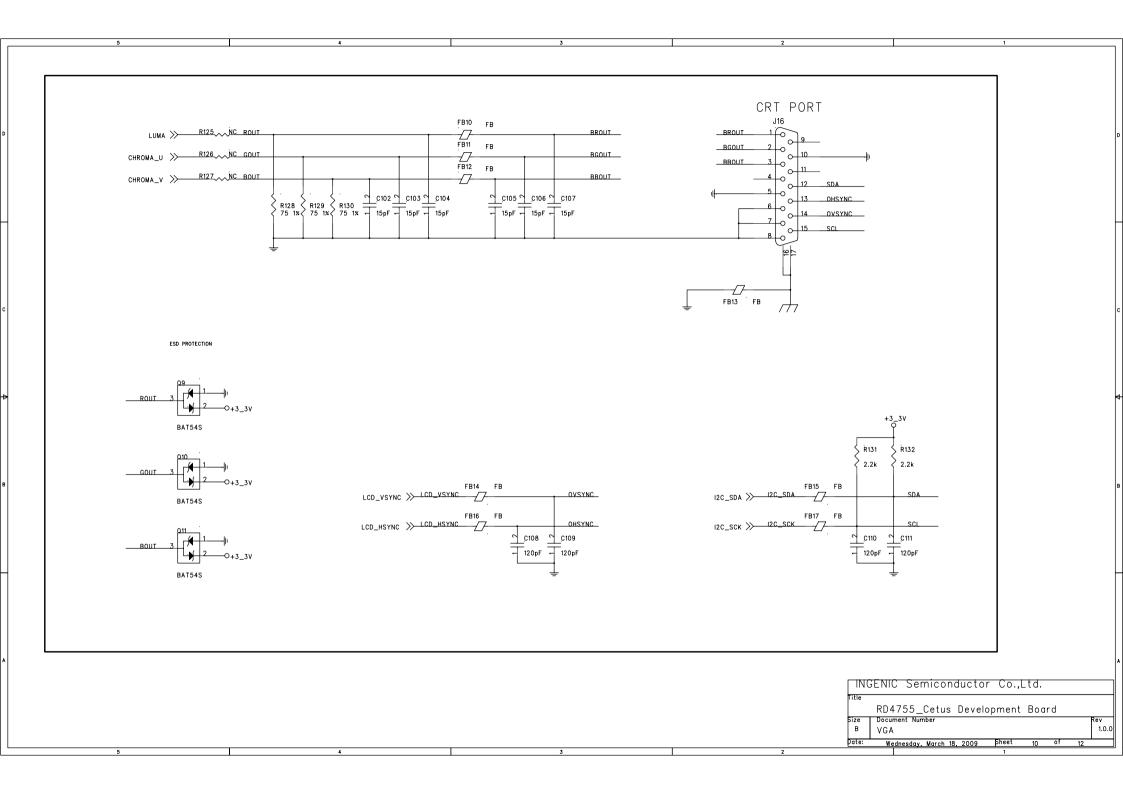


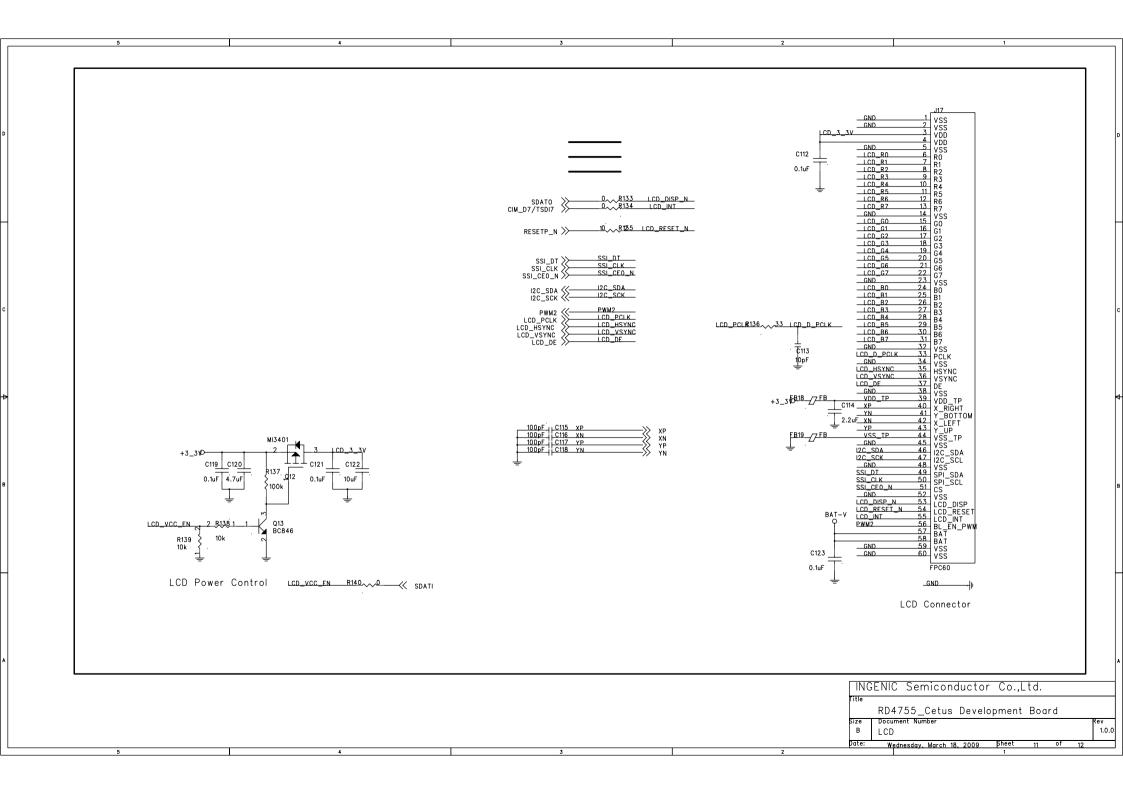






Wednesday, March 18, 2009





Data Revision Change	
Mor. 09. 2009 Revt.0 1. First Revision	
	•
	Ц
1	ř.
	ll l
<u>.</u>	H
	ll l
	ľ
	ll l
1	H
	i l
INCENIC Semici	onductor Co.,Ltd.
R04755_Cet	lus Development Board
History	10.0
The Spinish Sec	A 2 4500 PGW 19 17 D