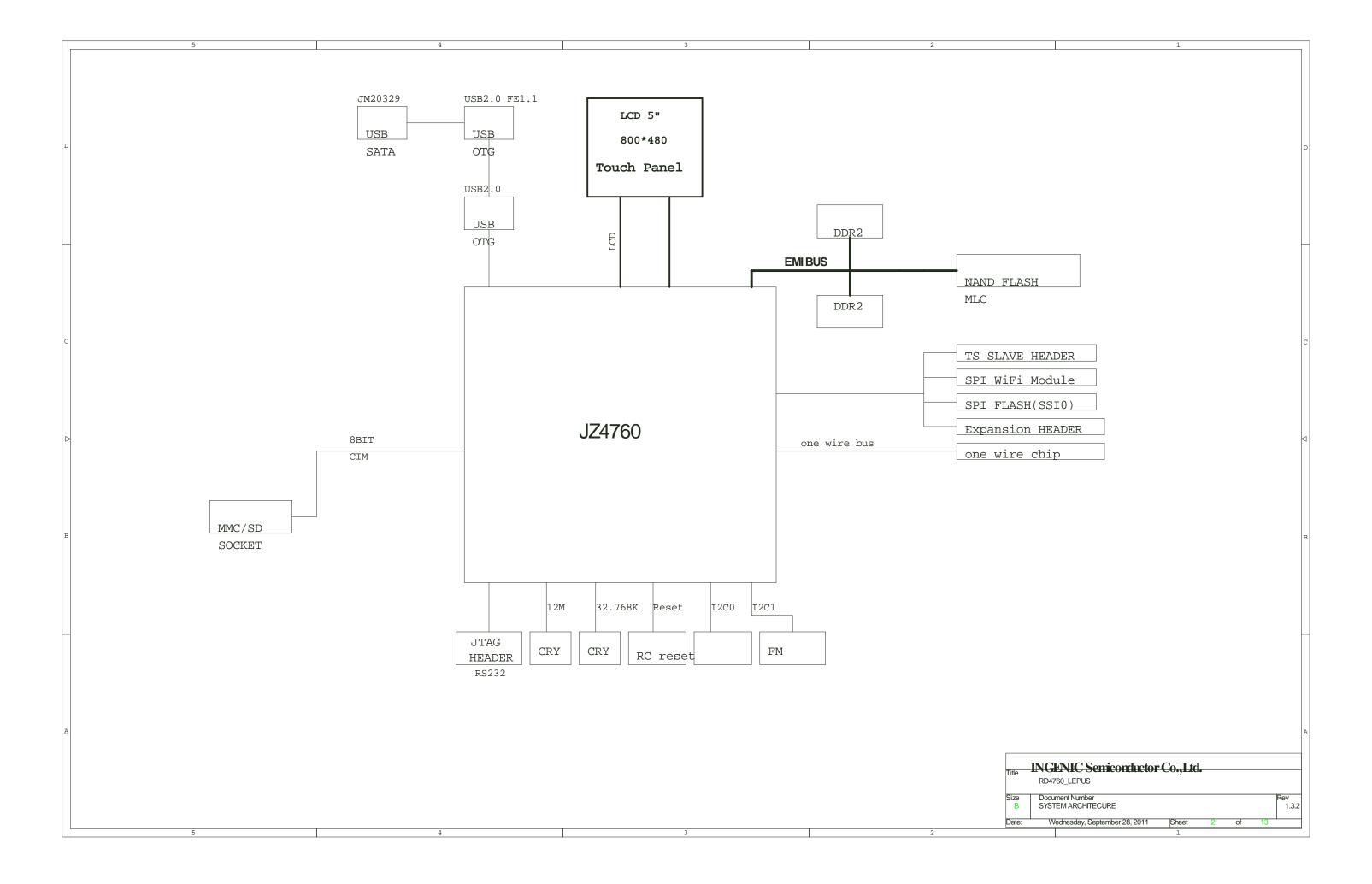
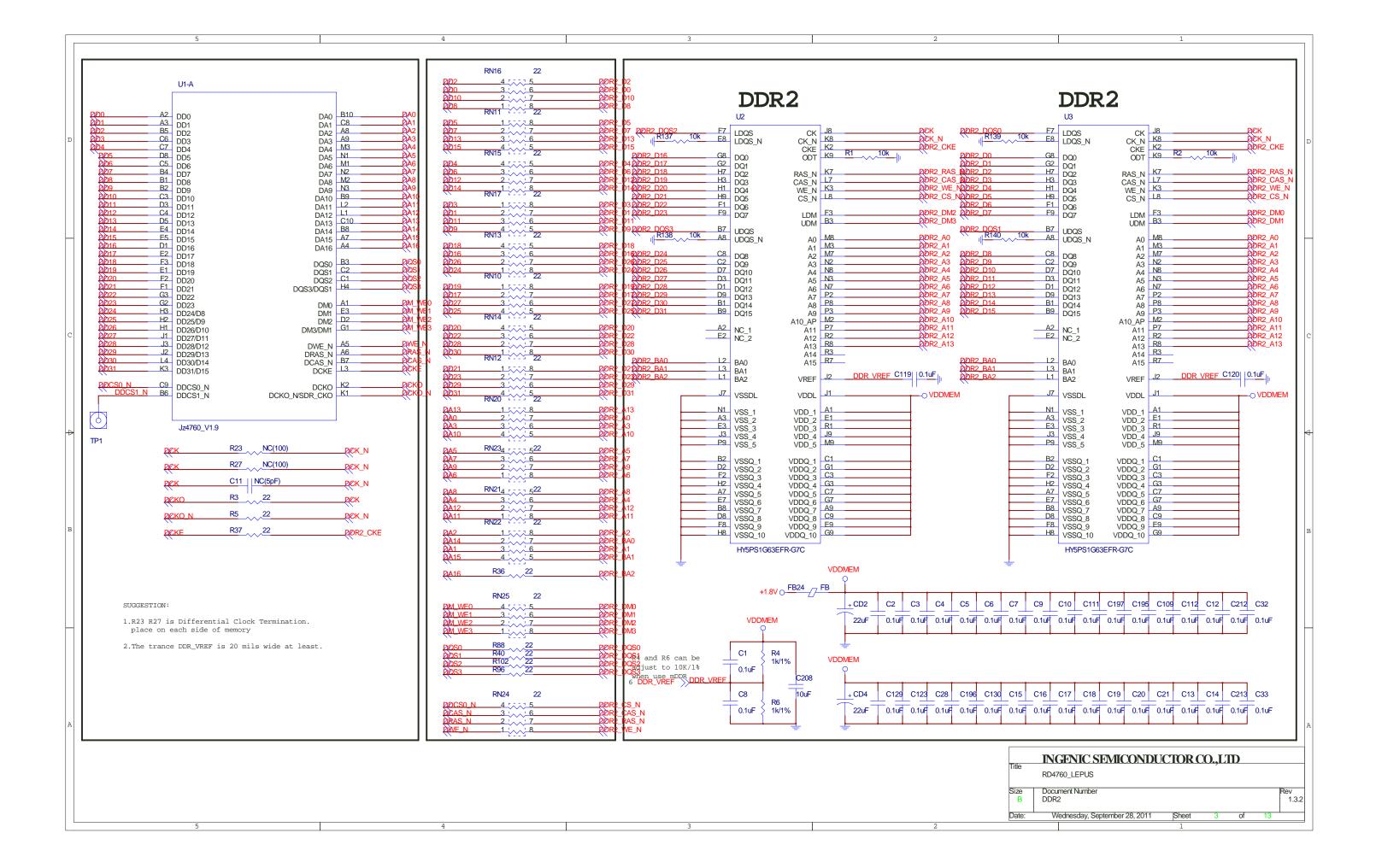
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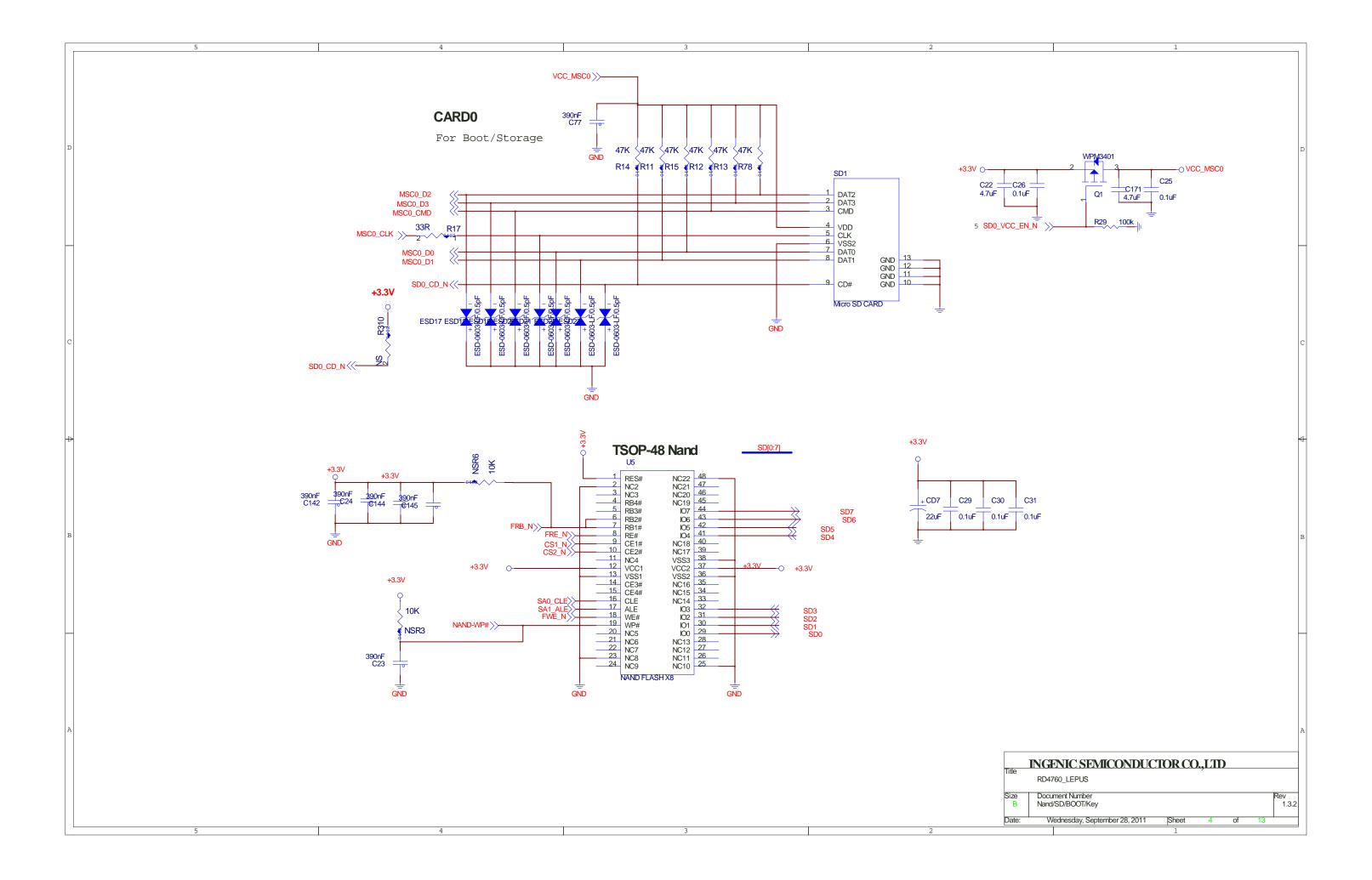
RD4760_LEPUS BOARD Schematic Revision 1.3.2

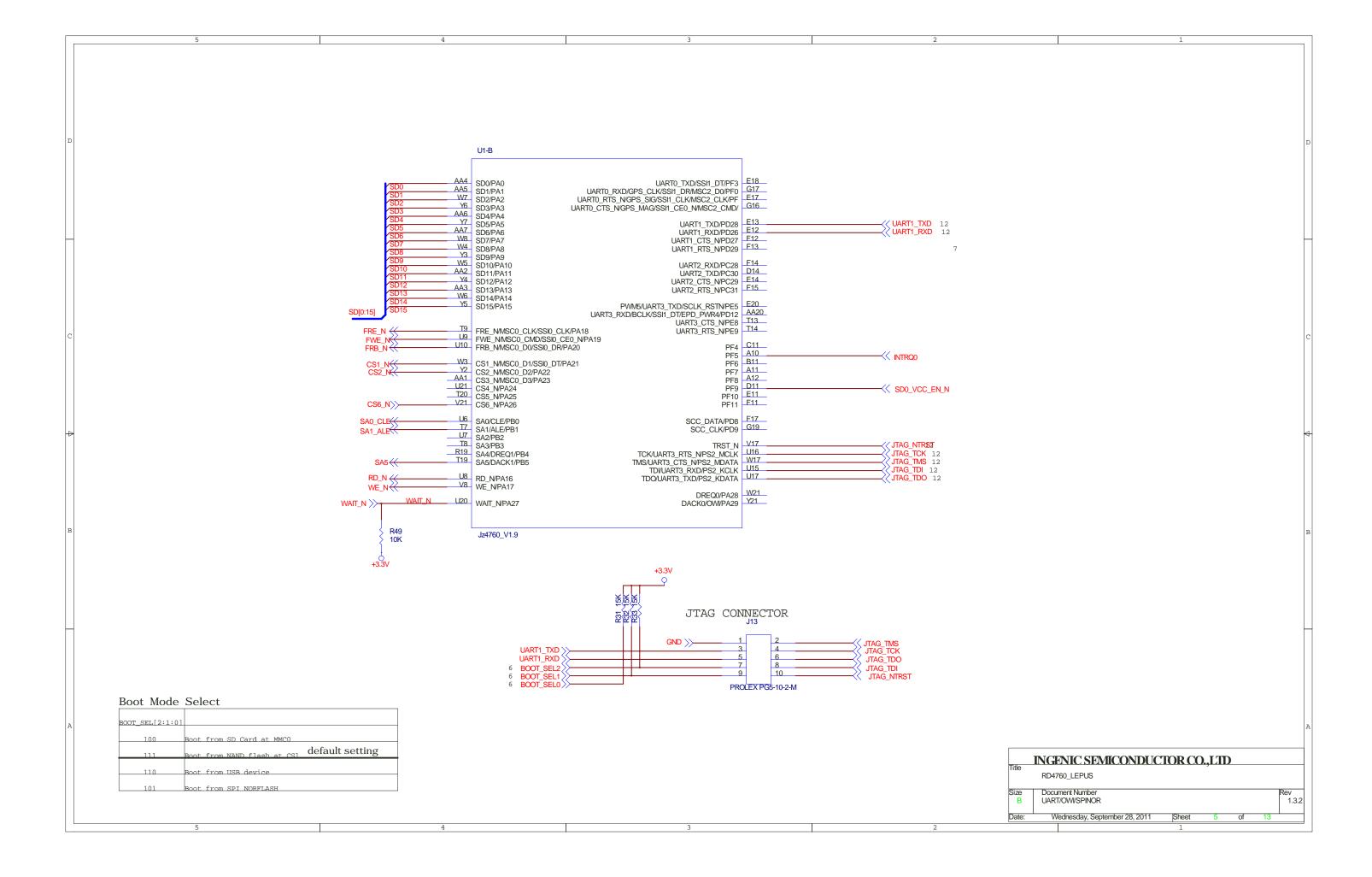
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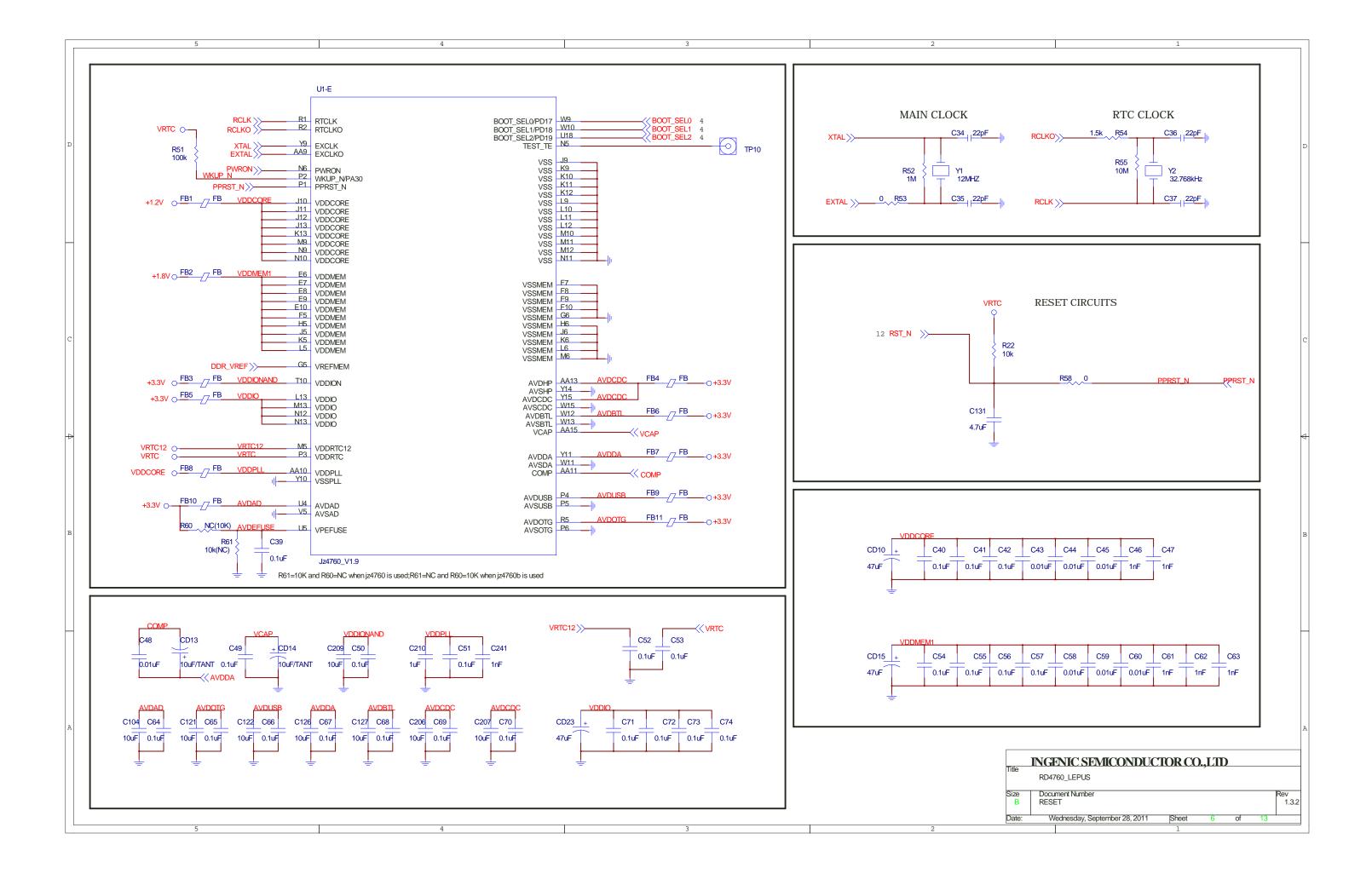
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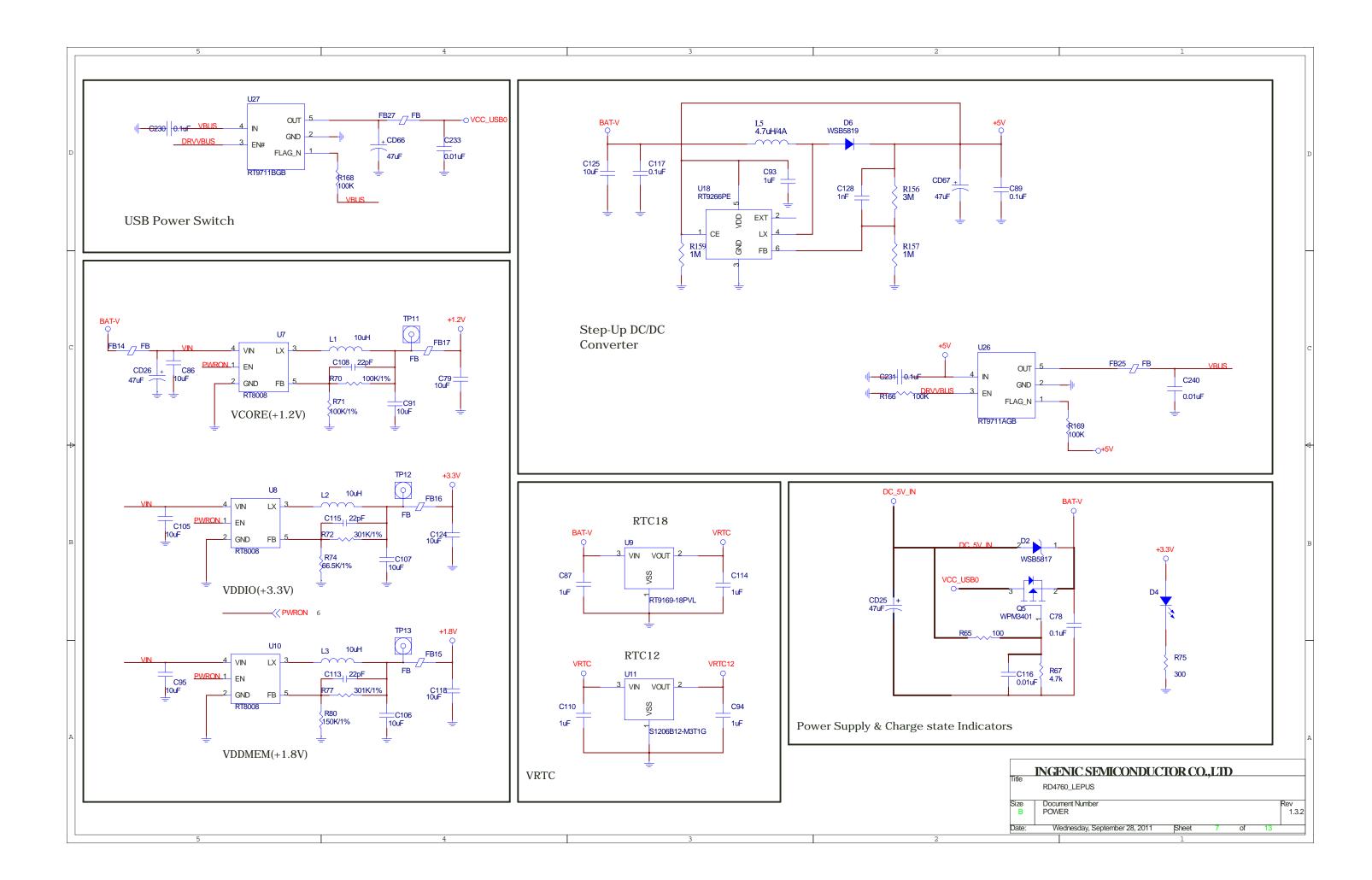


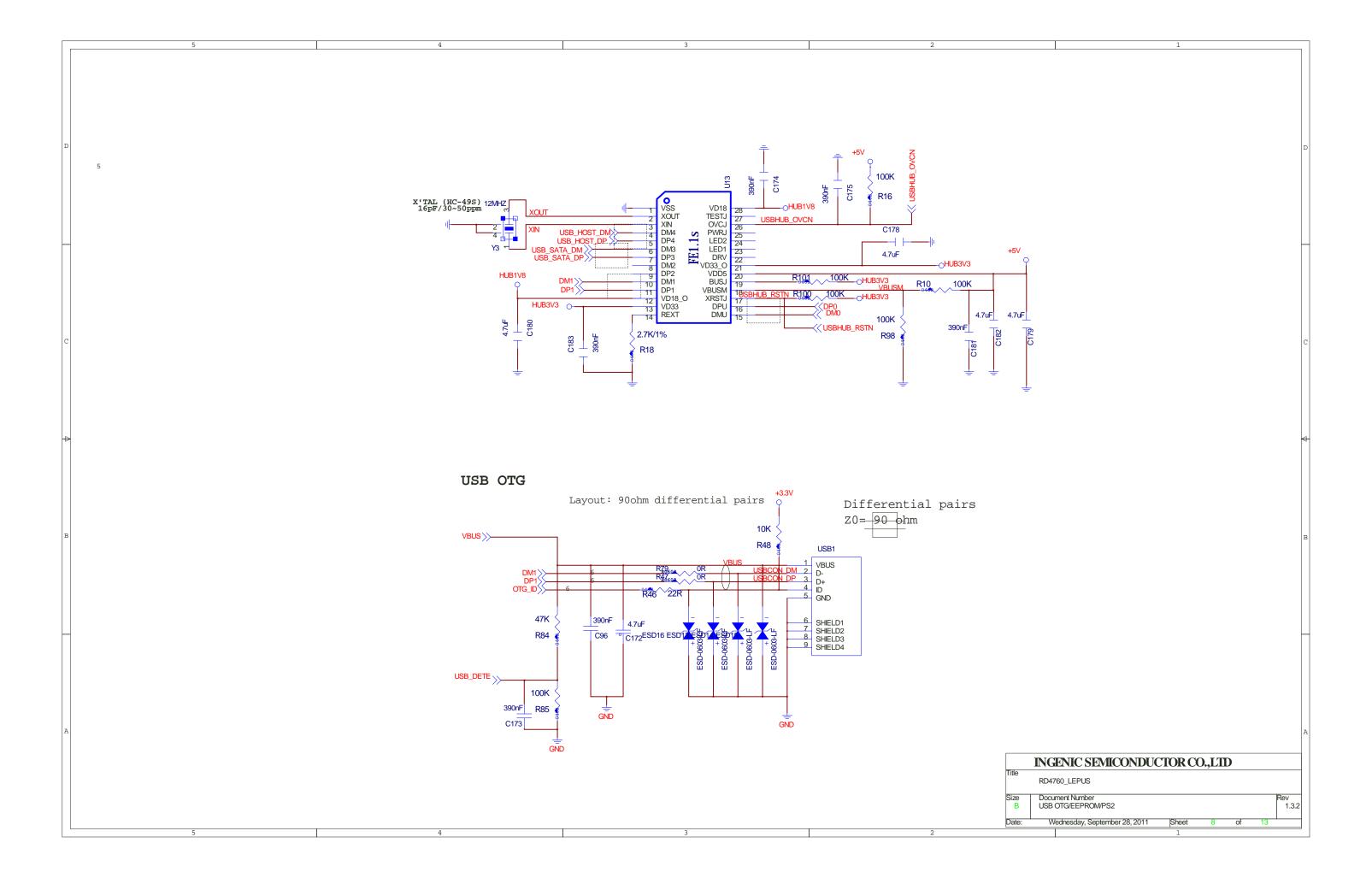


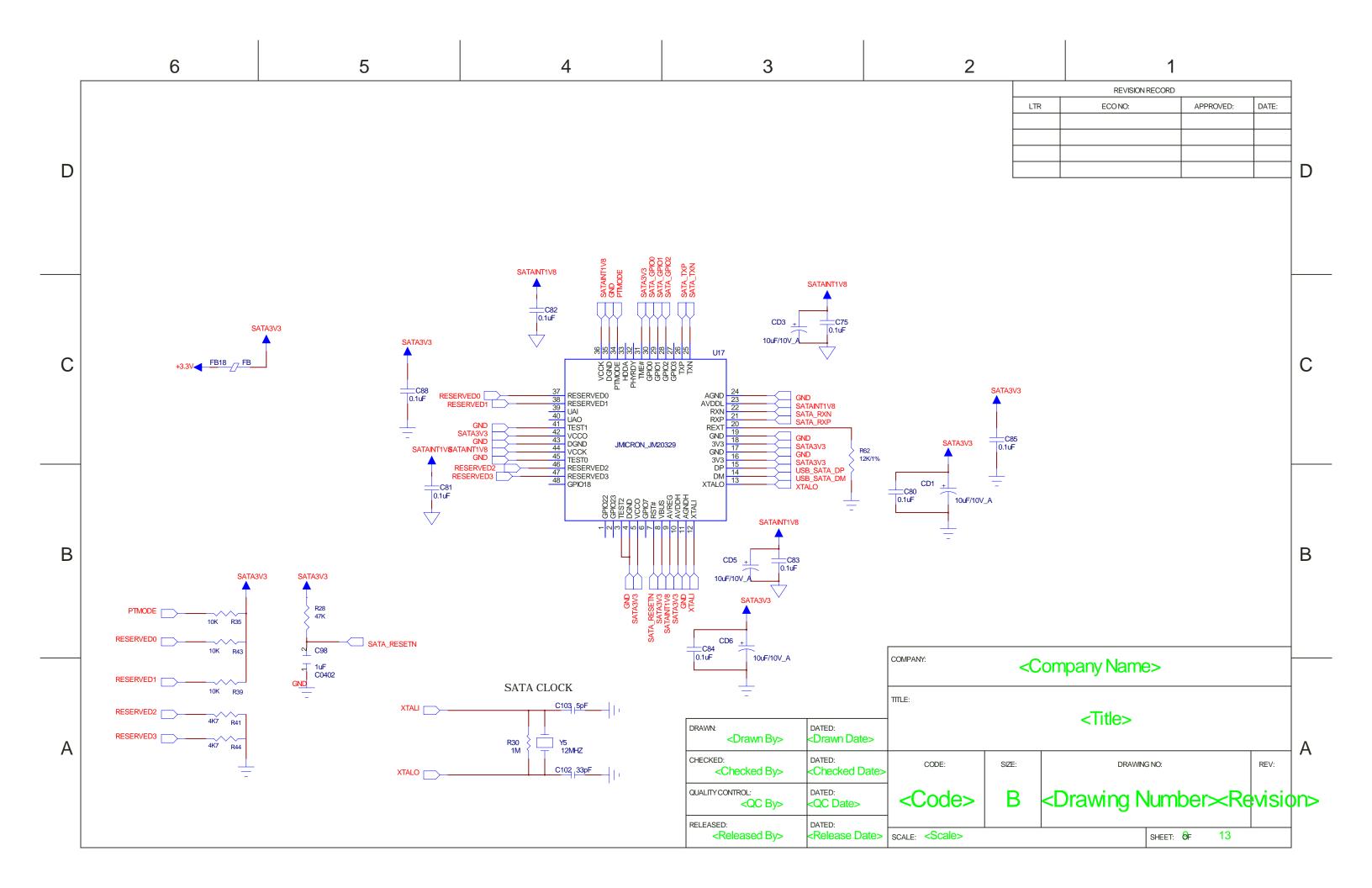


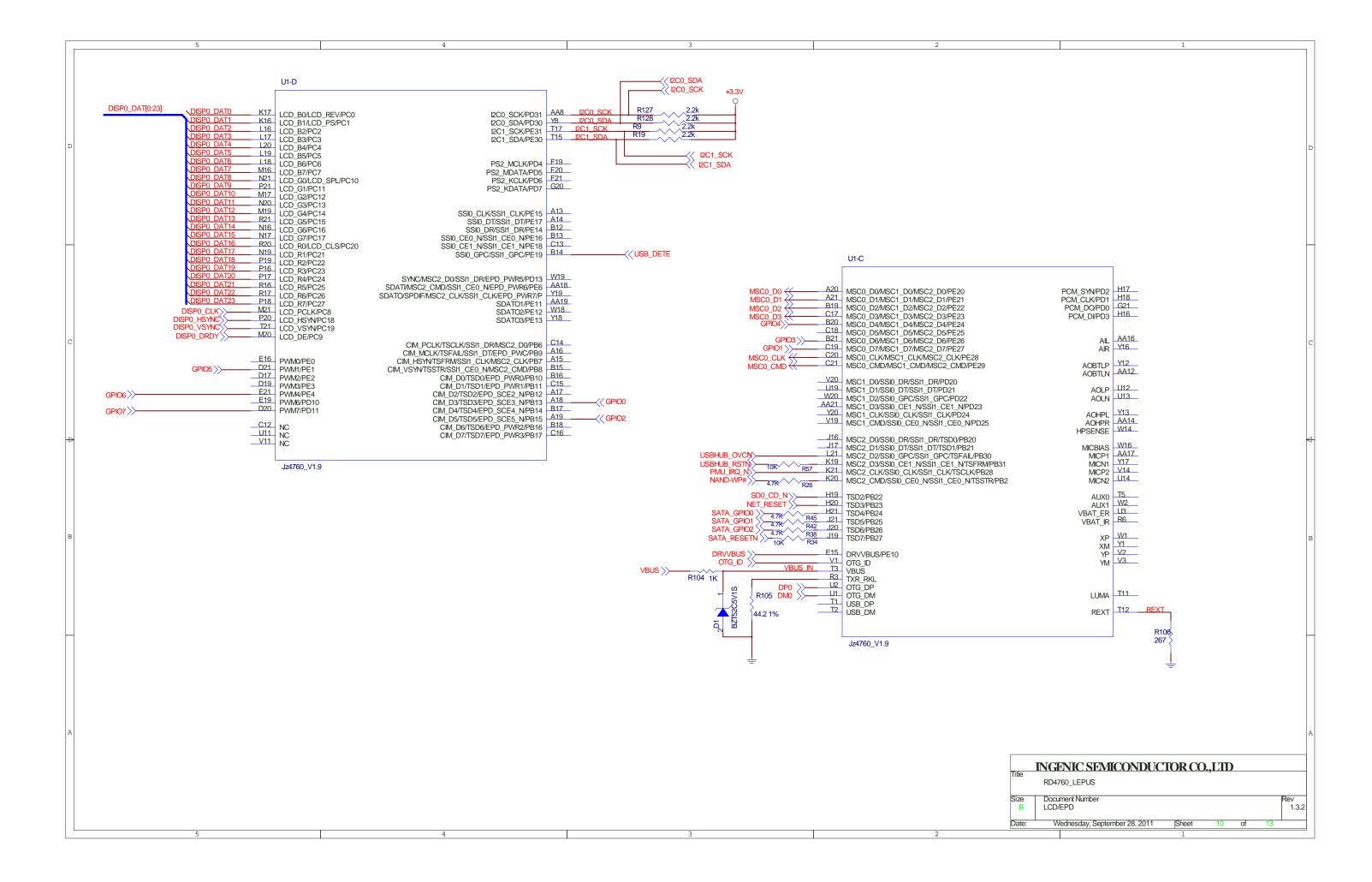


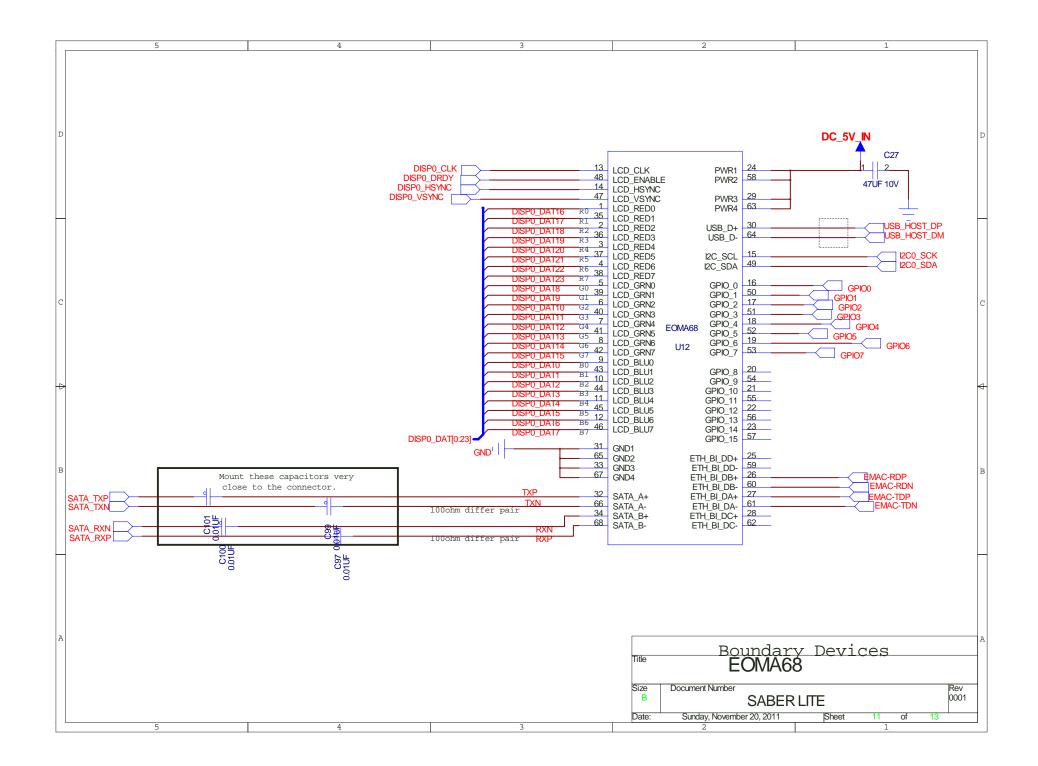


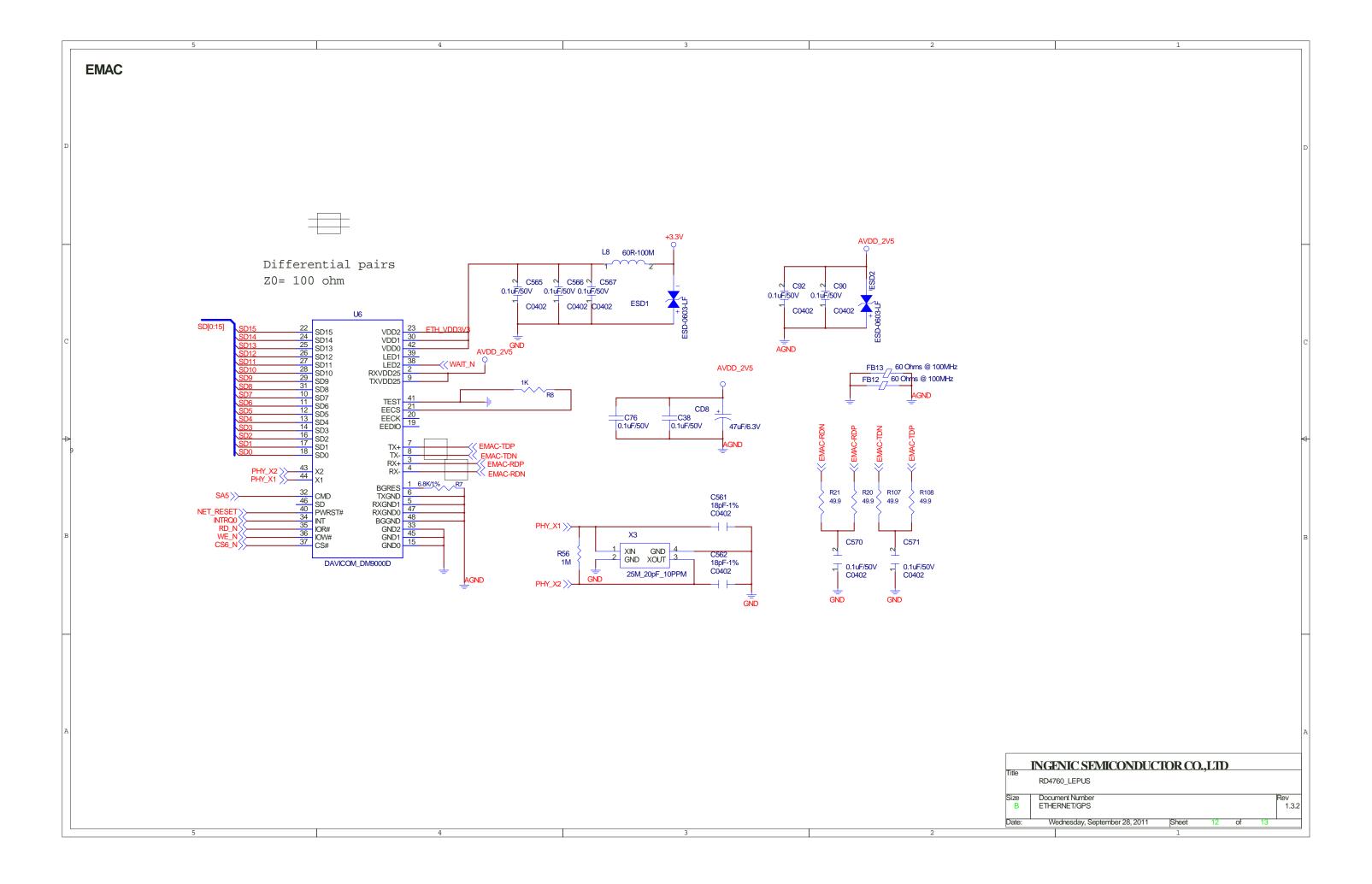












	5 Doto	Povicion	4 3 Change	2	1	
	Data	Revision	Change	1		
	Apr.20 2010	Rev1.0	1. First released Revision			
	May.21 2010	Rev1.1	1. Change jz4760_V1.6 to jz4760_V1.8			
			2. Remove reset chip IMP811 on PAGE6			
D			3. Delete net name PWM0 on PAGE7			,
			4. Change VRTC chip U9 from RT9169-33PVL to RT9169-18PVL on PAGE7			
			5. Change pullup signal on net CHARG_STAT_N from VRTC33 to BAT-V on PAGE7			
			6. Change U27 from RT9179AGB to RT9179BGB and controll is DRVVBUS on PAGE8			
			7. Add 1Kohm ID pullup resistance to +3.3V on PAGE9			
			8. Add C242~C245 for MIC1,MIC2 on PAGE9			
			9. Adjust C131~C142 to NC on PAGE9			-
			10. Remove U19,R176,C117 on PAGE8			
			11. Change 8bit and 4bit SD/MMC card pullup resistance to 10Kohm on PAGE4			
			12. Change left and right channel of J12 on PAGE10			
			13. Change C204,C205 to R178,R179 on PAGE9			
			14. Remove C198~C203 on PAGE9			
C			15. Remove C132,C136,C140 on PAGE9			
			16. Remove C134,C138,C142 on PAGE9			
			17. Change C193,C194 to R180,R181 on PAGE10			
			18. Change C143,C144 from 0.1uF to 1nF on PAGE10			
			19. Add GR14,GR15 for GU1 on PAGE14			
			1. Change R23 R27 to NC on PAGE3			
	Jun.24 2010	Rev1.1.1	2. Change net on SW11 from aux to aux1 on PAGE4			
			3. Change GF1 to BF9037 on PAGE14			
			4. Add insert detection net JD and adjust net HPSENSE on PAGE10			
			5. Adjust c43~c45 c58~c60 to 0.01uF and c46~c47 c61~c63 to 1nF on PAGE6			
			1. Change jz4760_V1.8 to jz4760_V1.9			
	Jul.15 2010	Rev1.1.2	2. Delete net CHROMA_U and CHROMA_V on PAGE 10			
	000 200		3. Delete VGA output circuit			
В			1. Change D6 from MBR0520 to WSB5819 on PAGE08			F
	Jul.23 2010	Rev1.2	2. Adjust LUMA output to AUDIO JACK 12 and delete J24 on PAGE10			
			3. Add insert detection circuit on PAGE10			
			3. Add itself detection direction AOL 10			
	Oct.29 2010	Rev1.2.1	1. Adjust R59 to 680K,R117 to 1M,R120 to 332K and R78,R93 to NC for power loose			
	Nov.5 2010	Rev1.3	อก Petrate ยงดา ite ราคาด in TP8 TP9 and add SA0 SA4 and SA5 for ethernet test on PAGE5			
	1NOV.3 2010	Nev1.5	2. Add R186,R187 for ethernet test on PAGE13 and adjust R70 to 121K on PAGE7			t
			1. Adjust R29 to 100K ohm on PAGE04			
		0 Rev1.3.1	2. Adjust +1.2V to +1.35V on PAGE06 and PAGE07			
	Dec.15 2010		3. Adjust DDR2 module name to H5PS1G63EFR-G7C on PAGE03			
			4. Add note of AVDEFUSE on PAGE06			
			5. Adjust C11 to NC and add note for mDDR on PAGE03			
A			6. Adjust U11 to S1206B on PAGE07]
	_		1. Change FB1 to R188 on PAGE06			
	Sep.28 2011	Rev1.3.2	2. Change FB13 to R189 on PAGE07	Title		
					60_LEPUS	
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	5		4 3	Date: We	dnesday, September 28, 2011 Sheet 13 of	13