

EVVOSEMI[®]

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ESD



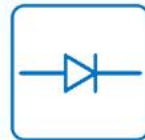
TVS



MOS



LDO



Diode



Sensor



DC-DC

Product Specification

▶ Domestic	Part Number	IRF5803
▶ Overseas	Part Number	IRF5803
▶ Equivalent	Part Number	IRF5803

EV is the abbreviation of name EVVO

P-Channel Enhancement Mode Field Effect Transistor

General Description

- High density cell design for ultra low $R_{DS(on)}$
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

Applications

- Load switch
- PWM application

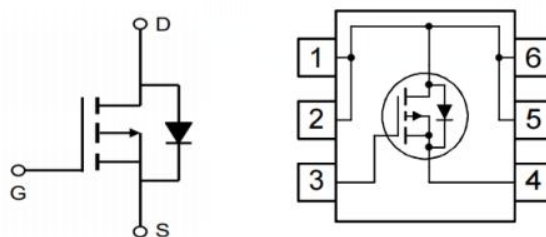
Product Summary

$$V_{DS} = -60V, I_D = -6.0A$$

$$R_{DS(ON)} < 180 \text{ m}\Omega @ V_{GS} = -10V$$

$$R_{DS(ON)} < 266 \text{ m}\Omega @ V_{GS} = -4.5V$$

SOT23-6L Pin Configuration



Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Maximum	Unit
Drain-source Voltage	V_{DS}	-60	V
Gate-source Voltage	V_{GS}	± 20	V
Drain Current	I_D	-6.0	A
Pulsed Drain Current ^A	I_{DM}	-15	A
Total Power Dissipation @ $T_A = 25^\circ\text{C}$	P_D	1.4	W
Thermal Resistance Junction-to-Ambient ^B	$R_{\theta JA}$	90	$^\circ\text{C} / \text{W}$
Junction and Storage Temperature Range	T_J, T_{STG}	-55~+150	$^\circ\text{C}$

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Electrical Characteristics (T_J=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D =-250μA	-60			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-48V, V _{GS} =0V, T _C =25℃			-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} =0V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =-250μA	-1		-2.5	V
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} = -10.0V, I _D =-1.5A		160	180	mΩ
		V _{GS} = -4.5V, I _D =-1.0A		195	266	
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0V			-1.3	V
Maximum Body-Diode Continuous Current	I _S				-2.0	A
Dynamic Parameters						
Input Capacitance	C _{iss}	V _{DS} =-15V, V _{GS} =0V, f=1MHZ		531		pF
Output Capacitance	C _{oss}			59		
Reverse Transfer Capacitance	C _{rss}			38		
Switching Parameters						
Total Gate Charge	Q _g	V _{GS} =-4.5V, V _{DS} =-20V, I _D =-1.5A		4.6		nC
Gate Source Charge	Q _{gs}			1.4		
Gate Drain Charge	Q _{gd}			1.62		
Turn-on Delay Time	t _{D(on)}	V _{GS} =-10V, V _{DS} =-15V, I _D =-1A, R _{GEN} =3.3Ω		17.4		ns
Turn-on Rise Time	t _r			5.4		
Turn-off Delay Time	t _{D(off)}			37.2		
Turn-off Fall Time	t _f			2.4		

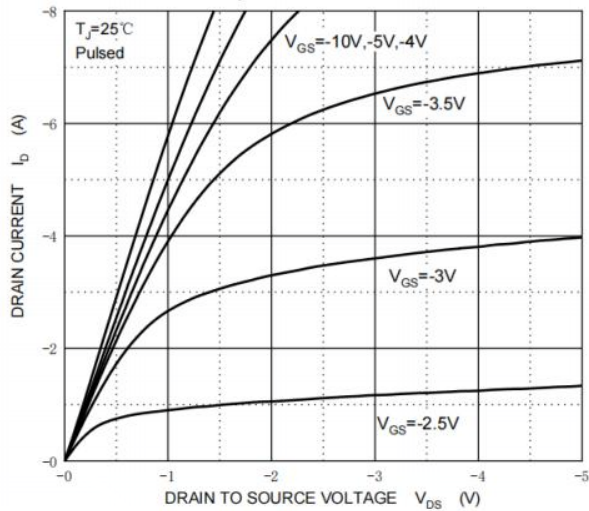
A. A.Pulse Test: Pulse Width≤300us,Duty cycle ≤2%.

B. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

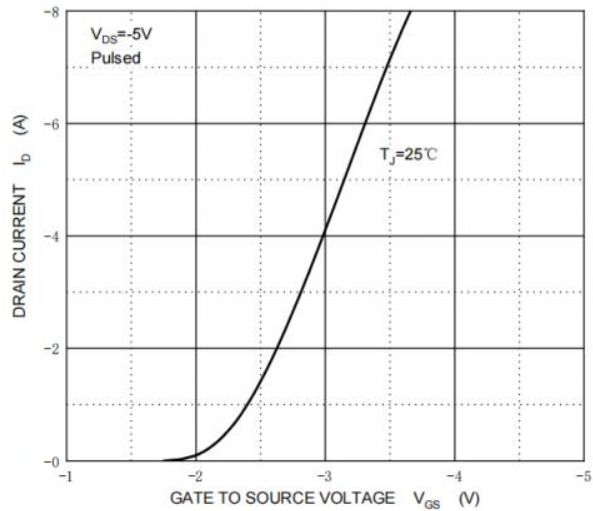
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Typical Performance Characteristics

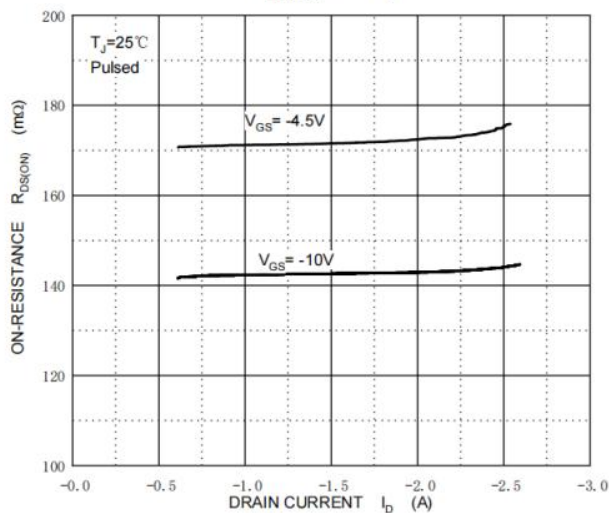
Output Characteristics



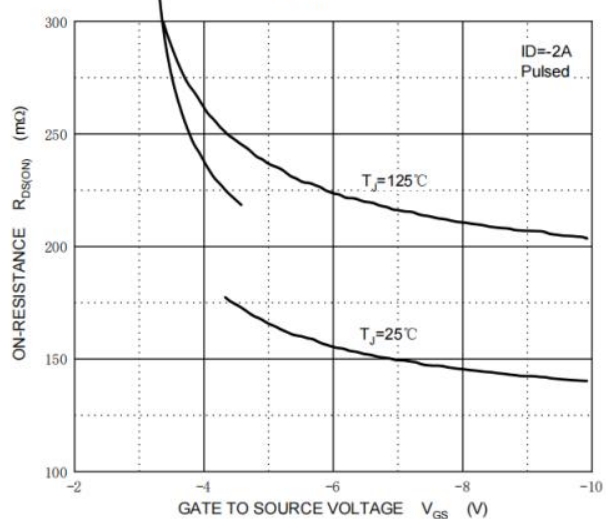
Transfer Characteristics



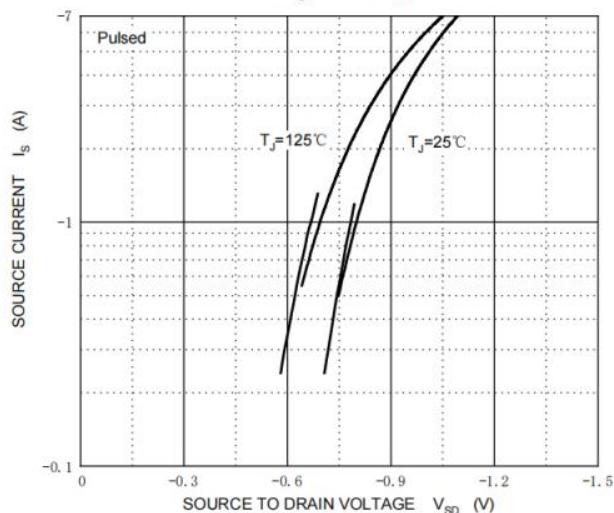
$R_{DS(ON)}$ — I_D



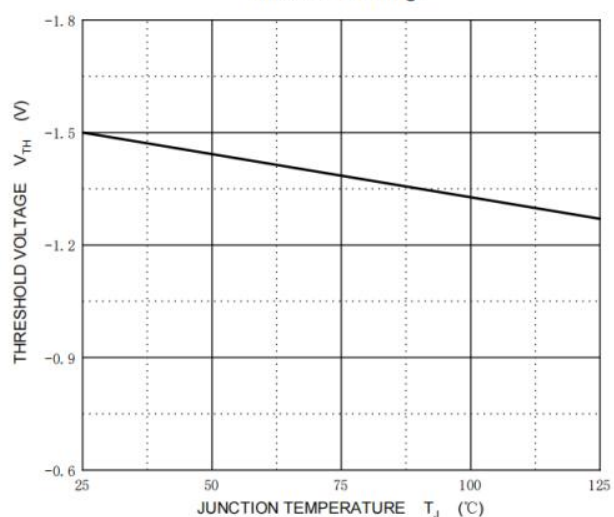
$R_{DS(ON)}$ — V_{GS}



I_S — V_{SD}

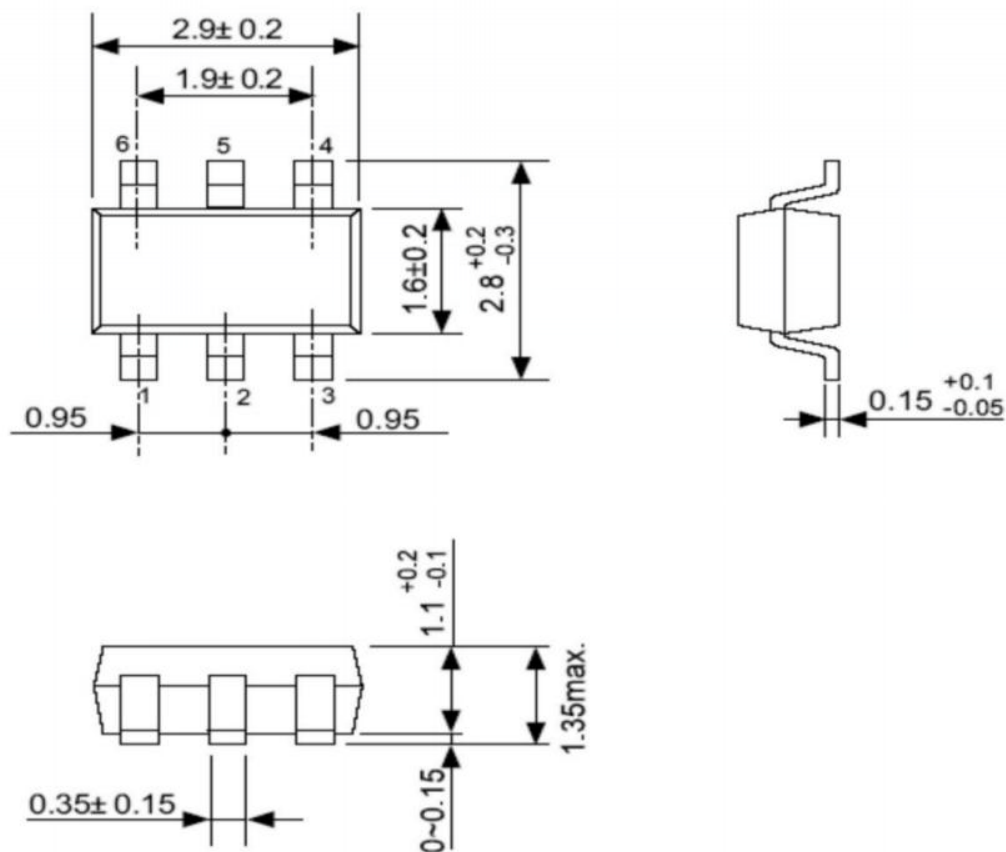


Threshold Voltage



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SOT23-6 PACKAGE INFORMATION



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