# **AN240P**

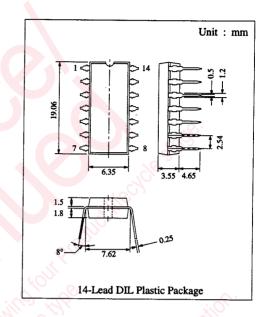
## TV Sound IF Amplifier, FM Detector Circuits

### **■** Description

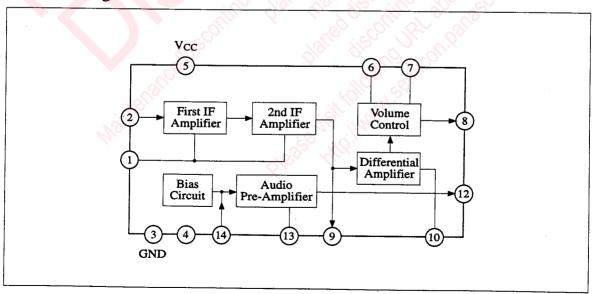
AN240P is an integrated circuit designed for Sound IF Amp and FM Detector in TV receiver sets.

#### Features

- Sound IF Amplifier
   FM Detector
   DC Volume Control Circuit
   Sound Pre-amplifier
- Differential Peak Detector Circuit
- Operates on 12V Supply



### ■ Block Diagram



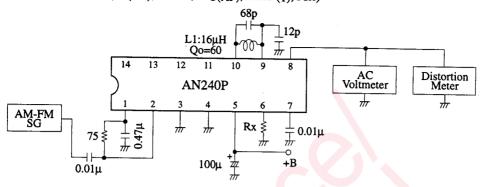
# ■ Absolute Maximum Ratings (Ta=25°C)

Item		Symbol	Rat	Unit	
Supply Voltage		Vcc	14.4		V
		V <sub>1-3</sub>	5	0	V
Voltage		V <sub>2-3, 10-3</sub>	4	-5	V
		V <sub>6-3</sub>	V <sub>5-3</sub>	5 *	V
		V <sub>7-3</sub>	V <sub>5-3</sub>	0	V
		V <sub>9-3</sub>	4	0	V
		V <sub>14-3</sub>	3	-5	V
Current		I <sub>1, 2, 14</sub>	1	-0.1	mA
		I <sub>6</sub> , 7, 9	1	-1	mA
		I <sub>8</sub>	0.5	-6	mA
		I <sub>10</sub>	1	-0.1	mA
		I <sub>12</sub>	0.5	-6	mA
		I <sub>13</sub>	1	-2	mA
Supply Current		Icc	50		mA
Power Dissipation (Ta ≤ 70 °C)		PD	445		mW
Temperature	Operating Ambient Temperature	Topr	-20 ~ +70		°C
	Storage Temperature	Tstg	-40 ~ +150		°C

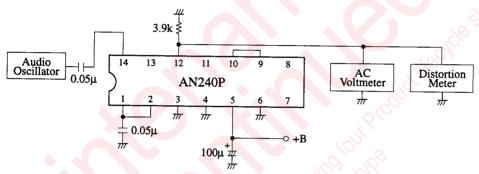
# ■ Electrical Characteristics (V<sub>CC</sub>=V<sub>5-3</sub>=12V, Ta=25°C)

	Item	Symbol	Test Cct.	Condition	min.	typ.	max.	Unit
Total Circui	t Current	I <sub>tot</sub>		10M, CO.,	17	22.5	27	mA
IF Input Vo	Itage	V <sub>1-3</sub>		70, 30, 170, 160; 16		2		V
Volume Con	ntrol	V <sub>6-3</sub>		Pin 1 - 2 shorted Pin 9 - 10 shorted		4.8	1001	V
De-emphas:	is	V <sub>7-3</sub>				6.1	$Q_{I}$	V
IF Output		V <sub>9-3</sub>		Co. Yun " Co. Fill YA		3.7		V
Audio Outp	out	V <sub>12-3</sub>	60		4.3	5.4	6.1	V
Input Limit	ing Voltage (-3dB)	V <sub>i(lim)</sub>	1	fo=4.5MHz, fm=400Hz, Δf=±25kHz	200	250	400	μVrms
AM Rejecti	on	AMR	1	fo=4.5MHz, fm=400Hz, Δf=±25kHz AM=400Hz 30%, Vi=100mVrms, Rx=0	40	50		dB
Input Imped	dance	R <sub>i(IF)</sub>		fo=4.5MHz, Pin 1-2		20		kΩ
Input Imped	- CV	Ci(IF)		fo=4.5MHz, Pin 1-2		4.7		pF
Output Imp	edance	Ro(IF)		fo=4.5MHz, Pin 9-3		3.25		kΩ
Output Imp		C <sub>O(IF)</sub>		fo=4.5MHz, Pin 9-3		10		pF
Demodulati	ion Output	V <sub>O(AF)</sub>	1	fo=4.5MHz, fm=400Hz, Δf=±25kHz	0.6	0.8	1.2	Vrms
THD (Demod.)		THD <sub>(1)</sub>	1	Vi=100mVrms, Rx=0		0.9	2.0	%
Output Resistance Pin 7		Ro		f=400Hz		7.5		kΩ
Output Resistance Pin 8		Ro		f=400Hz		300		Ω
Attenuation	(max.)	Att	1	fo=4.5MHz, fm=400Hz, Δf=±25kHz Vi=100mVrms, Rx=∞	60	80		dB
	Gain	Gv(AF)	2	f=400Hz, Vi=100mVrms	17.5	20	23	dB
	THD	THD <sub>(2)</sub>	2	f=400Hz, Vi=2Vrms		1.5		%
AF Pre-Amp	Output	Vo	2	f=400Hz, THD=5%	2	2.5		Vrms
P	Input Resistance	Ri(AF)		f=400Hz		50		kΩ
	Output Resistance	R <sub>O(AF)</sub>		f=400Hz		270		Ω

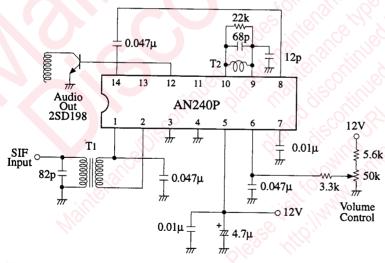
Test Circuit 1 (V<sub>i(lim)</sub>, AMR, V<sub>O(AF)</sub>, THD<sub>(1)</sub>, Att)



Test Circuit 2 (G<sub>V(AF)</sub>, THD<sub>(2)</sub>, V<sub>O</sub>)



### ■ Application Circuit



#### Coil Specifications

Symbol	Diagram	Turns of coil	Kind of wire	Core
Tı	N2 (3) (4) (4) (N1) (6) (6)	Ni 46T	2UEW 0.08	
		N2 6T	2UEW 0.08	L(④ - ⑥): 13µH±10%
T2	3-mm-4 2	48.5T	2UEW 0.08	L(③-④):13-8µH±10%
	① ③			Σ(⊕ - ⊕ ). 13-оμп±10%

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