VOLTAGE DETECTING, SYSTEM RESETTING IC SERIES

DESCRIPTION

M51955A,B/M51956A,B are semiconductor integrated circuits ideal for detecting input voltage and resetting all types of logic circuits such as CPUs.

They include a built-in delay circuit to provide a retardation time ($200\mu\text{sec}$ typ.).

They find extensive applications, including circuits for battery checking, level detecting and waveform shaping.

FEATURES

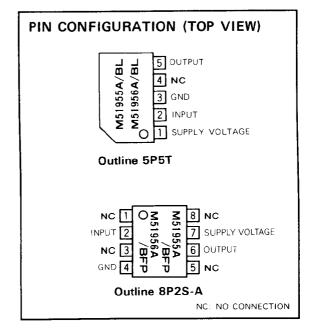
- Few external parts

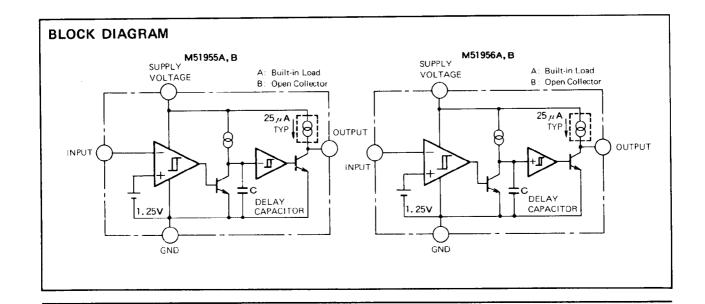
- Sudden change in power supply has minimal effect on the ICs
- Wide operation range of detecting input pin . . . Narrower ranges of ~0.3V \sim V_{cc} or ~0.3V \sim 7V (Input voltage detecting type) (M51955B, M51956B)
- Suitable for high supply voltage circuit with simple circuit structure (M51955B, M51956B)
- Permits easy configuration of circuit for protection against reverse connection or surges. (M51955B, M51956B)
- Wide application range
- SIL package of the same height as DIP (5-pin SIP)

APPLICATION

Reset circuit of Pch, Nch, CMOS, microcomputer, CPU and microcomputer, Reset of logic circuit, Battery check circuit, Switching circuit back-up voltage, Level detecting circuit, Waveform shaping circuit, Delay waveform generating circuit, DC-DC converter, Over voltage protection circuit.

RECOMMENDED OPERATING CONDITION

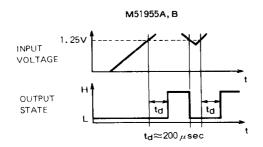


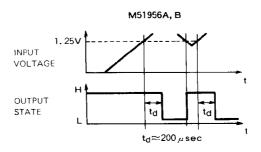




VOLTAGE DETECTING, SYSTEM RESETTING IC SERIES

FUNCTION DIAGRAM





ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C, unless otherwise noted)

Symbol	Parameter	Conditions		Ratings	Unit	
Vcc	Supply voltage			18	V	
İsink	Output Sink Current			6	mΑ	
Vo	Output voltage	A Type (Output with constant current load)		Vcc		
		B Type (Open colle	ector output)	18	V	
Pd	Power dissipation	5P SIL		450	m.W	
		8P FLAT		300		
	Thermal Derating	T 05.0	5P SIL	4.5	mW/C	
K _#		Ta ≥ 25 C	8P FLAT	3		
Topr	Operating temperature			- 30 ~ + 85	С	
Tstg	Storage temperature			-40~ +125	С	

ELECTRICAL CHARACTERISTICS (Ta = 25°C. unless otherwise noted)

"L" reset type	"H" reset type		
M51955A	M51956A		
M51955B	M51956B		

Symbol		Test conditions		Limits			1 11.5
	Parameter			Min	Тур	Max	1 Unit
Vs	Detecting voltage			1.20	1.25	1.30	V
∆Vs	Hysterisis voltage	$V_{CC} = 5V$		9	15	23	mV
Vs/4T	Detecting Voltage Temperature Coefficient				0.01	_	%/°C
Vcc	Supply Voltage Range	Ta = -30 ~ +85 ℃		2		17	V
VIN	Input voltage Range	Ta = -30 ~ +85 °C, V _{CC} ≤ 7V		-0.3		Vcc	_ v
		Ta = -30 ~ +85 ℃. Vo	00 > 7 V	-0.3		7	
lin	Input Current	V _{IN} =1.25V		_	100	500	nΑ
lcc	Circuit Current	Type A V _{CC} = 5V			390	590	u A
		Type B V _{CC} = 5V			360	540	
tpd	Delay Time	Ta = -30 ~ +85 °C		80	200	500	μS
Vsat	Output Saturation Voltage	L reset type V _{CC} = 5V, V _{IN} < 1.2V, I _{Sink} = 4mA			0.2	0.4	٧
		H reset type V _{CC} = 5V, V _{IN} > 1.35V, Isink = 4mA					
Vopl	Threshold Operating Voltage	L reset type minimum	$R_L = 2.2 k \Omega$, $V_{sat} \le 0.4 V$	-	0.67	0.8	V
		supply voltage for IC operation	R _L = 100k Ω. Vsat ≦ 0.4V	/ -	0.55	0.7	
Іон	Output Leakage Current	Туре В			-	30	nΑ
		Type 8. Ta = -30 - +85 ℃			_	1	μΑ
loc	Output Load Current	Type A $V_{CC} = 5V$, $V_O = 1/2 V_{CC}$		- 40	25	— 17	μΑ
VoH	Output High Voltage	Type A		V _{CC} - 0.2	V _{CC} - 0.06		٧

Note: Delay time can be changed by changing delay capacitor for external deray capacitor types (Please refer to typical characteristics.)



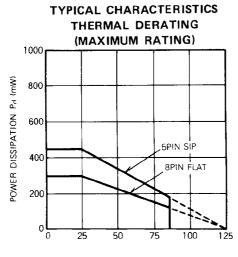
DETECTING VOLTAGE VS.

AMBIENT TEMPERATURE

(Input voltage detecting series)

VOLTAGE DETECTING, SYSTEM RESETTING IC SERIES

TYPICAL CHARACTERISTICS



AMBIENT TEMPERATURE Ta (°C)

DETECTING VOLTAGE V_s (V) 1.24 1, 23

40 -20

0

1.28

1, 27

1, 26

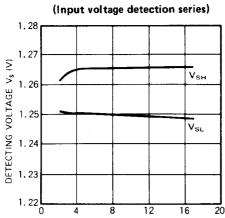
1.25

AMBIENT TEMPERATURE Ta (°C)

80 100

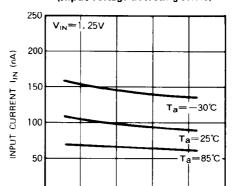
20 40 60

DETECTION VOLTAGE VS. SUPPLY VOLTAGE



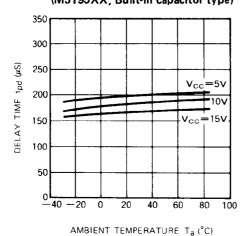
SUPPLY VOLTAGE VCC (V)

INPUT CURRENT VS. **SUPPLY VOLTAGE** (Input voltage detecting series)

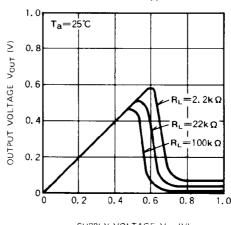


SUPPLY VOLTAGE VCC (V)

DELAY TIME VS. AMBIENT TEMPERATURE (M5195XX, Built-in capacitor type)



THRESHOLD OPERATING VOLTAGE ([L] reset type)

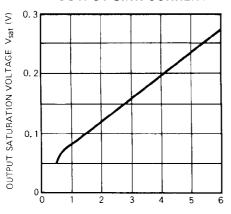


SUPPLY VOLTAGE V_{CC} (V)



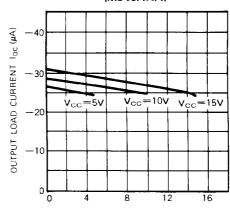
VOLTAGE DETECTING, SYSTEM RESETTING IC SERIES

OUTPUT SATURATION VOLTAGE VS. OUTPUT SINK CURRENT



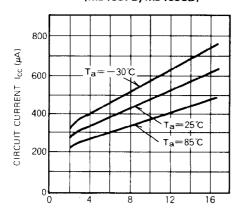
OUTPUT SINK CURRENT Isink (mA)

OUTPUT LOAD CURRENT VS. OUTPUT VOLTAGE (M519XXA)



OUTPUT VOLTAGE Vo (V)

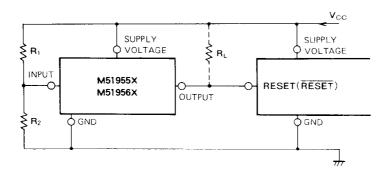
CIRCUIT CURRENT VS. SUPPLY VOLTAGE (M51957B, M51958B)



SUPPLY VOLTAGE V_{CC} (V)

VOLTAGE DETECTING, SYSTEM RESETTING IC SERIES

EXAMPLE OF APPLICATION CIRCUIT M5195XX Series Reset Circuit



Note 1. When the detecting supply voltage is 4.25V, M51951, M51952, M51953 and M51954 are used. In the case, R_1 and R_2 are not necessary. When the voltage is anything except 4.25V, M51955, M51956, M51957 and M51958 are used. In this case,

the detecting supply voltage is 1.25 $\times \frac{R_1 + R_2}{R_2}$ (V)

approximately. The detecting supply voltage can be set between 2V and 15V.

Note 2. When the delay time is short, M51951, M51952, M51955 and M51956 are available. These ICs have a delay capacity and the delay time is about $200 \mu s$. If a longer delay time is necessary, M51953, M51954,

M51957 and M51958 are used. In the case, the delay time is about $0.34 \times Cd(pF)\mu sec$.

Note 3. If M5195XX and the logic circuit have a common power supply, type A (built-in load type) can be applied whether a pull-up resister is included in the logic circuit or not.

Note 4. The logic circuit preferably should not have a pull-down resistor, but if one is present, add load resistor R_{\perp} to overcome the pull-down resistor.

Note 5. When the reset terminal in the logic circuit is of the low reset type, M51951, M51953, M51955 and M51957 are used and when the terminal is of the high reset type, M51952, M51954, M51956 and M51958 are used.

Note 6. When a negative supply voltage is used, supply voltage side of M5195XX and the GND side are connected to negative supply voltage respectively.

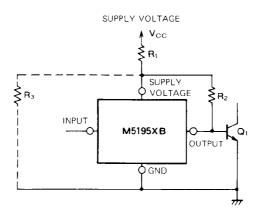


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Application to High Supply Voltage Circuit

The absolute maximum rating of supply voltage for M51955B, M51956B is 18V. By dividing supply voltage

using resistors, these ICs can be used in high supply voltage circuit.



In the above figure, the voltage applied to M5195XB is as follows. The voltage range is set between 2V and 17V.

at Q₁ ON:
$$\frac{\mathsf{R}_2 \cdot [\frac{\mathsf{R}_3}{(\mathsf{R}_1 + \mathsf{R}_3)} \cdot \mathsf{V}_{\mathsf{CC}} - (\mathsf{R}_1 /\!/ \mathsf{R}_3) \cdot \mathsf{I}_{\mathsf{CC}}] + (\mathsf{R}_1 /\!/ \mathsf{R}_3) \cdot \mathsf{V}_{\mathsf{BEI}}}{\mathsf{R}_2 + (\mathsf{R}_1 /\!/ \mathsf{R}_3)}$$

at Q₁ OFF:
$$\frac{\mathsf{R}_2 \, \cdot \, [\, \frac{\mathsf{R}_3}{(\mathsf{R}_1 + \mathsf{R}_3)} \cdot \mathsf{V}_{\mathsf{CC}} \, - (\mathsf{R}_1 /\!/ \mathsf{R}_3) \, \cdot \, \mathsf{I}_{\mathsf{CC}}}{\mathsf{R}_2 \, + (\mathsf{R}_1 /\!/ \mathsf{R}_3)}$$

$$R_1/\!/R_3 \equiv \frac{R_1 \cdot R_3}{R_1 + R_3}$$

V_{cc}: Circuit current of M5195XB

 V_{BEI} : Base-emitter voltage $\approx 0.7V$ (Transistor Q_1)

This circuit provides reverse protection (in case of reverse connection of power supply) and surge protection.

Using the application circuit, the directly rectified or smoothing commercial voltage can be applied as shown below.

