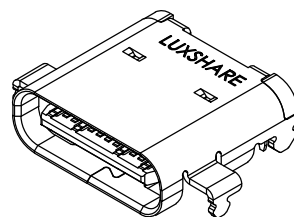
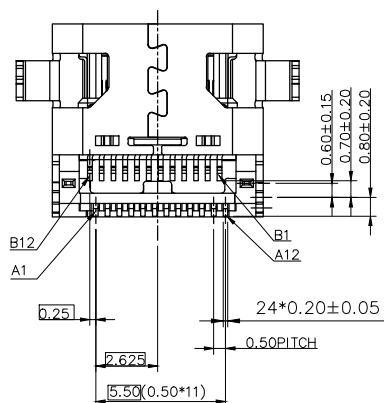
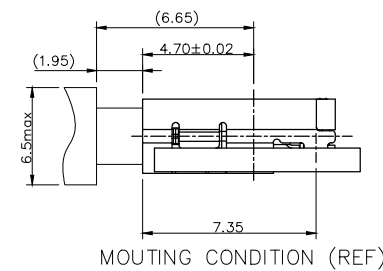
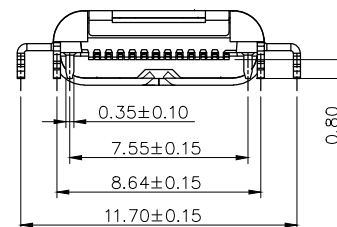
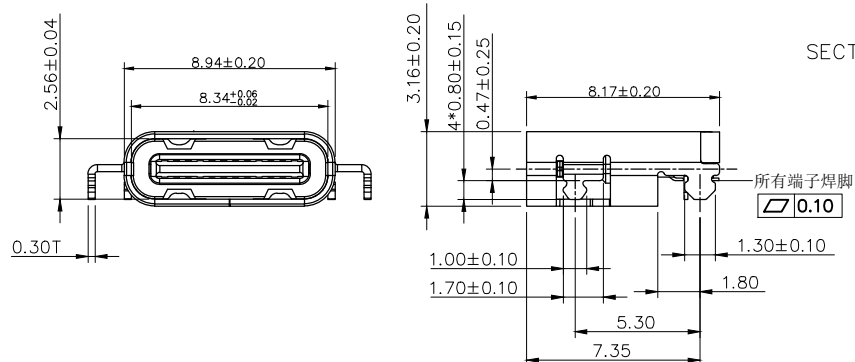
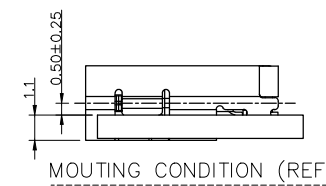
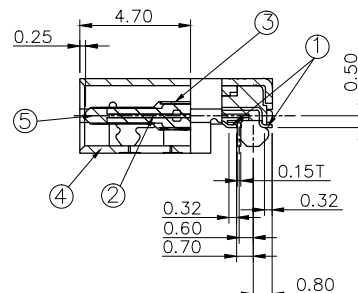
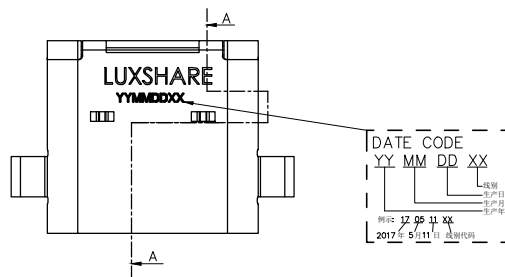





REV.	DESCRIPTION	DESIGN	APPROVAL	DATE
X1	NEW DRAWING	JIANGTAO XUE	DENNIS TAN	2017.06.08



NO.	PART NAME	MATERIAL	FINISH	Q'TY
5	HOUSING	HEAT RESISTING PLASTIC	BLACK,UL 94V-0	1
4	SHELL	STAINLESS STEEL	PLATING Ni 30u"MIN	1
3	EMI PLATE	STAINLESS STEEL	—————	1
2	GND PLATE	STAINLESS STEEL	PLATING Ni 80u"Min	1
1	CONTACT	COPPER ALLOY	MATING AREA Au 30u"MIN ; SOLDER TAIL AU 1U" MIN NI 80u"MIN UNDERPLATED	24

X.±	X.°±1°	Customer P/N: Customer REV.:	LUXSHARE-ICT SpeedTech			
.X± 0.25	.X°±					
.XX± 0.15	.XX°±					
.XXX±0.10	.XXX°±					
UNITS:	mm	PART NO. PUC050-2483-423-1H	TITLE: USB TYPE C FEMALE			
THESE DRAWINGS AND SPECIFICATIONS ARE THE PROPERTY OF LUXSHARE-ICT AND SHALL NOT BE REPRODUCED, COPIED OR CUED IN ANY MANNER WITHOUT THE PRIOR WRITTEN CONSENT OF LUXSHARE-ICT		APPD: DENNIS TAN 2017.06.08	DWG NO: C-UC37-01			
		CHKD: DENNIS TAN 2017.06.08				
		DRAW: JIANGTAO XUE 2017.06.08		SCALE 1:1	SHEET 1/2	REV. X1

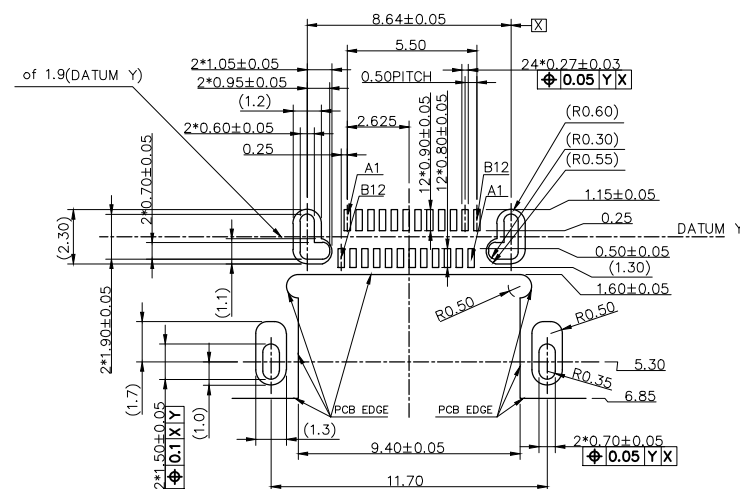
Customer P/N:	LUXSHARE-ICT SpeedTech			
Customer REV.:				
PART NO. PUC050-2483-423-1H	TITLE: USB TYPE C FEMALE			
APPD: DENNIS TAN 2017.06.08	DWG NO: C-UC37-01			
CHKD: DENNIS TAN 2017.06.08				
DRAW: JIANGTAO XUE 2017.06.08		SCALE 1:1	SHEET 1/2	REV. X1

	SCALE	SHEET	REV.
	1:1	1/2	X1

REV.	DESCRIPTION	DESIGN	APPROVAL	DATE
X1	NEW DRAWING	JIANGTAO XUE	DENNIS TAN	2017.06.08

Pin Assignments

Pin	Signal Name	Description	Mating Sequence	Pin	Signal Name	Description	Mating Sequence
A1	GND	Ground return	First	B12	GND	Ground return	First
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	Second	B11	SSRXp1	Positive half of first SuperSpeed RX differential pair	Second
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	Second	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair	Second
A4	VBUS	Bus Power	First	B9	VBUS	Bus Power	First
A5	CC1	Configuration Channel	Second	B8	SBU2	Sideband Use	Second
A6	Dp1	Positive half of the USB2.0 differential pair—Position 1	Second	B7	Dn2	Negative half of the USB2.0 differential pair—Position 2	Second
A7	Dn1	Negative half of the USB2.0 differential pair—Position 1	Second	B6	Dp2	Positive half of the USB2.0 differential pair—Position 2	Second
A8	SBU1	Sideband Use	Second	B5	CC2	Configuration Channel	Second
A9	VBUS	Bus Power	First	B4	VBUS	Bus Power	First
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	Second	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair	Second
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	Second	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair	Second
A12	GND	Ground return	First	B1	GND	Ground return	First



RECOMENDED PCB LAYOUT
PCB T: 0.50mm
TOL: ± 0.05

NOTES:

- 1.ELECTRICAL CHARACTERISTICS:
- 1-1.CURRENT RATING:VBUS AND GND CONTACTS RATED AT 10A,
PARALLELED FOR A TOTAL OF 1.25A;;
 - 1-2.INITIAL CONTACT RESISTANCE:40mΩ Max.;
 - 1-3.INSULATION RESISTANCE:100MΩ Min. AT 500V DC
 - 1-4.DIELECTRIC WITHSTANDING VOLTAGE:100V AC ONE MINUTE.
- 2.MECHANICAL CHARACTERISTICS:
- 2-1.MATING FORCE:5~20N;
 - 2-2.UNMATING FORCE:8~20N(INITIAL),6~20N(AFTER 1000CYCLES);
 - 2-3.FREQUENCY:500±50 CYCLES/HOUR,10000 CYCLES;
- 3.ENVIRONMENTAL:
- OPERATING TEMPERATURE: -55℃ TO +85℃;
 - 4.HARMFUL MATERIAL AND PROCESS CONTROL,PLEASE FOLLOW
DOC. NO."QW-Q-043"&RoHS 2.0&HF;