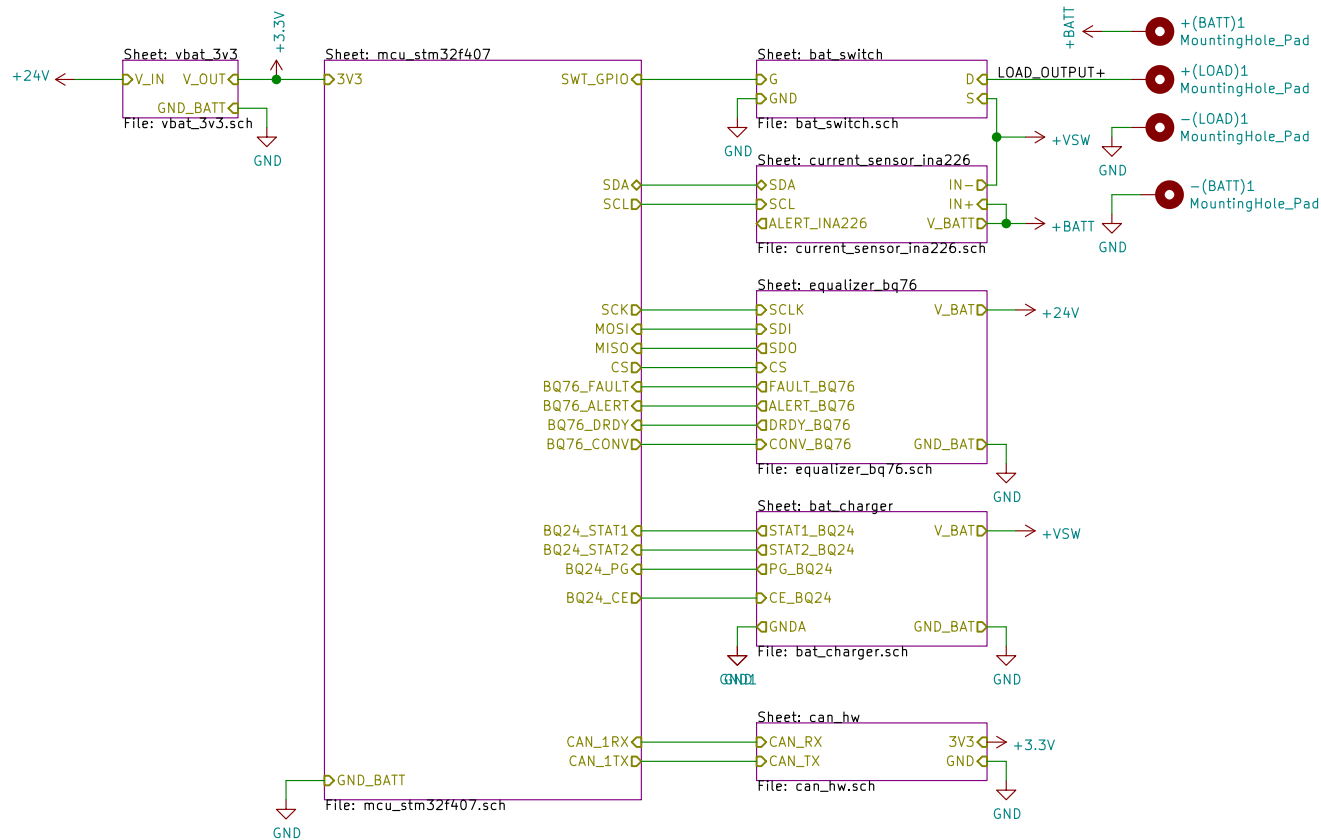


- H1 MountingHole H3 MountingHole
H2 MountingHole H4 MountingHole



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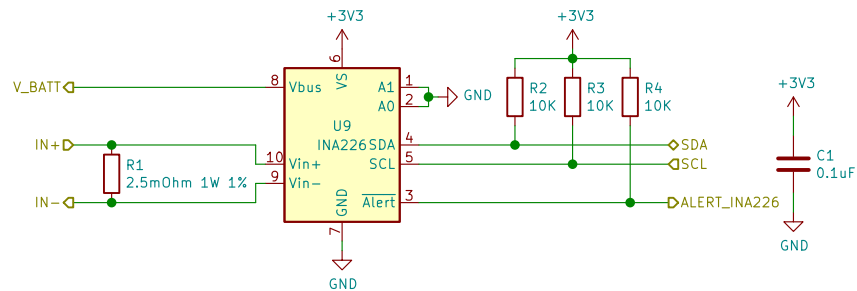
Ceccarelli Federico, Moya Martin, Santos Lucio

Sheet: /
File: bms_hardware.sch

Title: Battery Managment System (MBS)

Size: A4 Date: 2021-03-03
KiCad E.D.A. kicad 5.1.10-88a1d61d5890ubuntu20.04.1

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NOTAS

- La corriente maxima esperada a consumir es de alrededor de 20A. Por lo tanto, la resistencia calculada es de 2.5mOhm 1W 1%
- La dirección I2C predefinida por los pines A0 y A1 es la 0x80, ya que ambos pines están conectados a masa.
- La entrada de corriente no se filtra ya que no se pretenden corrientes fluctuantes que superen a la tasa de muestre del ADC (500KHz)

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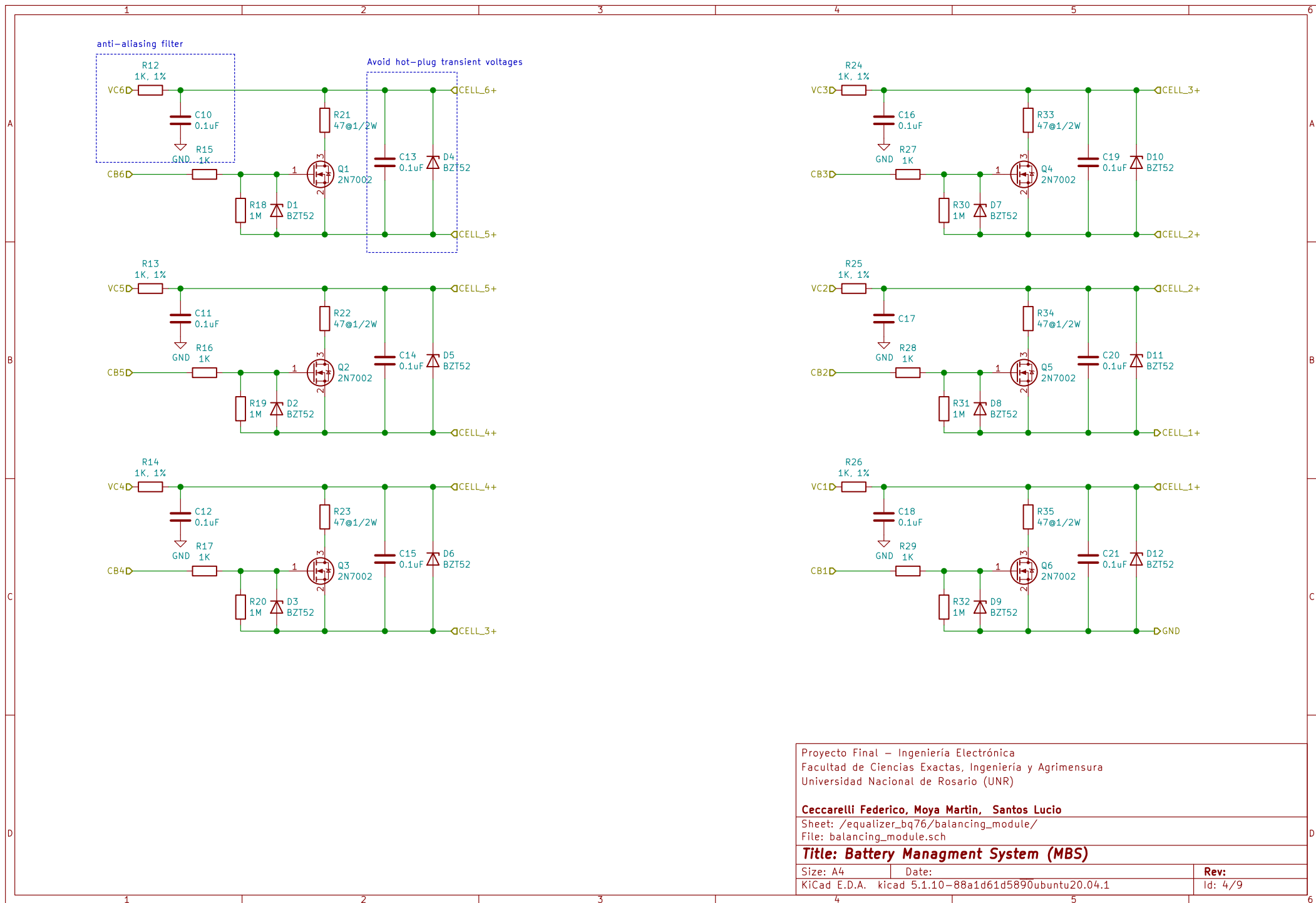
Ceccarelli Federico, Moya Martin, Santos Lucio

Sheet: /current_sensor_ina226/
File: current_sensor_ina226.sch

Title: Battery Managment System (MBS)

Size: A4 Date:
KiCad E.D.A. kicad 5.1.10-88a1d61d5890ubuntu20.04.1

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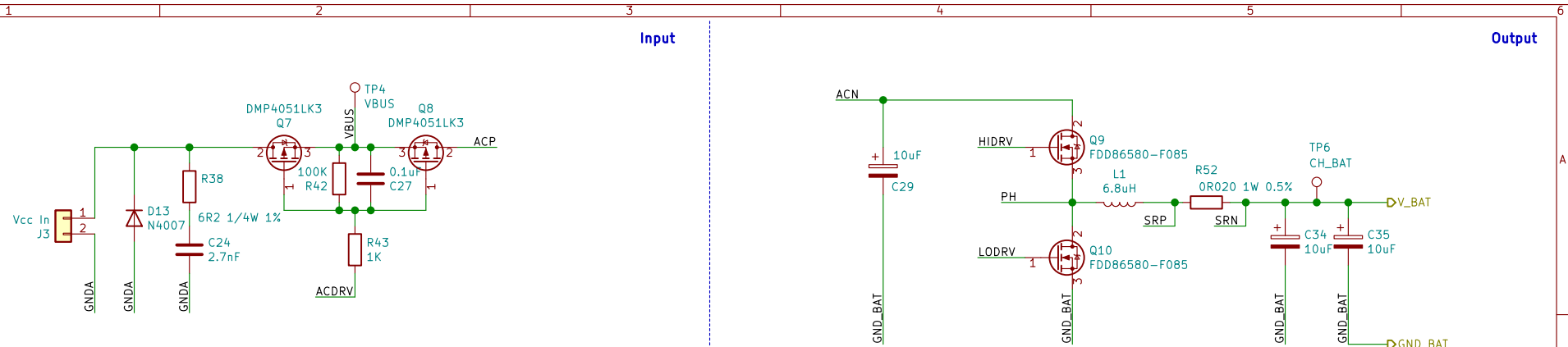
Ceccarelli Federico, Moya Martin, Santos Lucio

Sheet: /equalizer_bq76/balancing_module/
 File: balancing_module.sch

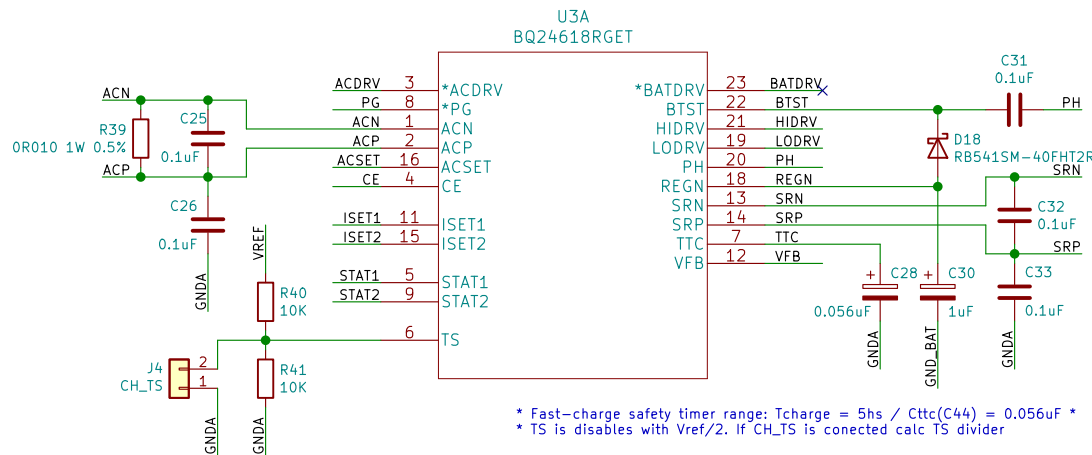
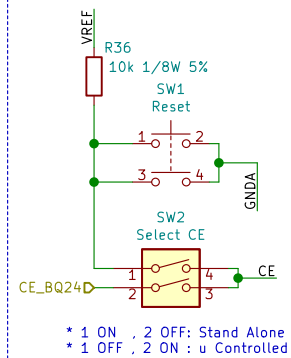
Title: Battery Management System (MBS)

Size: A4 Date: Kicad E.D.A. kicad 5.1.10-88a1d61d5890ubuntu20.04.1

Rev: Id: 4/9

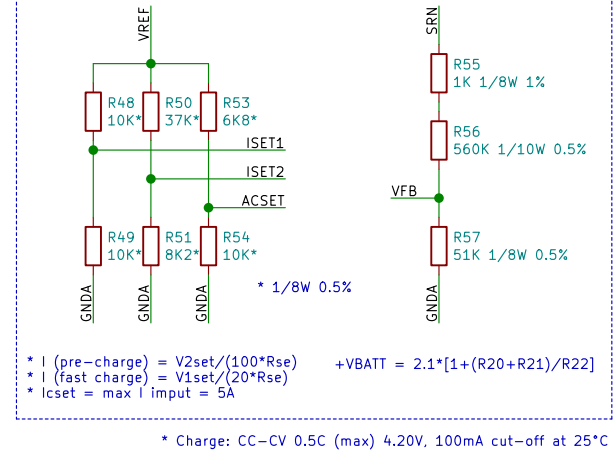


Reset & CE Source Selector

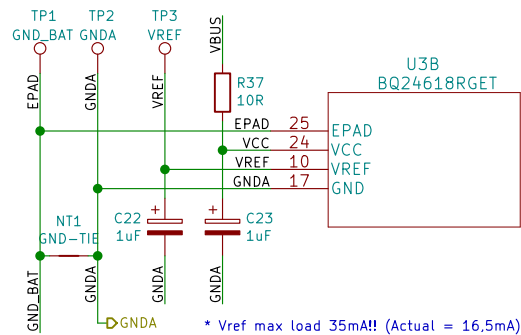
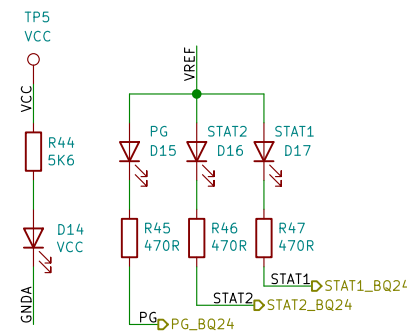


* Fast-charge safety timer range: $T_{charge} = 5hs / Ct_{tc}(C44) = 0.056uF$ *
* TS is disables with $V_{ref}/2$. If CH_TS is connected calc TS divider

I & V Charging Set Points



Status LEDs & Signals



* Vref max load 35mA!! (Actual = 16,5mA)

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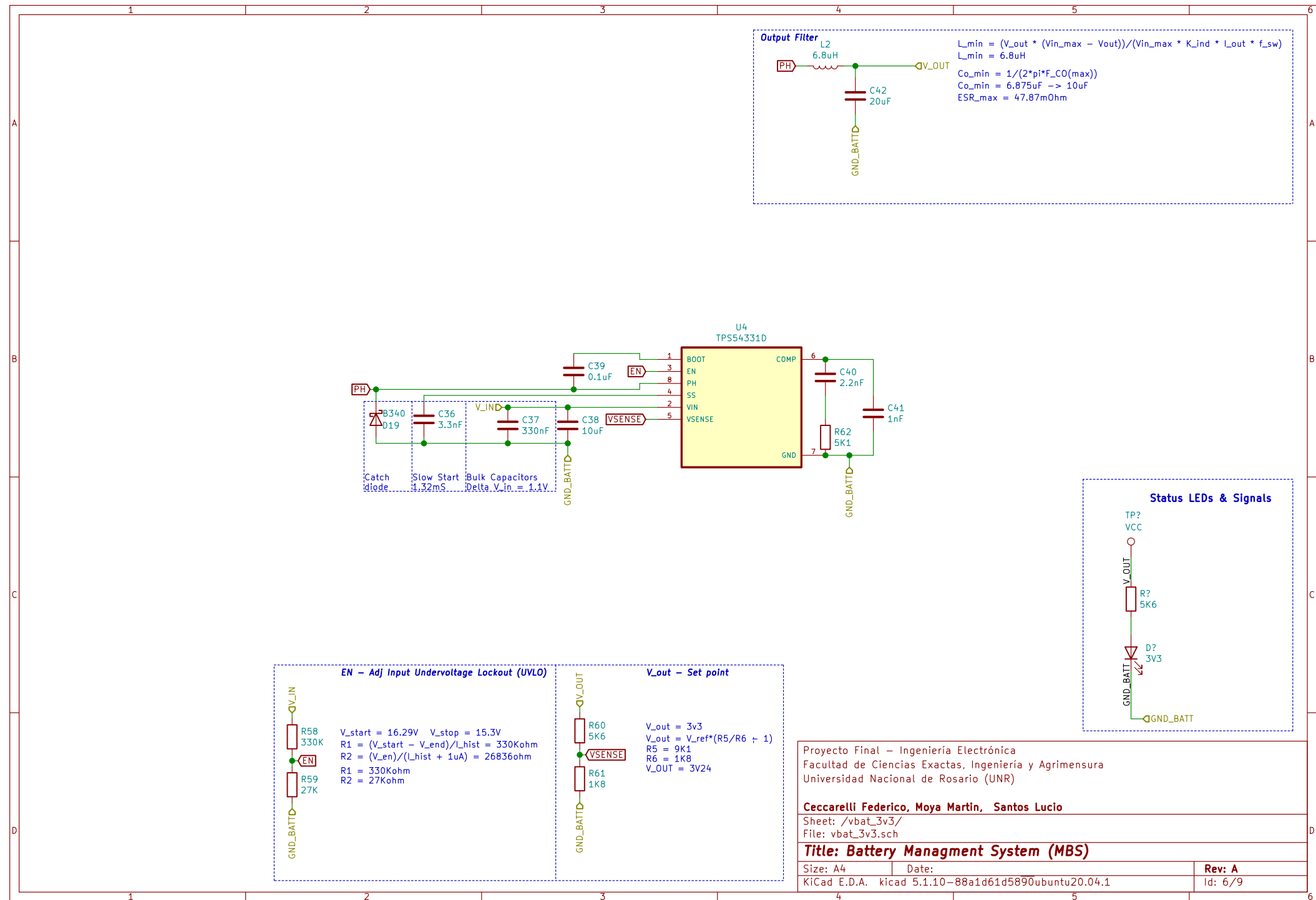
Ceccarelli Federico, Moya Martin, Santos Lucio

Sheet: /bat_charger/
File: bat_charger.sch

Title: Battery Managment System (MBS)

Size: A4 Date:
KiCad E.D.A. kicad 5.1.10-88a1d61d5890ubuntu20.04.1

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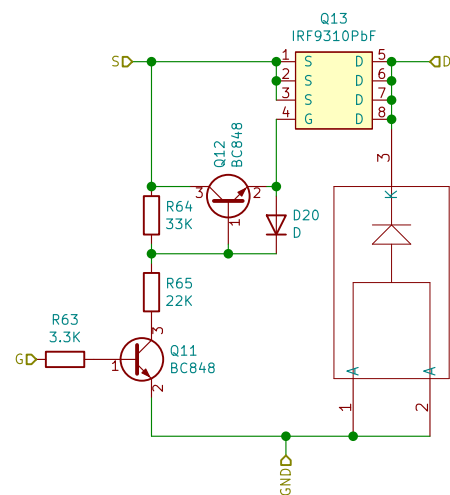
Ceccarelli Federico, Moya Martin, Santos Lucio

Sheet: /vbat_3v3/
 File: vbat_3v3.sch

Title: Battery Managment System (MBS)

Size: A4 Date: KiCad E.D.A. kicad 5.1.10-88a1d61d5890ubuntu20.04.1

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U5
FERD2045SB-TR
<https://ar.mouser.com/datasheet/2/389/ferd2045s-1849465.pdf>

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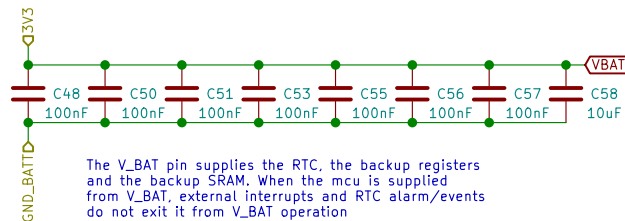
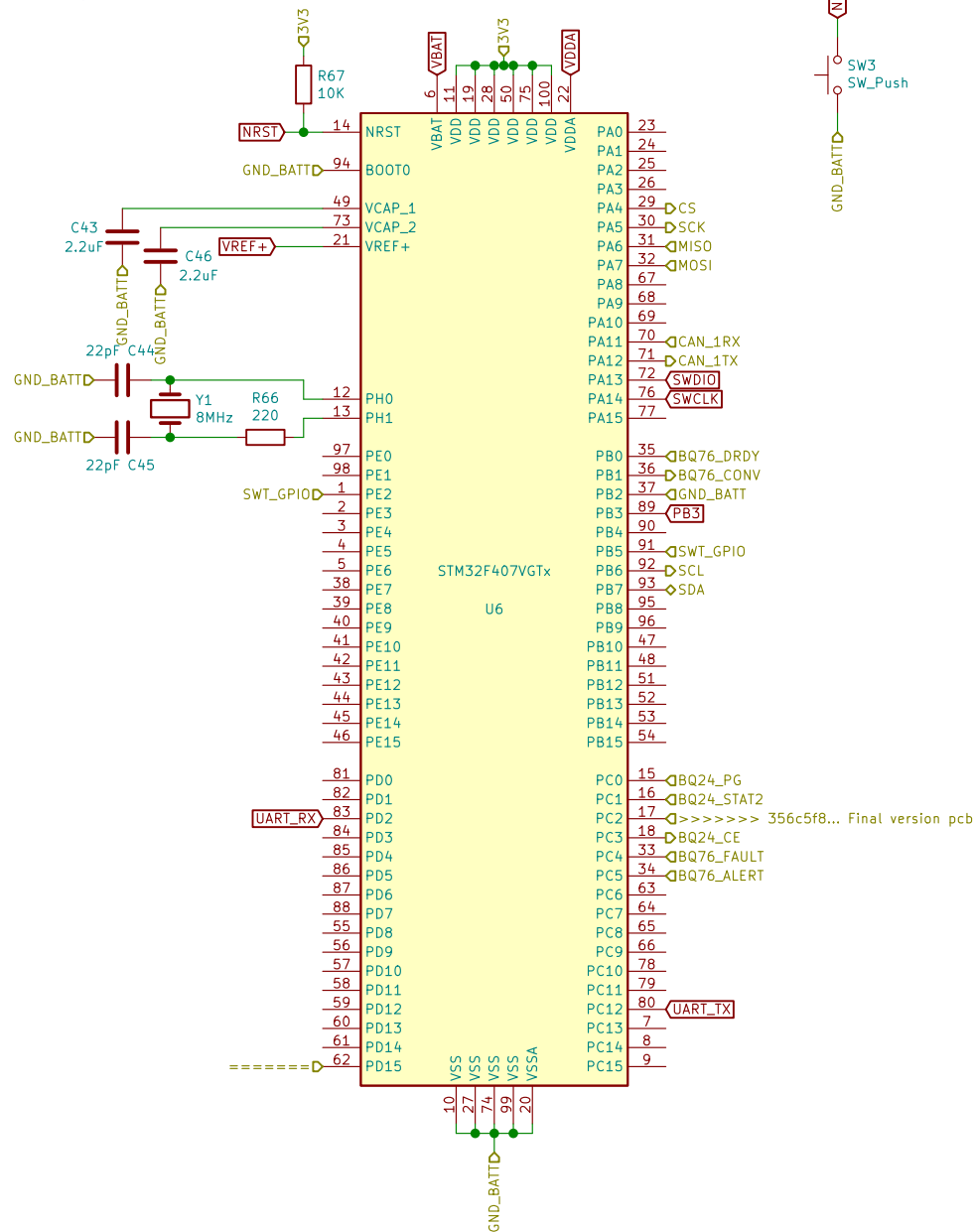
Sheet: /bat_switch/
File: bat_switch.sch

Title: Battery Managment System (MBS)

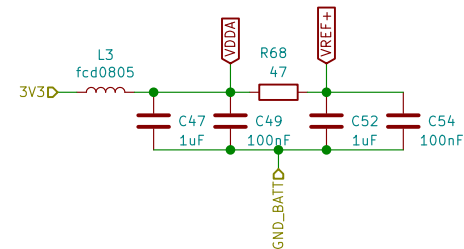
Size: A4 Date: KiCad E.D.A. kicad 5.1.10-88a1d61d5890ubuntu20.04.1

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BOOT pins configuration
 BOOT 0 BOOT 1 MODE
 0 X Main flash memory boot <- Selected
 1 0 System Memory boot (boot loader)
 1 1 RAM memory

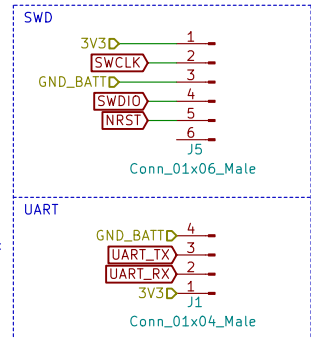


The V_BAT pin supplies the RTC, the backup registers and the backup SRAM. When the mcu is supplied from V_BAT, external interrupts and RTC alarm/events do not exit it from V_BAT operation



The VDD pins must be connected to VDD with external decoupling capacitors: one single Tantalum or Ceramic capacitor (min. 4.7 μ F typ. 10 μ F) for the package + one 100 nF Ceramic capacitor for each VDD pin.
 The VBAT pin can be connected to the external battery (1.65 V < VBAT < 3.6 V). If no external battery is used, it is recommended to connect this pin to VDD with a 100 nF external ceramic decoupling capacitor.
 The VDDA pin must be connected to two external decoupling capacitors (100 nF Ceramic + 1 μ F Tantalum or Ceramic).

Additional precautions can be taken to filter analog noise:
 -VDDA can be connected to VDD through a ferrite bead.
 -The VREF+ pin can be connected to VDDA through a 47ohm resistor



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Sheet: /mcu_stm32f407/
 File: mcu_stm32f407.sch

Title: Battery Managment System (MBS)

Size: A4 Date: KiCad E.D.A. kicad 5.1.10-88a1d61d5890ubuntu20.04.1

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