

Srinivas Jasti

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Education

University of California Berkeley, Haas School of Business

Master of Business Administration, May 2016

- Relevant Courses: Leading People, Operations, Finance, Managerial & Financial Accounting, Marketing, Corporate Turnarounds, Data Science & Data Strategy and Strategic Brand Management

North Carolina State University

MS, with Thesis, Electrical and Computer Engineering (GPA: 4.0/4.0), June 2007

- Inducted into honors society – Phi Kappa Phi, for excellent academic performance and research contribution

Indian Institute of Technology, Madras (IIT)

Bachelor of Technology, Electrical Engineering, June 2005

- All India Rank – 340 out of 200,000 examinees; Awarded prestigious PRATIBHA scholarship by state Govt.

Experience

2011 - Present

Qualcomm, Inc.

San Jose, CA

Developed new generation Wireless LAN chipsets for mobile products targeting smart devices, automotive, and Internet of Things markets, led multi-cultural teams and delivered impactful and often out-of-the-box solutions to senior leadership to achieve faster mobile product launches and win key customer accounts

Project Lead/Technical Product Manager (2015 – Present)

- Managed concept-to-productization of WLAN IP that was delivered on time, without any post silicon issues; QCA Vive™ connectivity solution is used by multiple smartphone OEMs with expected revenues of \$150M
- Co-advised senior leadership on actionable strategies to implement integrated mobile architectures and improve IP convergence that are expected to result in 15% operational cost savings in connectivity engineering units
- Led technology selection and co-managed a team of 15 to deliver IP for mobile chipsets with 2x Wi-Fi performance and helped program management team to manage resource constraints to reduce time to market

Staff/Lead Design Engineer (2013 – 2015)

- Resolved critical issues, working with cross functional teams – implementation, marketing, quality control, technology, and software- that helped to reduce re-spin costs and meet key customer evaluation deadlines. Worked with legal and technology teams to save a \$100M customer account and averted a major product cancellation
- Mentored cross-cultural team of 5 to develop optimized derivative products for worldwide consumer electronics and enterprise segments. Launched multiple derivative chipsets in Asia within a year.
- Architected and implemented high performance, low power front-end modules in QCA Vive™ product line that interface with leading Qualcomm Snapdragon™ powered designs
- Won multiple Qualstar awards including “Super Qualstar” award, given to top 0.05% performers recognized by VPs, for exceptional contribution in WLAN IP development

Senior Design Engineer (2011 – 2013)

- Developed QCA’s 11ac high performance Wireless LAN chip-sets deployed by 29 different vendors, including Cisco, Linksys, Netgear, TP-Link, Belkin, working with cross-cultural teams in Taiwan, China and US
- Proposed and implemented a new integration flow for WLAN IP development and database management; Aligned with multi-geographical teams to streamline IP verification and reduce design hand-off time by 25%

2007 – 2011

Hughes Network Systems, LLC

Germantown, MD

Developed Wireless Handsets and High performance RF functional blocks for satellite base-stations, and managed pilot programs to implement cost effective solutions

Member Technical Staff III, ASIC Design (2010 – 2011)

- Innovated communication sub-systems, through extensive research, aimed at reducing chipset costs by 50%
- Implemented comprehensive test algorithms and reviewed process methodology of a multi-corporation collaboration project to identify a design flow lapse. Executed faster debug procedures which saved about \$200K in re-spin costs for a \$1M chipset and enabled to meet \$10M project delivery milestone on time

Member Technical Staff II, ASIC Design (2007 – 2010)

- Performed critical research, reviewed design documents to solve a manufacturability defect in a IC vendor’s product, which was delaying million dollar broadband product launch; aligned with several functional groups to identify design flaw and recommended viable solution that resulted in on time service deployment
- Streamlined Analog flow and negotiated contracts with EDA tool vendors to reduce operational costs by \$100K

Additional

- M Rezk, S Jasti, “Radar Detection on Non-Contiguous BW Modes of Operation”, U.S. Patent Application- 14/518,897, Oct 2014
- Published and presented papers in IEEE international research conferences
- Volunteer Tutor for Literacy Council of Montgomery County, MD
- Amateur Volleyball league player and captain of college volleyball team