

Q(t+1) = JQ' + JQ = J = DQED

Question 2:

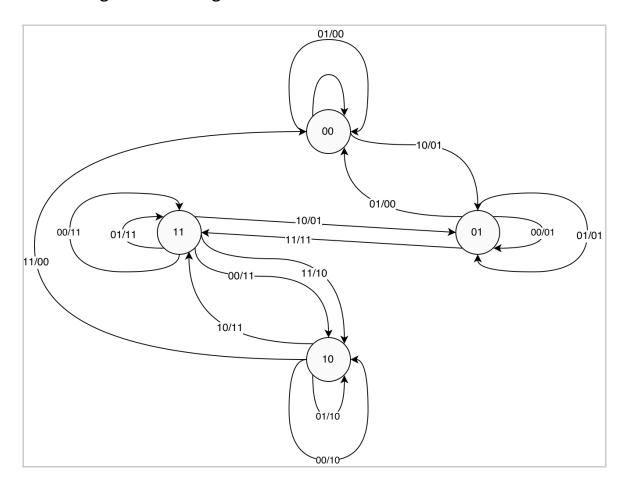
Design a sequential circuit with two JK flip flops and two inputs E and x. when E=0, the circuit holds its state. when E=1 and x=1 the circuit repeatedly counts through 0,2,3,1. when E=1 and x= 0, the circuit repeatedly counts through 0,1,3,2. Implement and test this circuit using LogicWorks.

Solutions:

Two inputs: E, x Two outputs: A, B

We only need two bits to represent the numbers so only two JK Flip-Flops will be used.

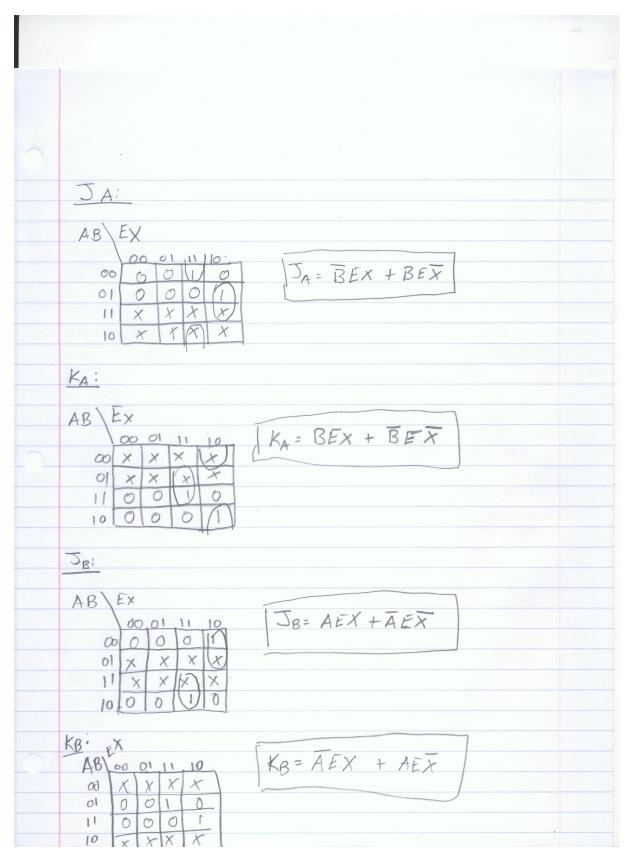
The following is a state diagram:



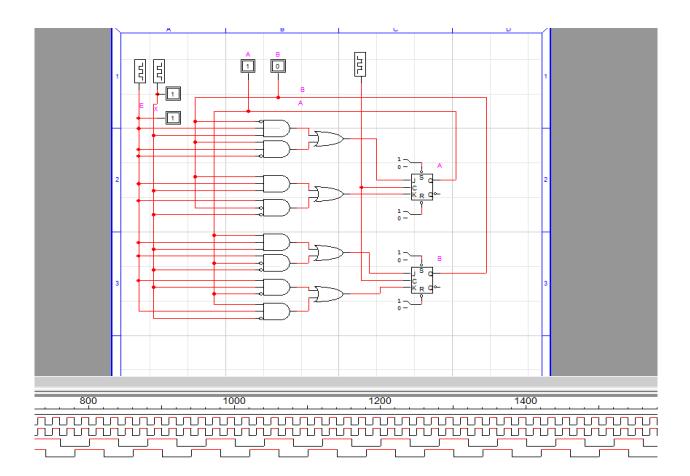
From this, We get our State Table:

Initial State Input			Next State/Outputs		JK Flip Flops				
A	В	E	X	A+	B+	JA	KA	JB	KB
0	0	0	0	0	0	0	X	0	X
0	0	0	1	0	0	0	X	0	х
0	0	1	0	0	1	0	x	1	х
0	0	1	1	1	0	1	X	0	X
0	1	0	0	0	1	0	X	X	0
0	1	0	1	0	1	0	X	X	0
0	1	1	1	1	1	1	X	X	0
0	11	1	0	0	0	0	X	X	1
1	0	0	1	1	0	X	0	0	Х
1	0	0	0	1	0	X	0	0	Х
1	0	1	1	0	0	X	1	0	х
1	0	1	0	1	1	X	0	1	х
1	1	0	1	1	1	X	0	X	0
1	1	0	0	1	1	X	0	X	0
1	1	1	1	1	0	X	0	X	1
1	1	1	0	0	1	x	1	X	0

Obtain the expressions:



And finally, the circuit:



Question 3:Design a sequential circuit to control a gumball machine. Gumballs are 15 cents each. The machine accepts nickels (N) or dimes (D). The machine should be able to indicate when a gumball should be dispensed (G) and whether or not change is given at that time (C). Follow the sequential design procedures set out in the course. Implement and test this circuit using LogicWorks. Specifically, show what happens when a user inserts 2 dimes, and what happens when a user inserts 3 nickels.

Solution:

One Input I.

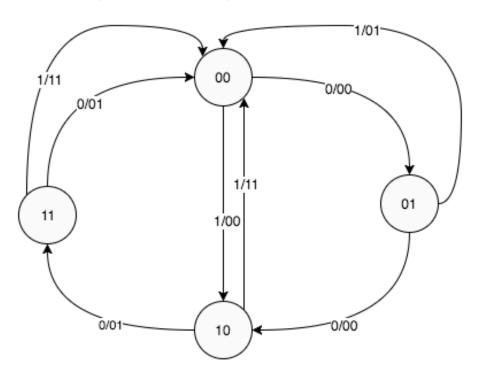
I is 0 if nickel is inserted and 1 of dime is inserted.

D = dime

N = nickel

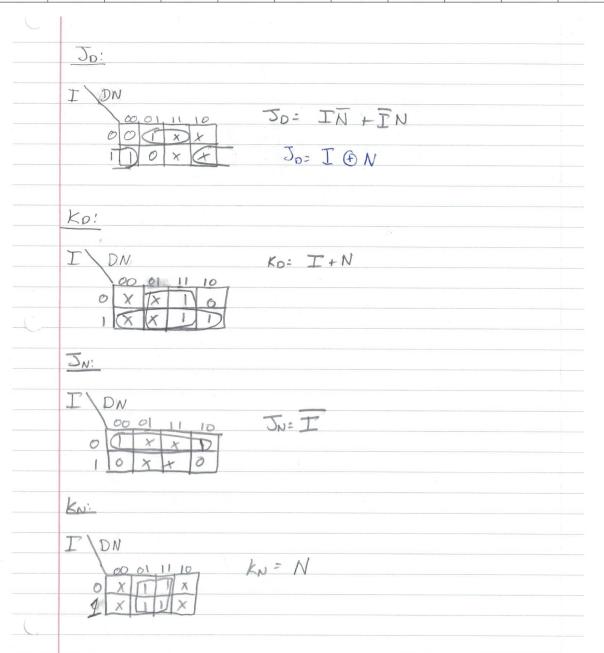
Two Outputs Gumball(G) and Change (C).

The following is the state diagram:



The following is the state table:

Input Current State		nt State	NextState		Outputs		JK Flip-Flops			
1	D	N	D ⁺	N*	С	G	J _D	K _D	J	K,
0	0	0	0	1	0	0	0	x	1	X
0	0	1	1	0	0	0	1	X	X	1
0	1	0	1	1	0	1	X	0	1	X
0	1	1	0	0	0	1	X	1	X	1
1	0	0	1	0	0	0	1	x	0	X
1	0	1	0	0	0	1	0	X	х	1
1	1	0	0	0	1	1	X	1	0	X
1	1	1	0	0	1	1	X	1	X	1



And finally, the circuit design:

