Control Settings

	PCMux.CTL	rsMux.CTL	rtMux.CTL	rdMux.CTL	regFile.WE	regInputMux.CTL	Arith.CTL	ArithMux.CTL	LOGIC.CTL	LogicMux.CTL	SHIFT.CTL	COSNT.CTL	CMP.CTL	ALUMux.CTL	NZP.WE	DATA.WE
BRxxx	0	x	x	x	0	x	x	x	x	x	x	x	x	x	0	0
ADD	1	0	0	0	1	0	0	0	x	x	x	x	x	0	1	0
MUL	1	0	0	0	1	0	1	0	x	x	x	x	x	0	1	0
SUB	1	0	0	0	1	0	2	0	x	x	x	x	x	0	1	0
DIV	1	0	0	0	1	0	3	0	x	x	x	x	x	0	1	0
ADD (immediate)	1	0	x	0	1	0	0	1	x	x	x	x	x	0	1	0
CMP	1	2	0	x	0	0	x	x	x	x	x	x	0	4	1	0
CMPU	1	2	0	x	0	0	x	x	x	x	x	x	1	4	1	0
CMPI	1	2	x	x	0	0	x	x	x	x	x	x	2	4	1	0
CMPIU	1	2	x	x	0	0	x	x	x	x	x	x	3	4	1	0
JSR	5	x	x	1	1	2	x	x	x	x	x	x	x	x	1	0
JSRR	3	0	x	1	1	2	x	x	x	x	x	x	x	x	1	0
AND	1	0	0	0	1	0	x	x	0	0	x	x	x	1	1	0
NOT	1	0	x	0	1	0	x	x	1	x	x	x	x	1	1	0
OR	1	0	0	0	1	0	x	x	2	0	x	x	x	1	1	0
XOR	1	0	0	0	1	0	x	x	3	0	x	x	x	1	1	0
AND (immediate)	1	0	x	0	1	0	x	x	0	1	x	x	x	1	1	0
LDR	1	0	x	0	1	1	0	2	x	x	x	x	x	0	1	0
STR	1	0	1	x	0	x	0	2	x	x	x	x	x	0	0	1
RTI	3	1	x	x	0	x	x	x	x	x	x	x	x	x	1	0
CONST	1	x	x	0	1	0	x	x	x	x	x	0	x	3	1	0

SLL	1	0	x	0	1	0	x	x	x	x	0	x	x	2	1	0
SRA	1	0	x	0	1	0	x	x	x	x	1	x	x	2	1	0
SRL	1	0	x	0	1	0	x	x	x	x	2	x	x	2	1	0
MOD	1	0	0	0	1	0	4	0	x	x	x	x	x	0	1	0
JMPR	3	0	x	x	0	x	x	x	x	x	x	x	x	x	0	0
JMP	2	x	x	x	0	x	x	x	x	x	x	x	x	x	0	0
HICONST	1	2	x	0	1	0	x	x	x	x	x	1	x	3	1	0
TRAP	4	x	x	1	1	2	x	x	x	x	x	x	x	x	1	0